

A Real-time Dynamic Vision Sensor Emulator using Off-the-shelf Hardware

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Abstract

Vision is one of our most important senses, a vast amount of information is perceived through our eyes. Neuroscientists have performed several studies using vision as input to their experiments. However, computational neuroscience has typically used Poisson-encoded images as spike-based visual sources. Recently neuromorphic Dynamic Vision Sensors have surfaced, while they have excellent capabilities, they remain scarce and difficult to use.

We propose a visual input system inspired by the behaviour of a DVS, but using a digital camera as a sensor. By using readily-available components, we believe, most scientist would have access to a spiking visual input source. While the primary goal was to use the system as a real-time input, it is also able to transcode well established images and video databases into spike train representations. Our main contributions are adding locally inhibitory behaviour, adaptive thresholds and proposing time-based encoding of the output spikes.

1 Introduction

In recent years the performance of computer processors has been advancing in smaller increments than it used to a few years ago. This is mainly because manufacturing technologies are reaching their limits. One way to improve performance is to use many processors in parallel, which has been successfully applied to parallel-friendly applications like computer graphics. Task like pattern recognition are still a hard task for computers even with these technological advances.

Our brains are particularly good at learning and recognizing visual patterns (e.g. letters, dogs, houses, etc.). In order to achieve better performance for similar tasks on computers, scientists have looked into biology for inspiration. This has lead to the rise of brain-like (neuromorphic) hardware, which looks to mimic functional aspects of the nervous system. We can divide neuromorphic hardware into sensors (providing input) and computing devices (make use of information from sensors). Visual input has been traditionally obtained from images that are rate-encoded using Poisson processes, while this might be a biologically-plausible encoding in the first phase of a “visual pipeline” it is unlikely that eyes transmit as much information into later stages. Furthermore, if we think of it in terms of digital networks, having each pixel represented by a Poisson process incurs in high bandwidth requirements.

In 1998, Mead proposed a silicon retina consisting of individual photoreceptors and a resistor mesh that allowed nearby receptors to influence on the output of a pixel [1]. Later, researchers developed frame-free Dynamic Vision Sensors (DVSs) [2], [3]. They feature independent pixels that emit a signal when its intensity value changes above a certain threshold. These sensors have μ -second response time and excellent dynamic range properties, although they are still not as commercially available as regular cameras.

In this work, we propose to emulate the behaviour of a DVS using a conventional digital camera as a sensor. Basing the emulator on widely available hardware, would allow most computational neuroscientists to include video as a spike-based input.

Katz, Nikolic, and Delbruck developed a DVS emu-

lator in order to test behaviours for new sensor models [4]. In their work, they transform the image provided a commercial camera into a spike stream at 125 frames per second (fps). In simple terms, the emulation is done by differencing video input with a reference frame; if this difference is larger than a threshold it produces an output and updates the reference. The number of spikes produced per pixel are proportional to how many times the difference would pass the threshold. This emulator has been merged into the jAER project, a Java-based Address-Event Representation software framework that specializes on processing DVS output in real time.

2 Work

Pixels in DVSs are independent and transform light input into a logarithmic representation. Most commercial cameras produce gamma-encoded images [5] to better utilize bits and, in older days, to be compliant with cathode ray tube (CRT) monitors. Figure 2 shows the response for the encoding process (crosses), CRT monitors (hexagons) and decoding process (dots). Since the encoding response is similar to the logarithmic used in the DVS, we skip this step of the emulation.

Asynchronous pixel behaviour is approximated via differencing the current image obtained by the camera and a reference frame; whenever a pixel's difference is larger than a certain threshold, we mark that position as "spiked". Depending on the selected type of output we simulate a receiver and update the reference accordingly. This is the basic DVS emulation and it's illustrated in Figure 2.

2.1 Output modes

2.1.1 Rate-based

As in previous emulators[4], the standard output format is rate-based. In this mode each spike can be interpreted as an increment or decrement in intensity. To calculate the number of spikes that represents this

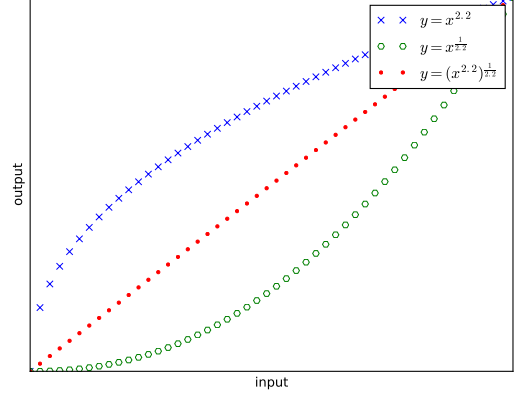


Figure 1: Gamma encoding and decoding functions, $\gamma = 2.2$

change in intensity we use the following expression

$$N_H = \left\lfloor \min \left(T, \frac{\Delta I}{H} \right) \right\rfloor \quad (1)$$

where N_H is the number of spikes needed to represent the change in intensity ΔI in terms of the threshold H . Notice that the maximum time to transmit all the spikes for one frame is bound by the frame rate of the camera (fps). If it's only possible to send one spike per millisecond, we can send as many spikes as milliseconds in one frame-capture period ($T = 1000/fps$). At this stage we model a perfect receiver, so the quantity that will update the reference is

$$\Delta R = N_s \times H \quad (2)$$

2.1.2 Time-based

In its worst case rate-based encoding can send a spike per millisecond per pixel, which can potentially saturate communication channels. One way to prevent this is to encode the value that each spike represents in the time each it is sent.

First we propose to linearly encode the number of thresholds exceeded. To do this the result of Equation 1 would in, a bin width (W_b) of 1 ms was used

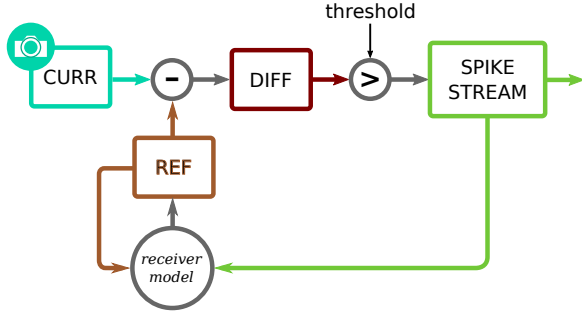


Figure 2: DVS emulation diagram. Circles indicate operations and rectangles states of data

for time discretization. As in the previous case the time to send all spikes for the current frame is at most T , which means the maximum difference possible is $T \cdot H$.

Figure 2.1.2 shows the value-to-spike-time relation, in our proposal earlier spikes represent larger changes in intensity. The main advantage of this encoding is that a single spike could represent multiple rate-based spikes, though the encoded values are limited by time resolution and the frame rate of the camera.

To decode on the receiver end, we must keep track of the time and value from the last collected spiked. Let Δt be the difference in arrival time between the previous and current spike (Eq. 3)

$$\Delta t = t_{now} - (t_{last} - N_H^{last} W_b) \quad (3)$$

where the subtraction of $N_H^{last} W_b$ resets the time reference to a multiple of time period T . From this it is easy to compute the number of thresholds exceeded using the expression

$$N_H^{now} = T - \text{mod}(\Delta t, T) \quad (4)$$

where ΔI_o is the change in intensity derived from the spike stream, t_{now} is the time when the current spike arrived, t_{last} is the time last spike was received, T is the inverse of the camera's frames per second, and mod calculates the arguments' division remainder.

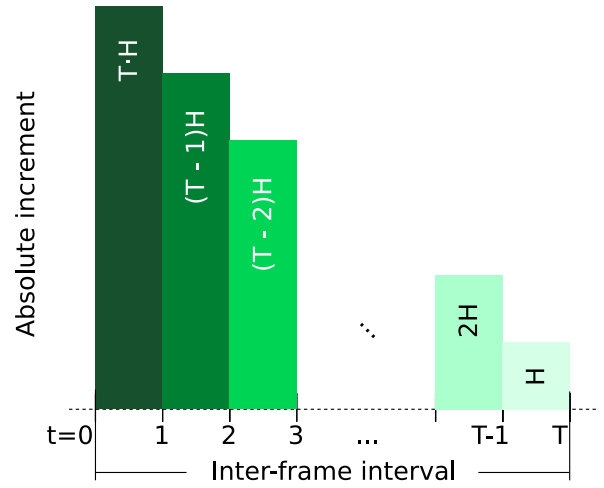


Figure 3: Linear time-based encoding of thresholds (H) exceeded

To overcome the limitation of encoded values we propose binary encoding of either the absolute difference in intensity or the number of thresholds exceeded. We propose different time encodings (raw value/binary, threshold multiples/binary, threshold multiples/linear)

2.2 Additional behaviours

2.2.1 Local inhibition

reduces bandwidth, persistence of spatial stimuli over frames.

2.2.2 Adaptive threshold

slow-charging pixels by reducing the threshold. Less parameters to configure.

2.2.3 History decay

Open source, and available in github.com

3 Results

Real-time DVS emulation on consumer hardware (Intel i5, 8GB ram) sPyNNaker wrapper for videos and images.

4 Conclusion

GPU computing version would allow for higher resolutions.

Working on ganglion cell/Difference of Gaussian encoding.

References

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