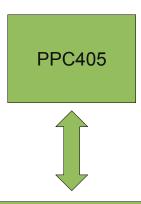
WPTR[9:0] FIR_WE **OCNT** BLOCK RAM 0 WFIFO **IREG CNTRL** ICNT BLOCK RAM 1 WFIFO_WE SYNC BLOCK RAM 2 WFIFO DATA[15:0] BLOCK RAM 3 CH1_R[15:0] CH1_I[15:0] CH2_R[15:0] BLOCK RAM 4 QM_FIR CH2_I[15:0] CH3_R[15:0] CH3_I[15:0] **BLOCK RAM 5**



PLB



SLAVE IPIF (Xilinx)

IREG

WFIFO CNTRL

QM_FIR

BLOCK RAM 0

BLOCK RAM 1

BLOCK RAM 2

BLOCK RAM 3

BLOCK RAM 4

BLOCK RAM 5