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### Operational Amplifiers/Buffers

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Note. For additional information on operational amplifiers, see National Semiconductor's Special Functions Databook.

## BI-FETTM/BI-FET IITM Op Amp Selection Guide

	DC	ELECTRICAL CHARACTERIS	STICS		AC ELECTRIC	AL CHARACTERISTICS
PART NUMBER	V <sub>OS</sub> – MAX OFFSET VOLTAGE (mV) (T <sub>A</sub> = 25°C)	$\Delta V_{OS}/\Delta T$ – T.C. OF $V_{OS}$ ( $\mu V/^{\circ}C$ )	I <sub>B</sub> — MAX BIAS CURRENT (pA) (T <sub>J</sub> = 25°C)	A <sub>VOL</sub> LARGE SIGNAL VOLTAGE GAIN (V/mV) MIN (T <sub>A</sub> = 25°C)	SR – SLEW RATE (V/μs)	e <sub>n</sub> — EQUIV. INPUT NOISE VOLTAGE (nV/√Hz (Note 2)
MILITARY BI-FET	OP AMP (Note 1)					
LF155	5	5	100	50	5	20
LF155A	2	5 (max)	50	50	5	20
LF156	5 .	5	100	50	12	12
LF156A	2	5 (max)	50	50	12	12
LF157	5	5	100	50	50	12
LF157A	2	5 (max)	50	50	50	12
INDUSTRIAL BI-F	ET OP AMP (Note 1)					
LF255	· 5	5	100	50	5	20
LF256	5	5	100	50	12	12
LF257	5	5	100	, 50	50	12
COMMERCIAL BI-I	FET AND BI-FET II OP AMP (	Note 3)				
LF351	10	10	200	25	13	16
LF351A	2	10	100	25	13	16
LF351B	5	10	200	25	13	16
LF355	10	5	200	25	5	25
LF355A	2	5 (max)	50	25	5	25
LF356	10	5	200	25	12	15
LF356A	2	5 (max)	50	25	12	15
LF357	10	. 5	200	25	50	15
LF357A	2	5 (max)	50	25	50	15 -
LF13741	15	10	200	25	0.5	37
BI-FET II DUAL OF	P AMPS (Characteristics for Eac	ch Amplifier) (Note 3)				
LF353	10 .	10	200	25	13	16
LF353A	2	10	100	25	. 13	16
LF353B	5	10	200	25	13	16
BI-FET II QUAD OF	P AMPS (Characteristics for Ea	ch Amplifier) (Note 3)				
LF347	10	10	200	25	13	16
LF347A	2	10	100	25	13	16
LF347B	5	10 -	200	25	13	16

## BI-FETTMBI-FET IITM Op Amp Selection Guide

	SEL	ECTION BY	DESIGN PARA	METER			ADDITIONAL NS PRODUCTS USING BI-FET TECHNOLOGY
Max Input Offset Voltage (Тд = 25°С)	2 mV LF155A/LF355A LF156A/LF356A LF357A LF351A LF353A LF347A	LF351B LF347B LF353B LF155/L	5 mV .F156/LF157 .F256/LF257		<u>10 mV</u> LF356/LF357	<u>15 mV</u> LF13741	<ul><li>LF111 Comparator</li><li>LF198 Sample and Hold</li></ul>
Max Input Bias Current (T <sub>J</sub> = 25°C)	<u><b>50 pA</b></u> LF155A/LF156A/ LF355A/LF356A/		100 p LF155/LF15 LF255/LF25 LF351A LF353A LF347A	6/LF157	200 pA LF355/LF356/L LF351/LF351B LF347/LF347B LF353/LF353B LF13741	F357	<ul> <li>LF11201 Series of Analog Switches</li> <li>LF11331 Series of Analog Switches</li> <li>LF11508 Series of Analog Multiplexers</li> <li>LF152 Instrumentation Amplifier</li> <li>LF13300 Integrating A/D Building Block</li> </ul>
Typ Equivalent Input Noise Voltage per $\sqrt{\text{Hz}}$ , f = 1000 Hz, R <sub>S</sub> = 100 $\Omega$	12 nV or Less LF156/LF156A LF157/LF157A LF256/LF257	1 LF356 LF356A LF357 LF357A	LF351A LF351B	LF155 LF155A LFT155 LF255	<u>25 nV To 37 nV</u> LF355 LF13741 LF355A		
Typ Slew Rate	LF2	5 V/μs  55/LF155A  255  855/LF355A	LF353B  12 V/μs LF156 LF156A LF256 LF356	13 V/μs LF351 LF351A LF351B LF353	50 V/μs LF157 LF157A LF357 LF357A		
			LF356A	LF353A LF353B LF347 LF347A LF347B			

## Military Op Amp Selection Guide

Device	Input Offset Voltage Max (mV)	Input Offset Voltage Drift Max (µV/°C)	Input Offset Current Max (nA)	Input Bias Current Max (nA)	Voltage Gain Min (Volts/V)	Bandwidth A <sub>V</sub> = 1 Typ (MHz)	Slew Rate A <sub>V</sub> = 1 Typ (V/μs)	NGE: -55°C < Output Current Min @ R <sub>L</sub> = 2k (mA)		Voltage Max (V)	Common Mode Range (V)	Differential Input Voltage (V)	Supply Current T <sub>A</sub> = 25°C Max (mA)	Compensation Components Per Amplifier	Package Type
SINGLE OP AMPS															
LH101	6	6 typ	500	1500	25k	1	0.5	5	±3	±22	±12	±30	3	0	TO-5 F.P.
LM101A	3	15	20	100	25k	1 .	0.5	5	±3	±22	±12	±30	3	1	TO-5 DIP F.P
LM101	6	6 typ	500	1500	25k	1	0.5	5	±3	±22	±12	±30	3	1	TO-5 F.P.
LM102	7.5	6 typ	*	100	0.999	10 *	10	$(R_L = 8 k\Omega)$	±12	±18	±10	•	5.5	0 '	TO-5
LM107	3	15	20	100	25k	1	0.5	7.5	±3	±22	±12	±30	3	0	TO-5 DIP F.P
LM108A	1	5	0.4	3	40k	1	0.3	1	±2	±20	±14	(Note 1)	0.6	1	TO-5 DIP F.P
LM108	3	15	0.4	3	25k	1	0.3	1	±2	±20	±14	(Note 1)	0.6	1	TO-5 DIP F.P
LM110	6	12	*	10	0.999	20	30	$(R_L = 8 k\Omega)$	±5	±18	±10	*	5.5	0	TO-5 DIP
LM112	3	15	0.4	3	25k	1	0.2	1.3 (R <sub>L</sub> = 10 kΩ)	±2	±20	±14	(Note 1)	0.6	0	TO-5 DIP F.P
LM118	4	*	50	250	20k	15	50 min	6	±5	±18	±11.5	(Note 1)	8	0	TO-5 DIP F.P
LM121A (R <sub>SET</sub> =70k)	0.65	0.2	1	30	16k	0.5	*	*	±5	±20	±15	±15	1.5	1	TO-5 DIP F.P
LM121 (R <sub>SET</sub> =70k)	1	1	3 .	30	16k	0.5	*	*	±5	±20	±15	±15	1.5	1 '	TO-5 DIP F.P.
LM143	6	*	7	35	50k	1	2.5	$(R_L \ge 5k)$	±4	±40	±38	±40	4	0	TO-5 DIP F.P
LM144	6	*	7	35	50k	2	$(A_V > 10)$	$(R_L \ge 5k)$	±4	±40	±38	±40	4	1	TO-5 DIP F.P
LF155A	2.5	10	25	0.05	25k	2.5	5	5	±5	±22	±20	±40	4	0	TO-5
LF155	7	20 .	50	0.1	25k	2.5	5	5	±5	±22	±20	±40	4 .	0	TO-5
LF156A	2.5	10	25	0.05	25k	5	15	5	±5	±22	±20	±40	7	0	TO-5
LF156	7	20	50	0.1	25k	5	15	5	±5	±22	±20	±40	7	0	TO-5
LF157A (A <sub>V</sub> ≥5)	2.5	10	25	0.05	25k	25	75	5	±5	±22	±20	±40	7	0	TO-5
LF157 (A <sub>V</sub> ≥5)	7	20	50	0.1	25k	25	75	5	±5	±22	±20	±40	7	0	TO-5
LM709A	3	15	250	600	25k \	1	0.3	5	±5	±22	±20	±40	3.6	3	TO-5
LM709	6	6 typ	500	1500	25k	1	0.3	5	±9	±18	±8	±5	5.5	3	TO-5 DIP
LM725A	0.7	2	18	180	1000	0.5	0.005	5	±3	±22	±13.5	±5	3.5	4	TO-5 DIP
LM725	1.5	5	40	200	1000	0.5	0.005	5	±3	±22	±13.5	±5	3.5	4	TO-5 F.P.
LM741A .	4	15	70	210	32k	1	0.5	7.5	±3	±22	±12	. ±30	4.0	0	T-O-5 DIP F.P
LM741	6	15 typ	500	1500	25k	1	0.5	5	±3	±22	±12	±30	2.8	0	TO-5 DIP F.P
LM748	6	*	500	1500	25k	1	0.5	5	±3	±22	±12	±30	2.8	1	TO-5
LM4250 ( $V_S = \pm 15V$ )	4	*	3	7.5	50k	0.1	0.03	$0.12$ (R <sub>L</sub> $\geq 100$ k)	±1	±18	±12	±15	0.011 set	0	TO-5 DIP

Note 1: Inputs have shunt-diode protection; current must be limited. \*Not specified

					MILITAR	Y TEMPER	ATURE RAI	NGE: -55°C	≤ T <sub>A</sub> ≤ +125°C		
Device	Input Offset Voltage	Input Offset Voltage Drift	Input Offset Current	Input Bias Current	Voltage Gain Min	Bandwidth A <sub>V</sub> = 1 Typ	Slew Rate A <sub>V</sub> = 1 Typ	Output Current Min	Supply Voltage Min Max	Common Mode Range	Differential Input Voltage

Device	Input Offset Voltage Max (mV)	Input Offset Voltage Drift Max (μV/°C)	Input Offset Current Max (nA)	Input Bias Current Max (nA)	Voltage Gain Min (Volts/V)	Bandwidth A <sub>V</sub> = 1 Typ (MHz)	Slew Rate A <sub>V</sub> = 1 Typ (V/μs)	Output Current Min @ R <sub>L</sub> = 2k (mA)	Supply ' Min (V)	Voltage Max (V)	Common Mode Range (V)	Differential Input Voltage (V)	Supply Current T <sub>A</sub> = 25°C Max (mA) (Note 2)	Compensation Components Per Amplifier	Package Types
DUAL OF AMPS															
LM158	5	. *	30	150	25k	1	*	0.8	±1.5	±16	V <sup>+</sup> −1.5	. V+	1.2	0	TO-5 DIP
LM1558	6	. *	500	1500	25k	1	0.5	5	±3	±22	±12	±30	5.0	0	TO-5
LM747A	4	15	70	210	32k	1	0.5	7.5	±3	±22	±12	±30	5.6	0	,DIP
LM747	6	*	500	1500	25k	1	0.5	5	±3	±22	±12	±30	5.6	0	DIP
QUAD OP AMPS															
LM124	7	7 typ	±30	150	50	1.0	*	10	-16	+16	0 to V <sup>+</sup> -1.5V	∨ <sub>p</sub> c	.3	0	D, F, J
LM146 (I <sub>SET</sub> = 10 μA	A) 5	5 typ	20	100	100k	1.2	0.4	1.2	±2	±22	±0.7	±30	2	0	DIP
LM148	6	15 typ	75	325 -	25k	1	0.6	5	±3	±22	±12	±30	3.6	0	DIP F.P.
LM149 (A <sub>V</sub> ≥ 5)	6	15 typ	75	325	25k	4	3	5	±3	±22	±12	±30	3.6	0	DIP F.P.
LM1900	*	*	*	150	0.8k	2.5	*	10 source 1 sink	±4	±36	*	*	12 ·	. 0	DIP

Note 2: Supply current for all channels of amplifier in the package.



## Industrial Op Amp Selection Guide

Not specified

Note 1: Inputs have shunt-diode protection; current must be limited.

Note 2: Supply current for all channels of amplifier in the package.

-	Device	Input Offset Voltage Max (mV)	Input Offset Voltage Drift Max (μV/°C)	Input Offset Current Max (nA)	Input Bias Current Max (nA)	Voltage Gain Min (Volts/V)	Bandwidth A <sub>V</sub> = 1 Typ (MHz)	Slew Rate A <sub>V</sub> = 1 Typ (V/μs)	Output Current Min @ R <sub>L</sub> = 2 kΩ (mA)	Supply \ Min (V)	Voltage <sup>(</sup> Max (V)	Common Mode Range (V)	Differential Input Voltage (V)	Supply Current T <sub>A</sub> = 25°C Max (mA) (Note 2)	Compensation Components Per Amplifier	Package Types
	SINGLE OP AMPS															
	LM201A	3	15	20	100	25k	1	0.5	5	±3	±22	±12	±30	3	1	TO-5 DIP F.P.
	LM202	10	15 typ	*	15	0.999	10	10	1	±12	±18	±10	*	5.5	0	TO-5
	LM207	2	20	20	100	25k	1	0.5	5	±3	±22	±12	±30	3	0	TO-5 DIP F.P.
	LM208A	1.0	5	0.4	3	40k	1	0.3	1	±2	±20	±14	(Note 1)	0.6	1	TO-5 DIP F.P.
	LM208	3	15	0.4	3	25k	1	0.3	1	±2	±20	±14	(Note 1)	0.6	1	TO-5 DIP F.P.
	LM210	4	*	*	3	0.999	20	30	1	±5	±18	±10	*	5.5	0	TO-5 DIP F.P.
	LM212	2	15	0.2	2	25k	1	0.3	1	±2	±20	±14	(Note 1)	0.6	0	TO-5 DIP F.P.
	LM216A	3	*	0.015	0.05	20k	1	0.3	1	±5	±20	±13	(Note 1)	0.6	0	TO-5 DIP F.P.
	LM216	10	*	0.05	0.15	10k	1	0.3	1	±5	±20	±13	(Note 1)	0.8	0	TO-5 DIP F.P.
	LM218	4	*	50	500	25k	15	50 min	5	±5	±18	±11.5	(Note 1)	8	0	TO-5 DIP F.P.
	LM221A (R <sub>SET</sub> = 70k)	0.65	0.2	1	30	16k	0.5	*	*	±5	±20	±15	±15	1.5	1	TO-5 DIP F.P.
	LM221 (R <sub>SET</sub> = 70k)	1	1	3	30	16k	0.5	*	*.	±5	±20	±15	±15	1.5	. 1	TO-5 DIP F.P.
	LF255	6.5	5 typ	20	50	25k	2.5	5	5	±5.	±22	±20	±40	4	0	TO-5
	LF256	6.5	5 typ	20	50	25k	5	15	5	±5	±22	±20	±40	7	0	TO-5
	LF257 (A $_{ m V}$ $\geq$ 5) DUAL OP AMPS	6.5	5 typ	20	50	25k	25	75	` 5	±5	±22	±20	±40	7	0	TO-5
	LM258	7.5	7 typ	150	500	15k	1 .	0.5	10-source 5-sink	3 (±1.5)	32 (±16)	V <sup>+</sup> −1.5	32	1.2	0	TO-5 DIP
	QUAD OP AMPS															
	LM224	9	7 typ	150	500	15k	1	*	10	3	32	V <sup>+</sup> ~1.5	32	2	0	DIP F.P.
	LM246	6	7 typ	100	250	50k	0.5	0.4	1.2	±2	±18	±1.5	±30	2.5	0	DIP
	LM248	7.5	15 typ	125	500	15k	1	0.5	5	±5	±18	±18	±36	4.5	0	DIP
	LM249	7.5	15 typ	125	500	15k	4	2	5	±5	±18	±18	±36	4.5	0	DIP
	LM2900	*	.*	*	200	1.2k	2.5	*	3–source 0.5–sink	+4	+36	*	*	10	0	DIP
	LM2902	10 (T <sub>A</sub> =25°)	* (	±50 (T <sub>A</sub> =25°C)	500 (T <sub>A</sub> =25°C)	100k ) typ	1	*	20-source 8-sink	3.0 single ±1.5 dual	26 single ±13 dual	-0.3V <sub>DC</sub> to +26 V <sub>DC</sub>	26 V <sub>DC</sub>	2		DIP

INDUSTRIAL TEMPERATURE RANGE:  $-25^{\circ}C \le T_{A} \le +85^{\circ}C$ 

### **Commercial Op Amp Selection Guide**

Device	Input Offset Voltage Max (mV)	Input Offset Voltage Drift Max (μV/°C)	Input Offset Current Max (nA)	Input Bias Current Max (nA)	Voltage Gain Min (Volts/V)	Bandwidth Ay = 1 Typ (MHz)	Slew Rate A <sub>V</sub> = 1 Typ (V/µs)	Output Voltage Swing R <sub>L</sub> = 10 kΩ (V)	Vo Min	pply Itage Max (V)	Common Mode Rejection Ratio (dB) Min	Differential Input Voltage (V)	Supply Current TA = 25°C Max (mA) (Note 2)	Compensation Components	Package Types
SINGLE OP A	AMPS														
LM201	10	10 typ	750	200	15k	1	0.5	5	±3	±22	±12	±30	3	1	TO-5 F. P.
LM301A	10	. 30	70	300	15k	1	0.5	5	±3	±18	±12	±30	3	1	TO-5 DIP
LM302	20	20 typ	*	30	0.9985	10	10	1	±12	±18	±10	*	5.5	0	TO-5
LM307	10	30	50 ·	250	15k	1 .	0.5	5	±3	±18	±12	±30	3	0 .	TO-5 DIP F. P.
LM308A	0.73	5	1.5	10	60k	1	0.3	1 ~	±2	±20	±14	(Note 1)	0.8	1	TO-5 DIP F.P.
LM308	10	30	1.5	10	15k	1	0.3	1	±2.	±18	±14	(Note 1)	0.8	1	TO-5 DIP F.P.
LM310	· 10	10 typ	*	10	0.999	20	30	1 '	±5	±18	±10	*,	5.5	0	TO-5 DIP F.P.
LM312	10	30	1.5	10	15k	1	0.3	1	±2	±18	±14	(Note 1)	0.8	0	TO-5 DIP F.P.
LM316A	6	*	0.03	0.1	30k	1	0.3	1	±5	±20	±13	(Note 1)	0.6	0 -	TO-5 DIP F.P.
LM316	15	*	0.1	0.25	15k	1	0.3	1	±5	±20	±13	(Note 1)	0.8	0	TO-5 DIP F.P.
LM318	15	*	300	750	20k	15	50	5	±5	±18	±11.5	(Note 1)	10	0	TO-5 DIP
LM321A	0.65	0.2	1	25	12k	0.5	*	*	±5 ^	±20	±15	±15	2.2	1	TO-5 DIP F.P.
(RSET = 70k)			•		,								•		
LM321	2.5	· 1	4	28	12k	0.5	* .	*	±5	±20	±15	±15	2.2	1	TO-5 DIP F.P.
(R <sub>SET</sub> = 70k)															
LM343	. 10	*	14	55	50k .	1	2.5	4	±4	±34	±34	±34	5.0	0	TO-5 DIP F.P.
								R <sub>L</sub> ≥ 5k)		•					
LM344	10	*	14	55	50k	2 -	, 30	4	±4	±34	±34	±34	5.0	1	TO-5 DIP F.P.
		•			-	-		R L ≥ 5k)		_0.	-0.	-0.	0.0		
LF351	10	10 typ	0.1	0.2	25k	4	13	±12	-18	18	70	±30	3.4	0	H, N
LF351A	2	10 typ	0.05	0.2	50k	4	13	±12	-18	18	80 .	±30	2.8	0	H, N
LF351B	5	10 typ	0.1	0.1	50k	4	13	±12	-18	18	80	±30	2.8	0	H, N
LF355A	2.3	5	1	5	25k	2.5	5	5	±5	±22	±20	±40	4	0	TO-5, Mini-DIP
LF355	13	5 typ	2	8	15k	2.5	5	5	±5	±18	±16	±30	4		TO-5, Mini DIP
LF356A	2.3	5	1	5	25k	5	15	5	±5	±22	±20	±40	10		TO-5, Mini-DIP
LF356	13	5 typ	2	8	15k	5	15	5	±5	±18	±16	±30	10	. 0	TO-5, Mini-DIP
LF357A	2.3	5	1	5	25k	25	75	5	±5	±22	±20	±40	10	0	TO-5, Mini-DIP
(A <sub>V</sub> ≥ 5)			•	-	2011		,,	ŭ	_0		-20	-40		Ü	
LF357	13	5 typ	2	8	15k	25	75	5	±5	±18	±16	±30	10	0	TO-5, Mini DIP
(A <sub>V</sub> ≥ 5)		O 1, p	-	-	101			ŭ	_5	- 10	-10	-50		,•	10-0, mini Dir
LF13741	20	10 typ	2	8	15k	1	0.5	5	±4	±18	±16	±30	4	0	TO-5, Mini-DIP



### Commercial Op Amp Selection Guide

Device	Input Offset Voltage Max (mV)	Input . Offset Voltage Drift Max (μV/°C)	Input Offset Current Max (nA)	Input Bias Current Max (nA)	Voltage Gain Min (Volts/V)	Bandwidth AV = 1 Typ (MHz)	Slew Rate AV = 1 Typ (V/µs)	Output Voltage Swing R <sub>L</sub> = 10 kΩ (V)	Vo	oply Itage Max (V)	Common Mode Rejection Ratio (dB) Min	Differential Input Voltage (V)	Supply Current TA = 25°C Max (mA) (Note 2)	Compensation Components	Package Types
SINGLE OP	AMPS (Co	ntinued)						* ,						***************************************	
LM709C	10	12 typ	500	1500	15k	1	0.3	5	±9	±18	± <b>8</b> .	±5	6.6	3	TO-5 DIP
LM725C	3.5	2 typ	50	250	125k	0.5	0.005	5	±3	±22	±13.5	±5	5	4	TO-5 DIP
LM741C	7.5	15 typ	300	800	15k	1	0.5	5	±3	±18	±12	±30	2.8	0	TO-5 DIP F.P.
LM741E	4	15	70	210	32k	1	0.5	7.5	±3	±18	±12	±30	3.75	0	TO-5 DIP F.P.
LM748C	6	6	0.5	1.5	25k	1	0.5	5	±3	±18	±12	±30	2.8	1	TO-5 DIP
LM4250C	6	*	8	10	50k	0.1	0.03	0.12	±1	±18	±12	±15	0.011	0	TO-5 DIP
							$(A_V > 10)$	(RL≥100k)					(Set)		
DUAL OP AI	MPS												•		
LF353	10	10 typ	0.1	0.2	25k ~	4	13	±12	-18	18	70	±30	3.4	0	N, H
LF353A	2	10 typ	0.05	0.2	50k	4	13	±12.	-18	18	80	±30	2.8	0	N, H
LF353B	5	10 typ	0.1	0.1	50k	4	13	±12	-18	18	80	±30	2.8	0	NH
LM358	7.5	7 typ	150	500	15k	1	*	8	±1.5	±15	V <sup>+</sup> -1.5	V <sup>+</sup>	1.2	0	TO-5 DIP
LM1458	6	*	300	800	15k	1	0.2	5	±3	±18	±15	±30	5.6	0	TO-5 DIP
LM747C	6	*	300	800	15k	1	0.5	5	±3	±18	±12	±30	5.6	0	DIP
LM747E	4	. 15	70	210	32k	1	0.5	7.5	±3	±18	±12	±30	5.6	0	DIP
QUAD OP A	MPS														
LF347	10	10 typ	0.01	0.2	25k	4	13	±12	-18	18	70	±30	3.4	0	N, J
LF347A	2	10 typ	0.05	0.2	50k	4	13	±12	-18	18	80	±30	2.8	0	N, J
LF347B	5	10 typ	0.1	0.1	50k	4 .	13	±12	-18	18	80	±30	2.8	0	N, J
LM324	9	7 typ	150	500	15k	1	*	10-source	3	32	V <sup>+</sup> -1.5	32	2	0	DIP F. P.
								5-sink	(±1.5)	(±16)					e
LM346	5	10 typ	100	250	100k	8.0	0.4	±12	-18	18	70	±30	0.62	0	N, J
LM348	7.5	15 typ	100	400	15k	1	*	5	±5	±18	±18	±36	4.5	0	DIP F. P.
LM349	7.5	15 typ	100	400	15k	4	3	5	±5	±18	±18	±36	4.5	0	DIP F. P.
$(A_V \ge 5)$														-	
LM3900	*	*	*	200	2.8k	2.5	20	10	4	36	. *	*	10	0	DIP
									(±2)	(±18)					

COMMERCIAL TEMPERATURE RANGE  $0^{\circ}C \le T_{\mbox{\scriptsize A}} \le +70^{\circ}C$