Format of Lab Exam **IT110**
40 Marks - 2 hrs

1.

   a. **Minimization Using K-Map**   (7 marks)
- Writing Expression minimization each step using K map (5 marks)
- Circuit Verse simulation (2 marks)

   b. **Flip Flop Conversion**   (6 marks)
- Write up as written in your assignment
- Each truth table , Conversion table, Excitation table, verification table
- Circuit verse implementation

2. **Code Converter**   (10 marks)
- Write up as written in your assignment with each reduction steps (4 marks)
- Truth Table (3 marks)
- Circuit verse implementation (3 marks)

3. **HDL Program**   (7 marks)
- Writing the program and execute in eda (5 marks)
- Clock waves (2 marks)

4. **Counters OR PLA (Any 1)**   (10 marks)
- Write up as written in your assignment with each reduction steps (4 marks)
- Truth Table (3 marks)
- Circuit verse implementation (3 marks)