Interest Upstream Circuit Traversal int $\frac{4}{3}$ int $\frac{4}{2}$ int $\frac{4}{4}$ int int (plaintext) R4 **P2 P1** R1 **R2** R3 K1 K1 int K2 K2 **K**3 K3 K4 K4 Generate key Generate K1 Generate K2 Generate K3 Generate K4 Stream Stream Stream streams Stream create content Content Downstream Circuit Traversal data 4 data 4 data 4 data 4 data (plaintext) **P2** R2 R3 R4 **P**1 R1 No K1 K1 Verify? K2 K2 K3 K3 K4 K4 Yes data Verify signature, Verify MAC, Verify MAC, Verify MAC, Verify MAC, XOR with K4 XOR with K3 XOR with K2 XOR for all keys XOR with K1 stream stream K1-K4 stream stream