CHRIS A. TREES

SR. PROGRAM MANAGER & RESEARCH DEVELOPER

EXPERIENCES

2018 2012

Research Developer

Mail Services, LLC

- ◆ Des Moines, IA (remote)
- Researched NFC production process to leverage USPS commercialization incentives.
- Evaluated and integrated AGILE, CI/CD, QA testing and system virtualization.
- Evaluated Angular, Ember, React, Vue, Backbone and Polymer.
- Intro Team for Atlassian JIRA, Confluence, Bitbucket and Bamboo toolchains.
- Evaluated production site processes for refresh and maintenance sustainablity.
- Production site was a PHP frontend with restricted storage, network, and SQL database.

2012 2010

Sr. Software Engineer / Field Sales Engineer

- ♀ San Jose, California (and remote)
- Social Mobile communications platform serving the commercial nuclear power market.
- Developed HTML5 map annotation for mobile field devices.
- Established QA Test Automation and build documentation.
- Worked as Sales Engineer with CEO to adopt the platform to specific needs of new customers.
- · Acquired by CurtissWright.

2010 2002

Consultant

CAT Services

Cedar Falls. Iowa

- Croft Utility Partners Cloud infrastructure design for small communities.
- Grasshorse Animation Studio "Proto-Motion" mixed media stop motion animation.
- Vianeta Communications Training materials for data center installation and testing.

2002 2001

Founder / Architect

♥ Mountain View, California

- Matrix grid network of distributed storage and compute engine for accelerated batch processing.
- Using COTS servers, a grid of indexed storage was used to maintain matrix reduce results in cache.
- Pantera IP was a matrix index tracking of distributed cache algorithm partials for performance.

2001 1985

Previous Millennium

Company & Project per line

♥ Various, California

- Fusion One Integration of Levient redundant high availability server infrastructure stack.
- Monggo Remote desktop service using WAP devices (cell phones).
- Vertical Networks Lead hardware & mechanical design for telephony product "Instant-Office".
- Rapid City Communications Built high-speed-density backplane for VLAN switch.
- SkyStream FPGA design for video streaming switch.
- Sun Microsystems Netra group consultant for an embedded user control console.
- IKOS Field Applications Engineer for ASIC simulation products.
- General Magic Partner development systems. Created touch based magic cell phone prototype.
- National Semiconductor Custom FPGA for ISO-Ethernet product development kit.
- Hughes LAN ATM network switch hub chassis backplane and FPGA based control bus.
- The Engineering Dept (TED) PC Transporter Apple II card used to run IBM Software.
- Lockeed MSC P387 Video display subsystem control board and test station.



EDUCATION

1985 1980

B.S. Electrical Engineering

Iowa State University

Ames. Iowa

Math Honors, Nuclear Engineering Studies

Courses (selective list)

- Docker and Kubernetes
- DevOps: Advanced Kubernetes
- DevOps: Automation with Terraform
- Ansible Automation
- Cumulus Linux and Ansible
- GNS3 Advanced Switching
- React, Redux & Firestore
- Flutter, Dart & Firebase

- Progressive Web Apps (PWA)
- Appium Mobile Automation Testing
- Machine Learing with Python & R
- Ethereum Developer
- Xamarin: Android and iOS shared C# Code
- Web Development with Go
- · Active Directory Lab using Hyper-V
- Complete Blender Creator (3D Modelling)

CONTACT INFO

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MANAGEMENT

Program Management Start-up Consulting Strategic Partnerships Intrapersonal Networking Grant Funding Venture Funding

IP MANAGEMENT

DevOps & SecOps CI/CD/CO

SDN & SDS

OpenStack, Mesos & Proxmox VirtualBox, VMWare & Hyper-V

Docker & Kubernetes

Nagios & WhatUp GOLD

TheDude & SolarWinds

TCP/IP Stack

DNS Bind

IPXE & UEFI

SNMP

Snort

Wireshark

pfsense, ClearOS & Smoothwall

Active Directory

Kerberos

OpenAFS

Windows Defender

LDAP

SOFTWARE

Agile Methodologies Extreme Programming (XP) Cloud Computing Release Management LAMP Web Stack MySQL, Delphi, MariaDB PHP. Phython. Bash Node, JS, React & Angular Dart & Flutter Progressive Web Apps

HARDWARE

ECO & BOM Management Hardware Architecture Firmware Design FPGA & ASIC Design Verilog, VHDL OrCad. PADS. ProTel