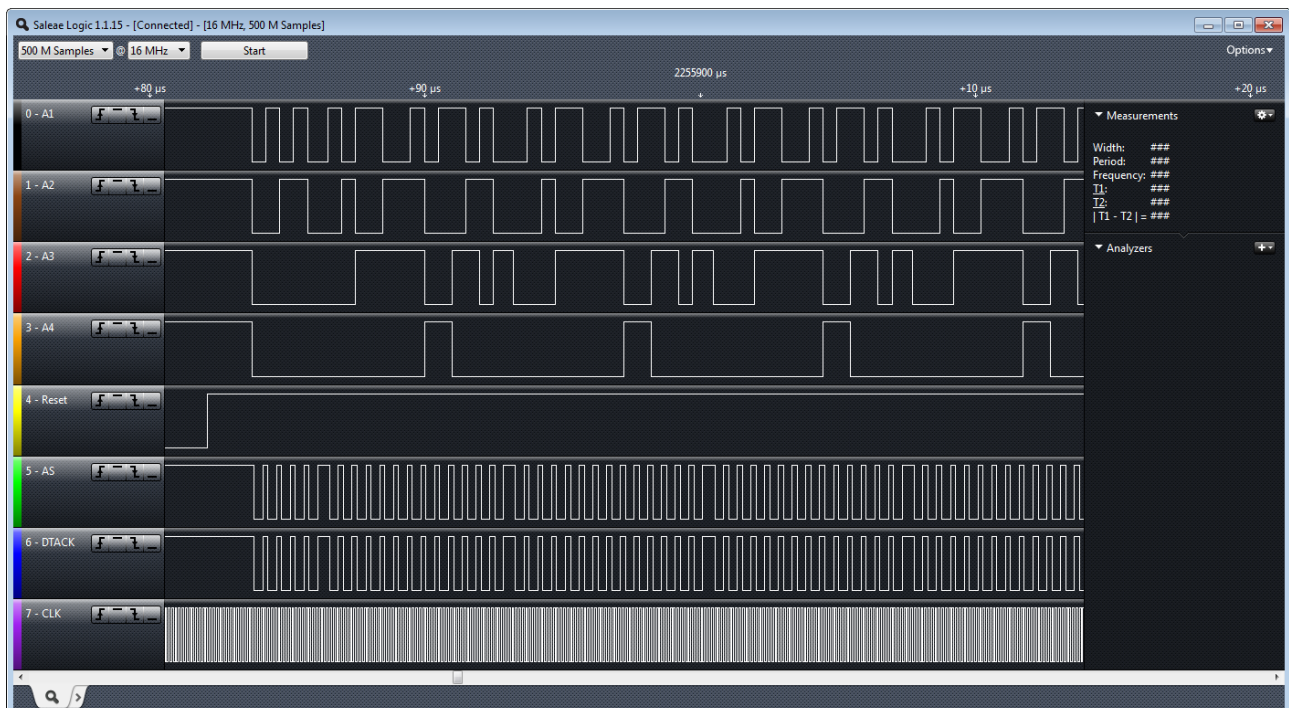Microcomputer Design

Chandler Griscom

15Jan2016

Design Report #5

I. Time – 32.25 Hours total
   a) Week 4 (Wed-Sun) – 13.5 Hours
   b) Week 5 (Mon-Wed) – 18.75 Hours

II. Accomplishments
   a) Soldered Board
   b) Finished VHDL
   c) Tested ROM and RAM, ran simple programs (after lots of debugging)
   d) Established DUART communication (after lots of debugging)
   e) Implemented DUART and periodic (1ms) interrupts

III. Planned Work
   a) Begin writing monitor program
   b) Start thinking about the flash-writing process

IV. Project Completion
   a) Hardware: 100%
   b) Software: 30%

RAM-testing loop waveforms