

# Microcomputer Design

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## Design Report #4

- I. Time – 12 Hours total
  - a) Week 3 (Wed-Sun) – 11 Hours
  - b) Week 4 (Mon-Wed) – 1 Hour
- II. Accomplishments
  - a) Studied timing diagrams, interrupt logic
  - b) Write most of VHDL (netlist, R/W logic, dtacks, interrupts, GPIO)
  - c) PCB Shipped
  - d) Tweak and resubmit schematic version 2
- III. Planned Work
  - a) Solder Thursday or Friday
  - b) Begin looking into:
    - 1. DUART interrupt logic
    - 2. Flash sector write/caching logic
- IV. Project Completion
  - a) Hardware: 90%
  - b) Software: 10%