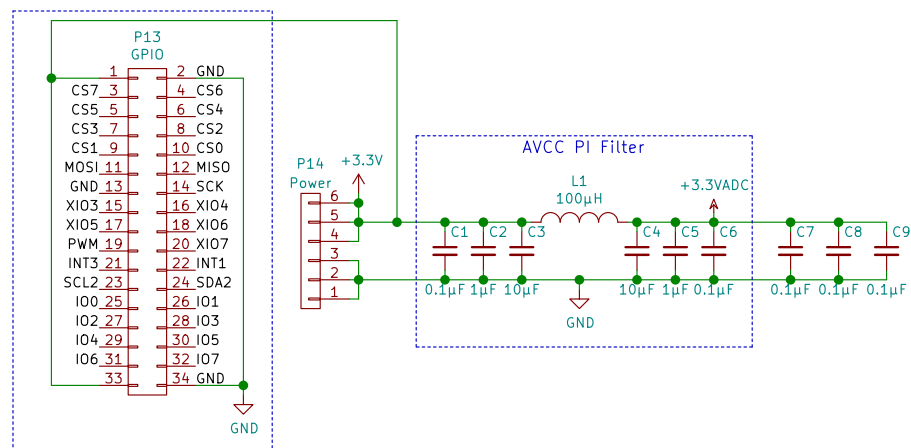
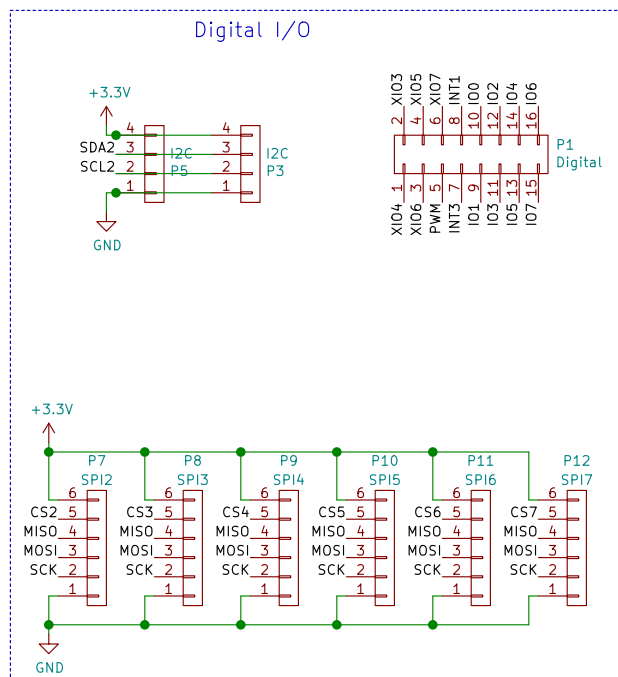


The ADC is connected to INT1 for $\overline{\text{EOC}}$
JP1 can be cut to disconnect ADC from INT1



Cody Nybo

Sheet:

File: CHIP_IO_Breakout.sch

Title: C.H.I.P. IO Breakout with ADC & DAC

| | |
|---------|------------------|
| Size: A | Date: 2016-10-14 |
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KiCad E.D.A. kicad Rev-6964-Dev Build

Rev: 1

Id: 1/1