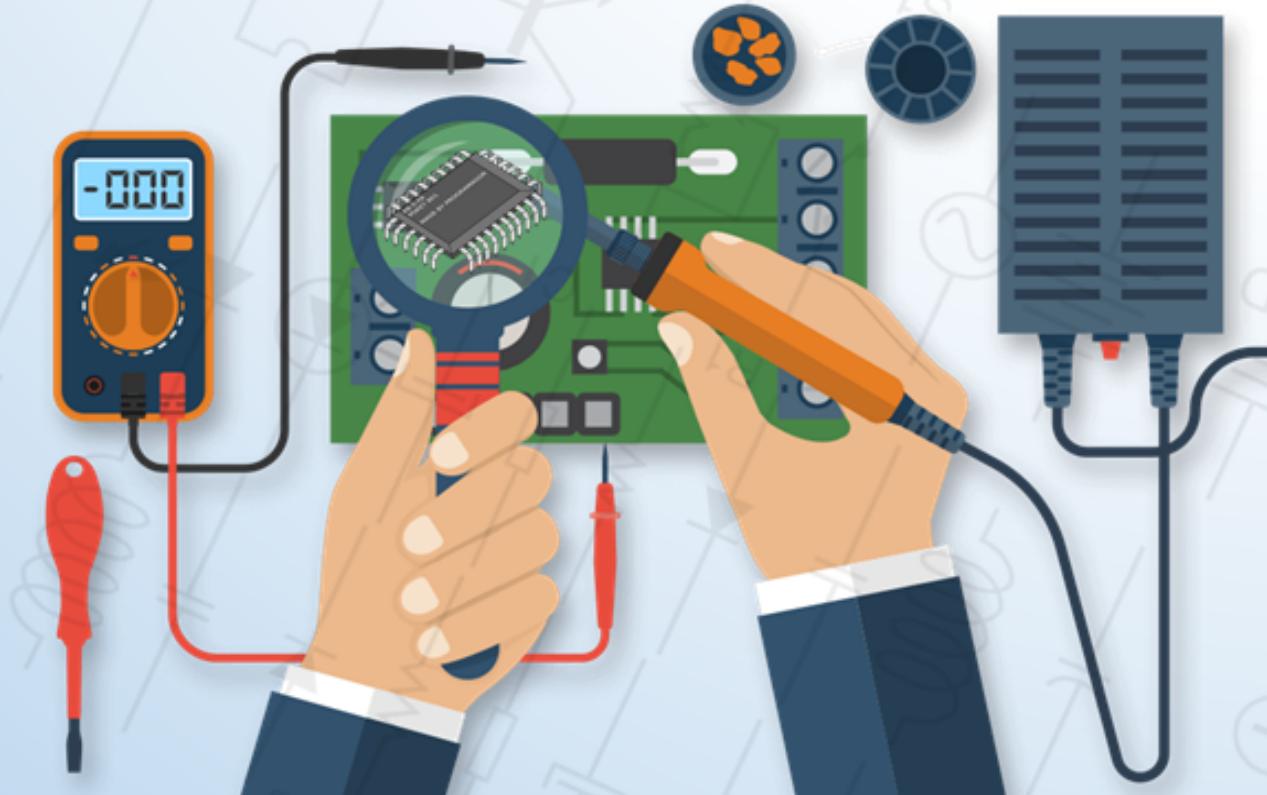




HO CHI MINH CITY UNIVERSITY OF TECHNOLOGY
COMPUTER ENGINEERING

Electronic Device Component

Lab's Report



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Cao Minh Quang - 2052221

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CHAPTER 1

Diode Circuits Analysis

1 Introduction

This laboratory exercise aims to demonstrate different diode wave shaping circuits. In general, these exercises aims to help students to fully understand how these diode circuit works in theory and actual situations. This laboratory exercises also helps students to understand about DC Sweep analysis and Time Domain (Transient) in simulation configuration.

- DC Sweep analysis calculates the steady-state voltages and currents when sweeping a source, model parameter, global parameter, or temperature over a range of values. You can view the results of a DC sweep analysis in either a text output file or in the graphical display of the Probe window.
- Transient analysis involves a set of techniques used to analyze simulation data or experimental results in the time domain, specifically when the system under study is transitioning between two states

The targets are summarized as follows:

- Create Project and design as provided schematic to analyse V-I characteristic with DC Sweep configuration.
- Analyse diode characteristic.
- Analyse some practical diode application circuits.

2 Diode simulation circuit

In this manual, a simple circuit presented below is used in order to demonstrate principles of a diode. Finally, the simulation based DC Sweep configuration is run to analyse the circuit.

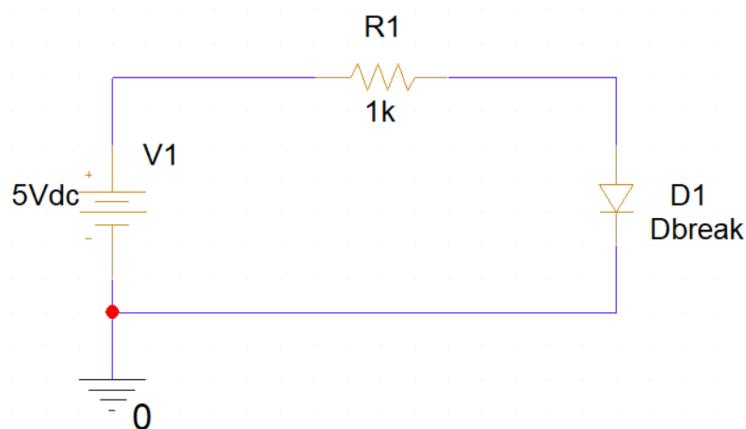


Figure 1.1: A simple simulation circuit using Diode

Step 1: Students are proposed to create a new project in PSPICE. Please select on the option **Create a blank project**.

Step 2: From the list of **Favorites** devices, search the component named **Dbreak**, which is a popular diode in circuit. Other components such as VDC, Resistor and the Ground, which are already used in Lab 1, are also added to the project as well. Finally, set the voltage supply of V1 to **5Vdc**.

Step 3: Create a simulation project by clicking on the menu **PSpice, New Simulation Profile**. After the name of the simulation profile is given, following dialog is shown:

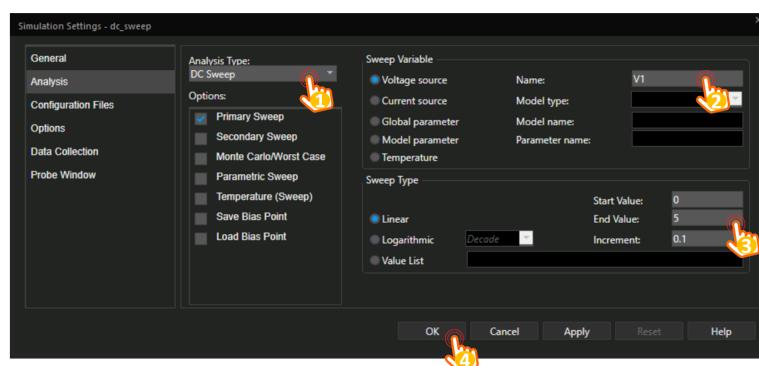


Figure 1.2: Configure the DC-Sweep simulation Profile

Following fields are required to configure:

- **Analysis Type:** select **DC Sweep** mode
- **Sweep variable:** name of input source to analyze, which is (**V1**) in this case.

- **Sweep Type:** vary voltage of the source, which is from 0V to 5V and the incremental step is 0.1V.

Step 5: Run simulation (F11 or from menu **PSpice**, select **Run**). The simulation result for DC Sweep mode is launched as follows:

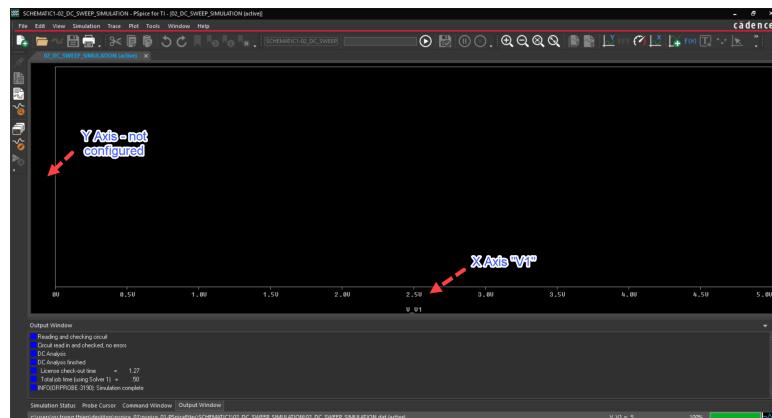


Figure 1.3: Simulation results for DC-Sweep mode

However, the screen is empty without any tracking information. In this case, we need to monitor the current in the circuit having a diode. To do this, from menu **Trace**, select **Add Trace** to open following dialog:

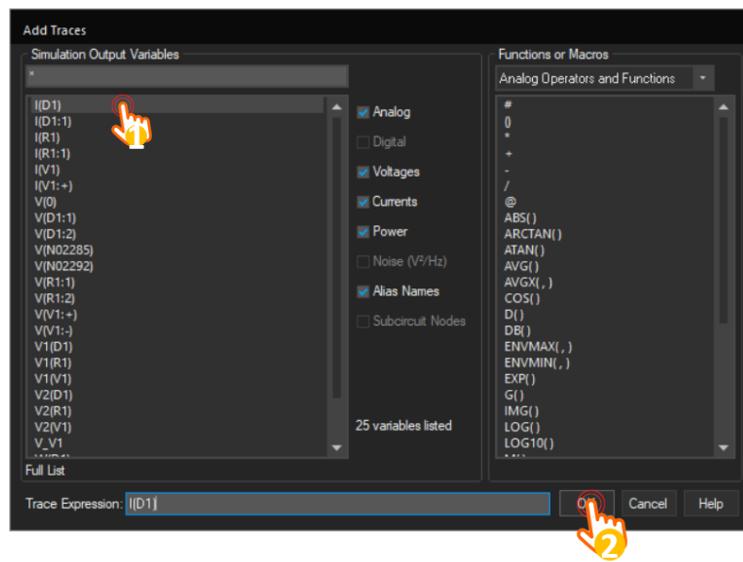


Figure 1.4: Add $I(D1)$ to Y-Axis Trace

Choose **I(D1)** which is the current passing through the diode (and also the main current in the circuit having 2 components in a series). Finally, simulation results are depicted as follows:

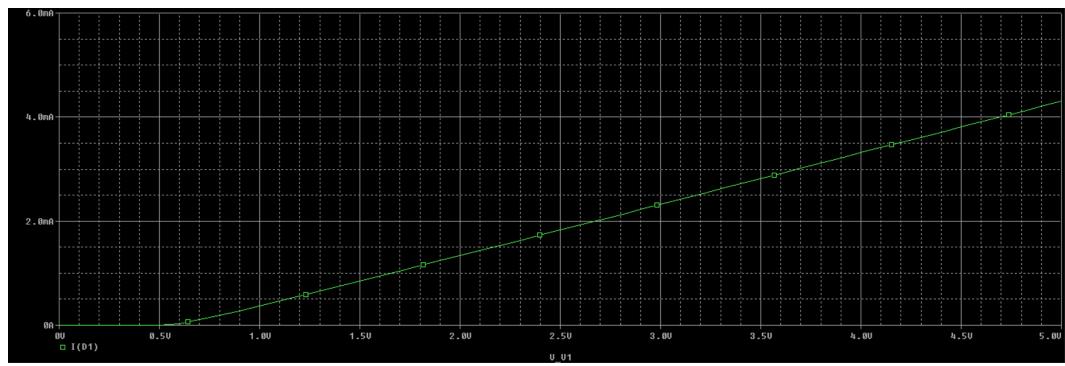


Figure 1.5: Simulation Result

Step 6: In order to make tracking information more readable, right click on the tracking line and choose **Trace Property** as follows:

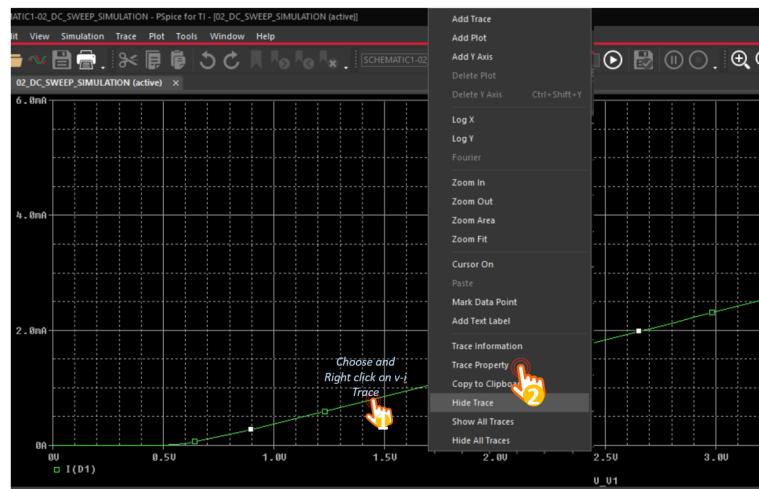


Figure 1.6: Configure Trace Property

The following dialog is opened, allow you to change the color and also the width of the tracking line

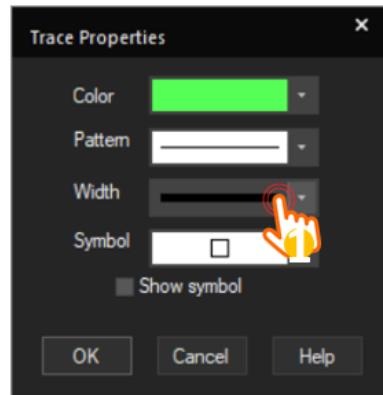


Figure 1.7: Change Trace Width to make it more readable

The final shape of the simulation result is look like the following picture:

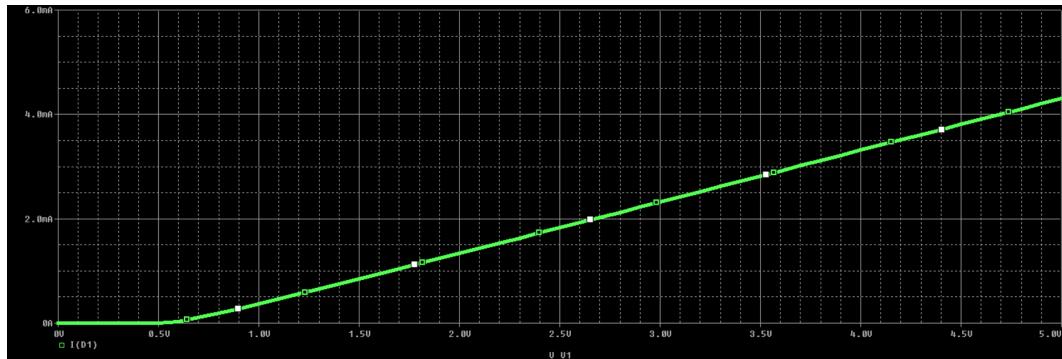


Figure 1.8: Simulation result after changing the Trace Width

The whole data for every point of the input voltage can be extract to text file by right click on the curve and select the option **Copy to Clipboard**. Finally, the data can be pasted to text editor, for instance, Notepad IDE.

1	V_V1	I (D1)
2	0	4.51145941877531e-26
3	0.1	5.67677205586876e-13
4	0.2	2.30075512866725e-11
5	0.3	1.09023241402322e-09
6	0.4	5.19599562003783e-08
7	0.5	2.27803048431717e-06
8	0.6	3.30820579793995e-05
9	0.7	0.000103550661481039
10	0.8	0.000188098468350652
11	0.9	0.000278027216306514
12	1	0.000370545375345954

Figure 1.9: Extract data to Notepad Editor

It is obviously that when the power supply is less than 0.7V, the current is close to zero (in order of 10^{-9} at 0.3V). However, when the input voltage is 1V, the current in the circuit is around 0.37mA. This result can be estimated as $(1V - 0.7V)/1k$, when the practical diode model is applied. Students are proposed to check the simulation results at other points, such as $V_1 = 2V$ or $V_1 = 4V$.

Step 7: A very important analysis with a diode is the relation between the voltage and the current across the diode, which is popular known as the V-I characteristic of the diode. To do this, on the simulation result windows, **double click on the X-Axis** to open the dialog as following:

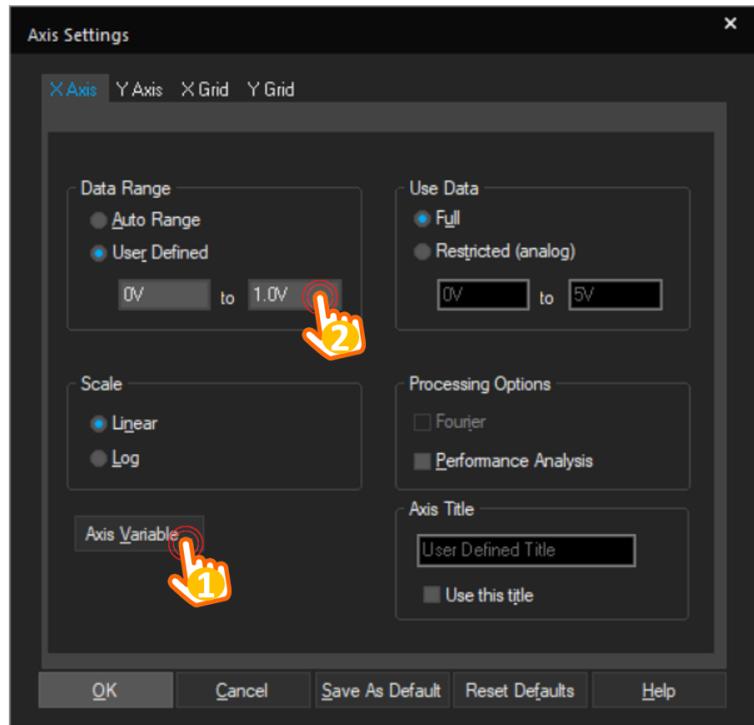


Figure 1.10: Set the X-Axis of the simulation result

Firstly, clicking on the **Add Variables** button and select V(D1:1), which is the voltage across the diode. Secondly, on the data range, **select from 0V to 1V in the User Defined option**. Finally, add a trace from menu Trace, and select I(D1) as previous simulation. The figure bellow is achieved:

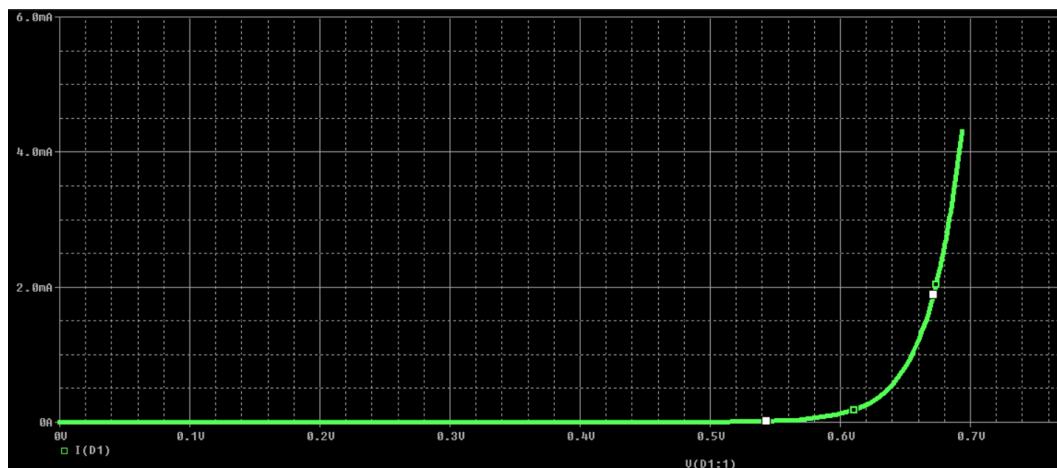


Figure 1.11: V-I characteristic of a diode

This figure confirms that in the forward bias mode, the drop-down voltage of a diode is around 0.7V, which is the default value in the practical diode model.

Step 8: The bias simulation is also applied in this lab. By clicking on menu **PSpice, New Simulation Profile**. Similar to the first lab, after providing the name for the simulation profile(e.g. bias_point), only the analysis type is changed to **Bias Point**. Finally, click **OK** button to create the second simulation profile.

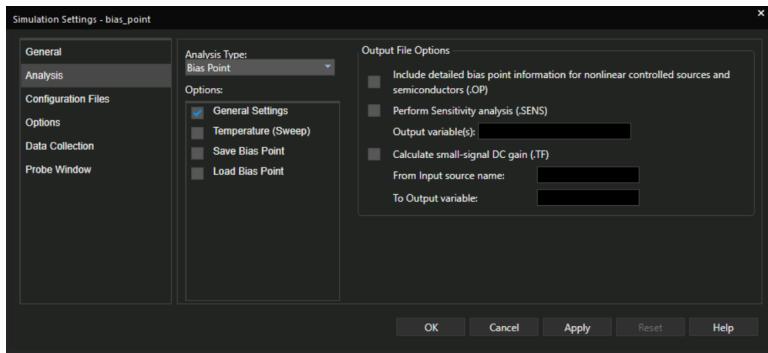


Figure 1.12: Add Bias Point simulation profile to the project

Currently, there are two simulation profiles in the project, which can be browsed as following. In order to set a profile to active, right click on this profile and select **Make Active**.

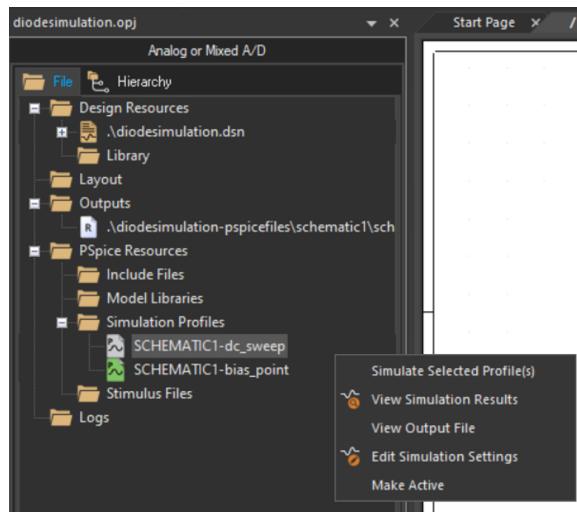


Figure 1.13: Set the active simulation profile

Bias simulation results are displayed as follows:

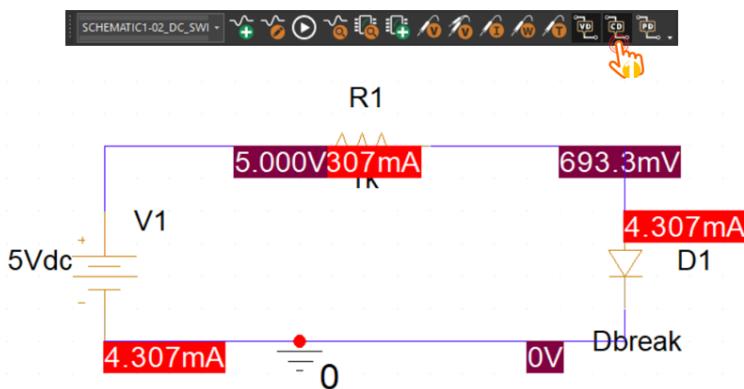


Figure 1.14: Enable Bias Current Display

From the simulation results, it is confirmed that the drop-down voltage of a diode is around 0.7V, which is very closed to the practical diode model.

3 Exercise and Report

3.1 Complete diode model

According to the above simulation circuit (Figure 1.2), determine the voltage across the resistor and the diode (V_R , V_D), and also the current I in the circuit with **two different values of R1**, including **220 Ohm** and **1.5K Ohm**. It is assumed that the complete diode model is used to analyse, having the forward voltage at **0.7V** and the internal resistor equal to **50 milliohms**.

Finally, the simulation on PSpice is run to double check with theory calculation. Brief explanations concerning the difference between theory and simulations can be provided in the report.

3.1.1 Theory calculation

Notes:

Explanations, formulas, and equations are expected rather than only results.

According to the properties of complete diode model:

We have $V_D = 0.7 + I \times r_d$

Formula to calculate $V_R = I \times R$

Formula to calculate $I = \frac{V - 0.7}{R + r_d}$

Finally, when $R = 220$ Ohm, $I = \frac{5 - 0.7}{220 + 50 \times 10^{-3}} = 19.54(mA)$

$$\Rightarrow V_R = 19.54 \times 10^{-3} \times 220 = 4.2988(V)$$

And when $R = 1.5K$ Ohm, $I = \frac{5 - 0.7}{1500 + 50 \times 10^{-3}} = 2.86(mA)$

$$\Rightarrow V_R = 2.86 \times 10^{-3} \times 1500 = 4.29(V)$$

3.1.2 PSpice Simulation

Set the simulation profile to bias-point. Moreover, enable both **Enable Voltage Bias Display** and **Enable Current Bias Display** to show the simulation results.

Students are supposed to capture the screen in PSPice and present in this report.

Simulation results (images):

Your image goes here

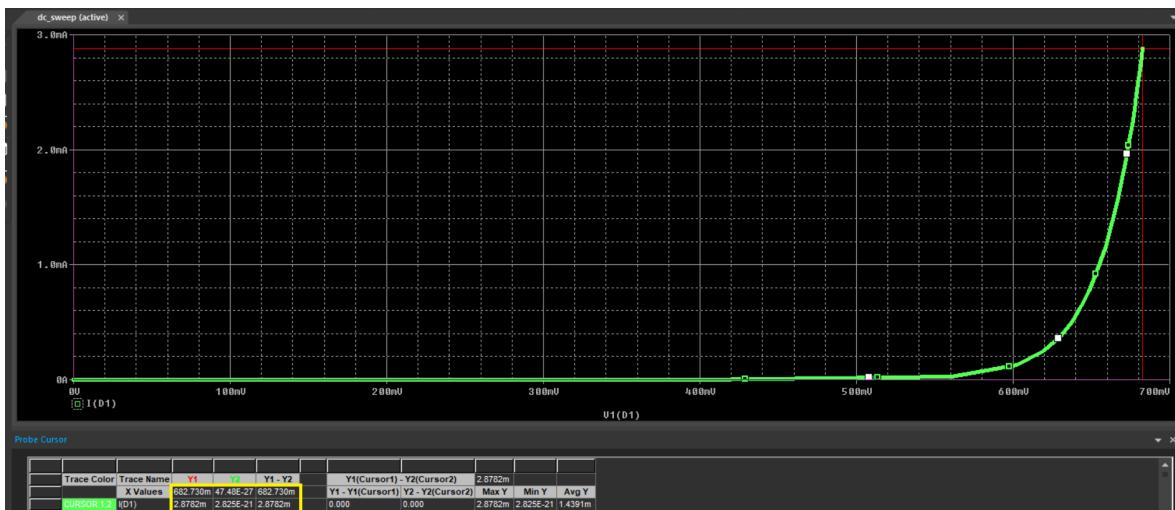


Figure 1.15: The V-I characteristic when $R=1500\text{k}\Omega$.

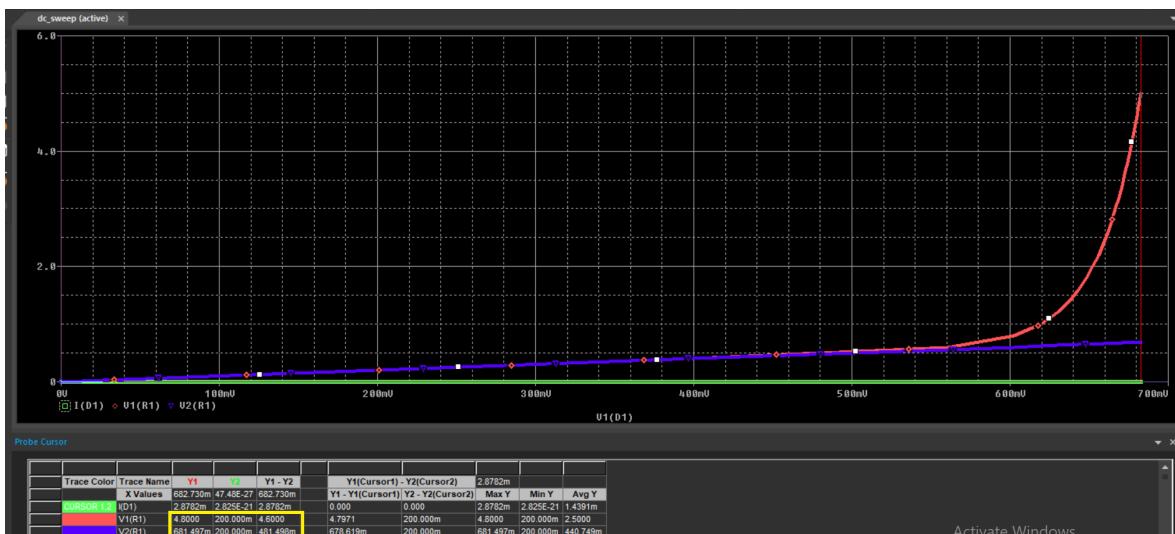


Figure 1.16: The graph of V_R when $R=1500\text{k}\Omega$.

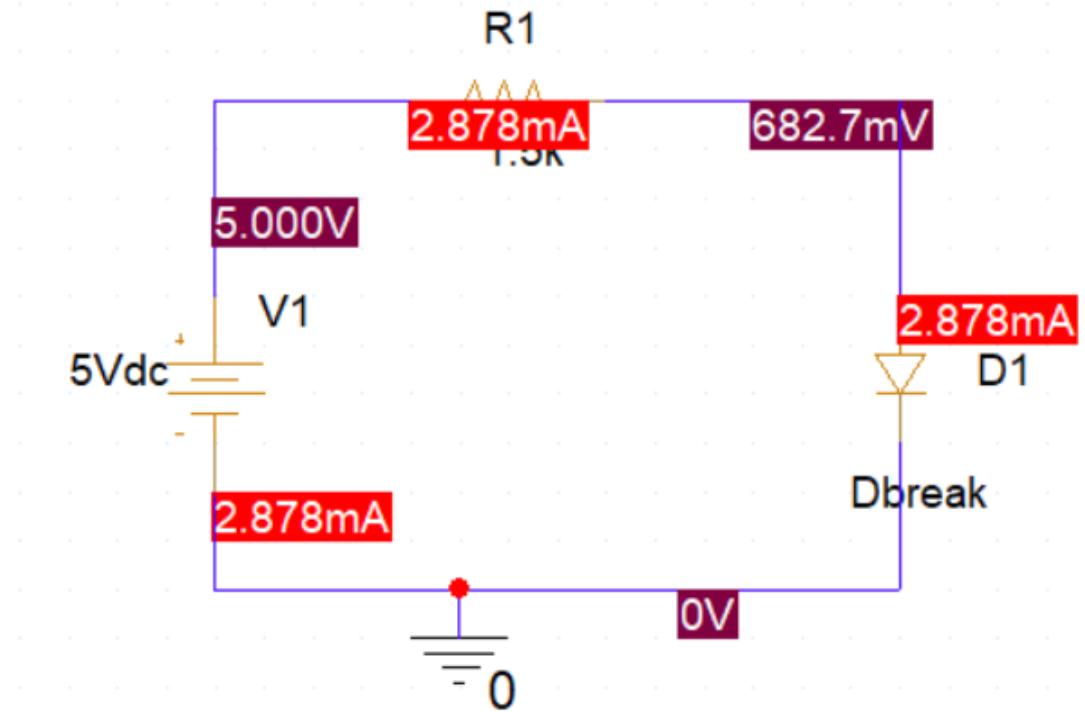


Figure 1.17: The bias point simulation when $R=1500\text{k}\Omega$.

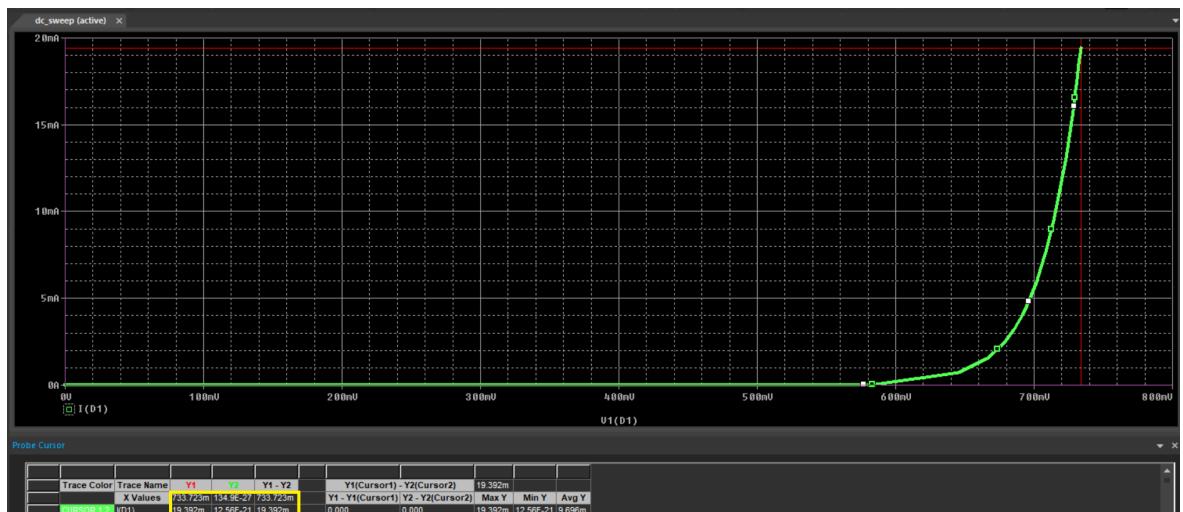


Figure 1.18: The V-I characteristic when $R=220\text{k}\Omega$.

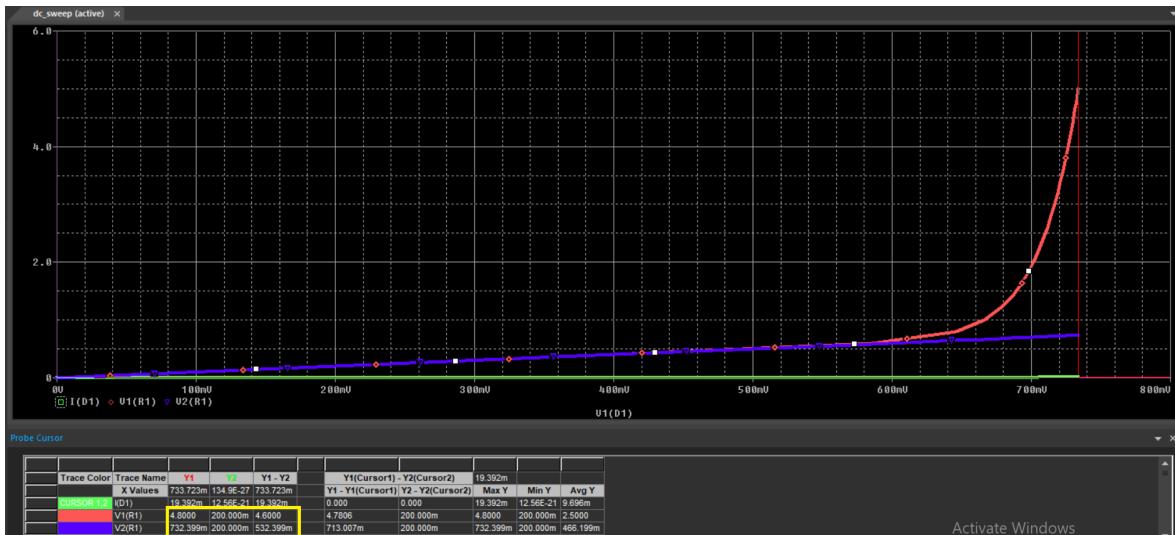


Figure 1.19: The graph of V_R when $R=220\text{k}\Omega$.

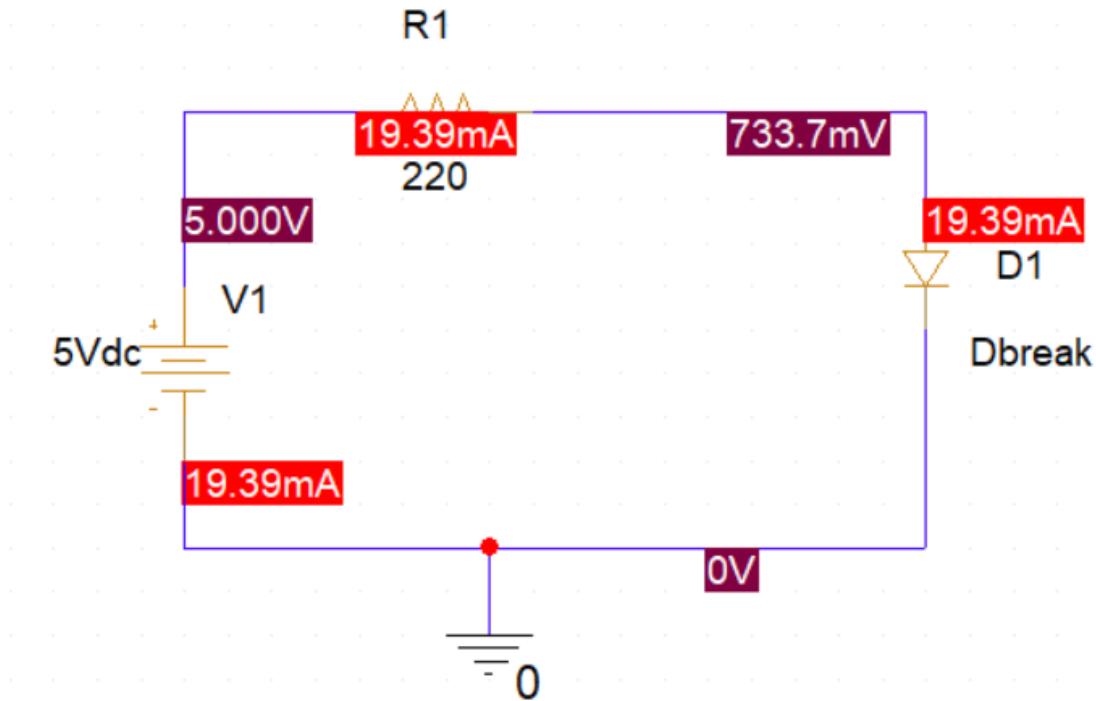


Figure 1.20: The bias point simulation when $R=220\text{k}\Omega$.

3.1.3 Comparison

In this section, the theory calculations and PSpice simulations are summarized in the table below to compare the difference. Students are supposed to fill all information in the table.

	Theory			PSpice		
	V_R	V_D	I	V_R	V	I
$R = 220 \text{ Ohm}$	4.2988 (V)	0.7 (V)	19.54 (mA)	4.2663 (V)	0.7337 (V)	19.39 (mA)
$R = 1.5K \text{ Ohm}$	4.29 (V)	0.7(V)	2.86 (mA)	4.317(V)	0.6827(V)	2.878 (mA)

According to the above **Exercise Results**, give some comments about observation (between calculation results and simulation results):

The values obtained by theory's calculation and Pspice' simulation are really close to each other. Thus, we can consider both are exactly the same.

3.2 Diode in a series

Similar to the previous exercise, determine the value of the voltage V_{D1} , V_{D2} , V_{D3} and the current I for the given circuit. Then, simulate again the circuit using PSpice. However, in this case, the practical diode model is used with the forward voltage is 0.7223V.

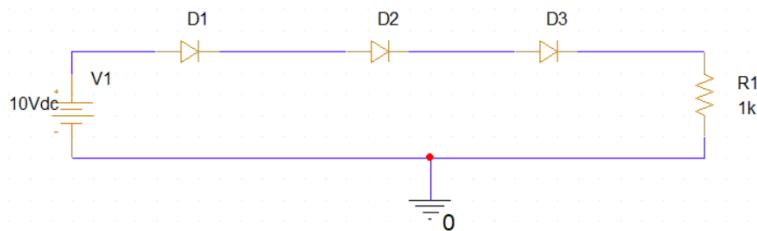


Figure 1.21: Find the voltage and the current in the given circuit

3.2.1 Theory Calculation

Notes:

Explanations, formulas, and equations are expected rather than only results.

According to the properties of practical diode model:

We have $V_{D1} = V_{D2} = V_{D3} = 0.7223(V)$

$$V_{R1} = V_{source} - 3 \times 0.7223 = 7.83(V)$$

$$\text{Formula to calculate } I: I = \frac{V_{source} - 3 \times 0.7223}{R_1}$$

$$I = \frac{10 - 3 \times 0.7223}{1000} = 7.8(mA)$$

3.2.2 PSpice Simulation

Set the simulation profile to bias-point. Moreover, enable both **Enable Voltage Bias Display** and **Enable Current Bias Display** to show the simulation results.

Students are supposed to capture the screen in PSPice and present in this report.

Simulation results (images): Your image goes here

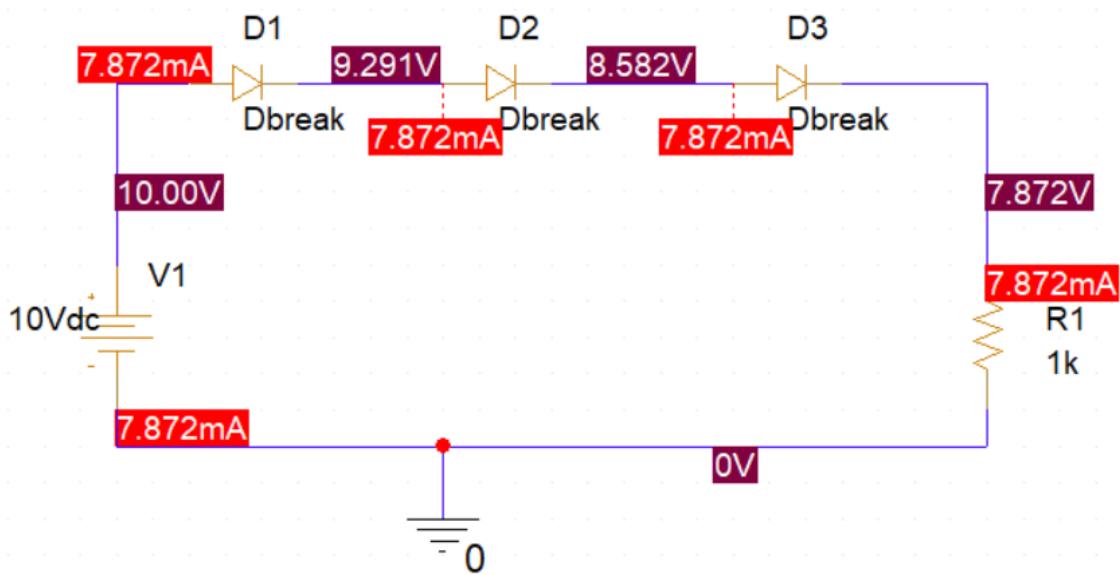


Figure 1.22: The bias point simulation.

3.2.3 Comparison

	V_{D1}	V_{D2}	V_{D3}	V_{R1}	I
Calculation	0.7223(V)	0.7223(V)	0.7223(V)	7.83(V)	7.8(mA)
PSpice	0.709(V)	0.709(V)	0.71(V)	7.872(V)	7.872(mA)

The circuit in this exercise is a simple solution to design a power supply by leveraging a voltage drop of a diode. For example, a SIM module used to send SMS messages has a good voltage supply at 4.3V. In this case, a diode is connected from a 5V supply (which is

a very popular voltage) and then, connected to the module SIM. Not only used to protect the module to avoid reverse current, a diode is a low-cost solution to generate 4.3V power supply for the SIM module.

3.3 Circuit Analysis with Diode

In PSpice, some exercise in the lesson can be simulated to confirm the results. Although it is not exactly the same values (e.g. voltage and current), the simulation in PSpice is a tool to check your solution. An example simulation circuit having diode and resistors is depicted as follows:

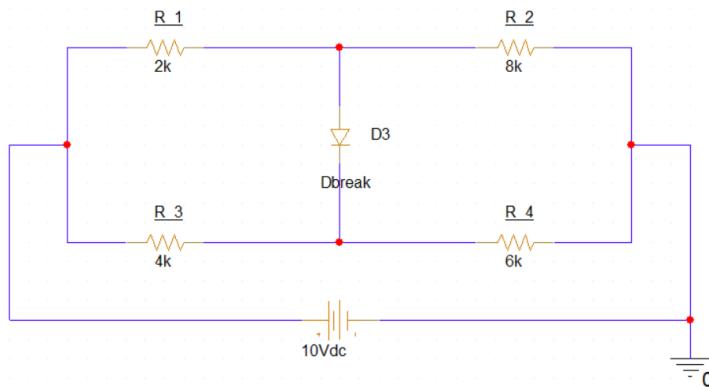


Figure 1.23: Circuit analysis with diode

Students are proposed to analyse this circuit using practical diode model, with the drop-down voltage is around 0.7V. Then the simulation is run on PSpice to double check with your results.

3.3.1 Theory calculation

It is assumed that the voltage at anode and cathode of the diode V_1 and V_2 . It is assumed that the diode is in forward bias mode.

According to the practical diode mode: $V_1 - V_2 = 0.7V$

Students are proposed to construct the equations to determine the current across all the resistors.

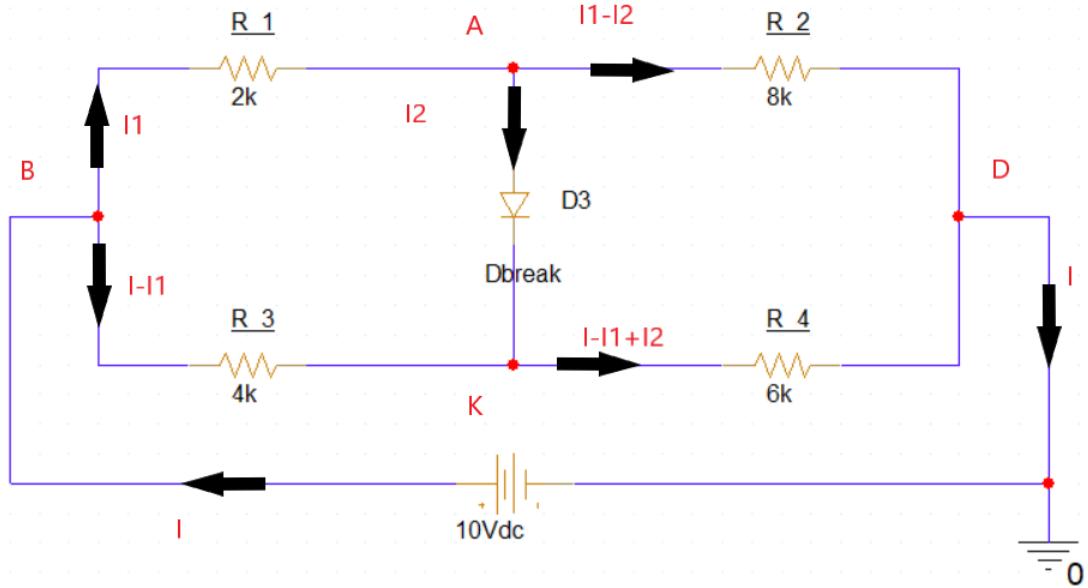


Figure 1.24: Dividing the current at each node to find the value through each branches.

We have the system of 3 equations related to the voltage in the circuit:

$$\begin{cases} V_{AK} = V_{AB} + V_{BK} \\ V_{AK} = V_{AD} + V_{DK} \\ V_{BD} = V_{BK} + V_{KD} \end{cases}$$

=>

$$\begin{cases} 0.7 = -2000I_1 + 4000(I - I_1) \\ 0.7 = 8000(I_1 - I_2) - 6000(I - I_1 + I_2) \\ 10 = 4000(I - I_1) + 6000(I - I_1 + I_2) \end{cases}$$

=>

$$\begin{cases} I = 2.065(mA) \\ I_1 = 1.26(mA) \\ I_2 = 325(μA) \end{cases}$$

3.3.2 PSpice simulation

The bias point profile is used to run the simulation in this exercise. Students are proposed to capture the screen on PSpice showing the current and the voltage in the circuit.

Your image(s) goes here

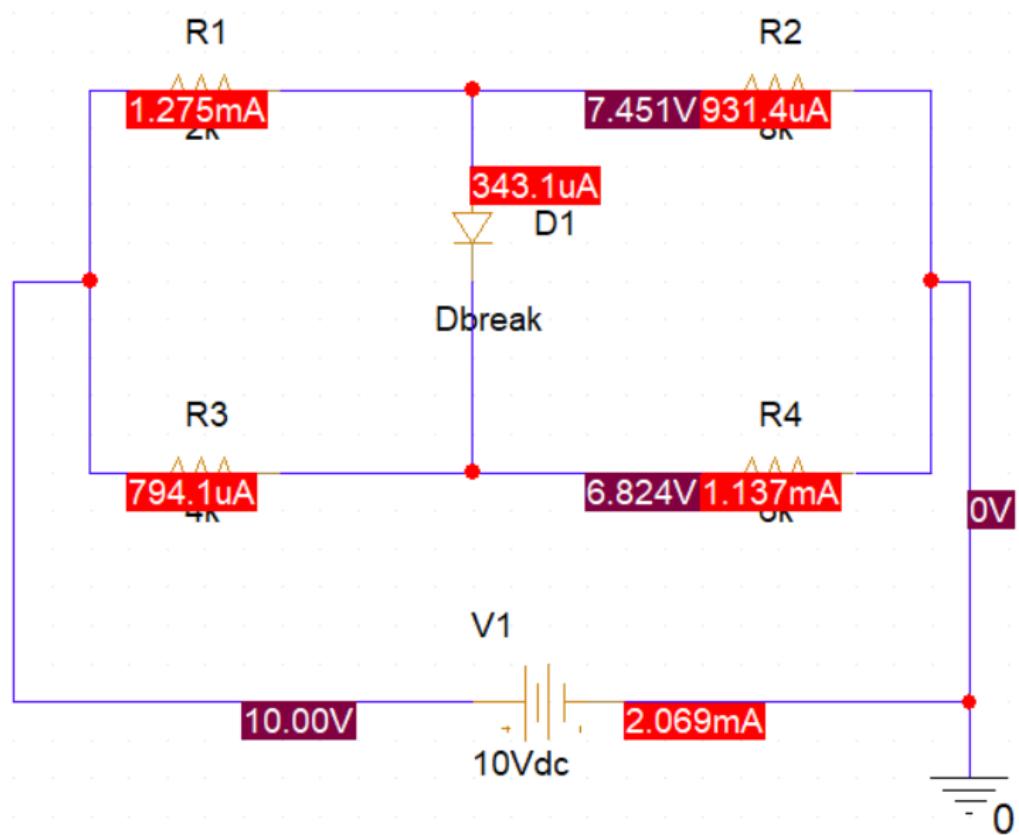


Figure 1.25: The bias point simulation when $V=10(V)$.

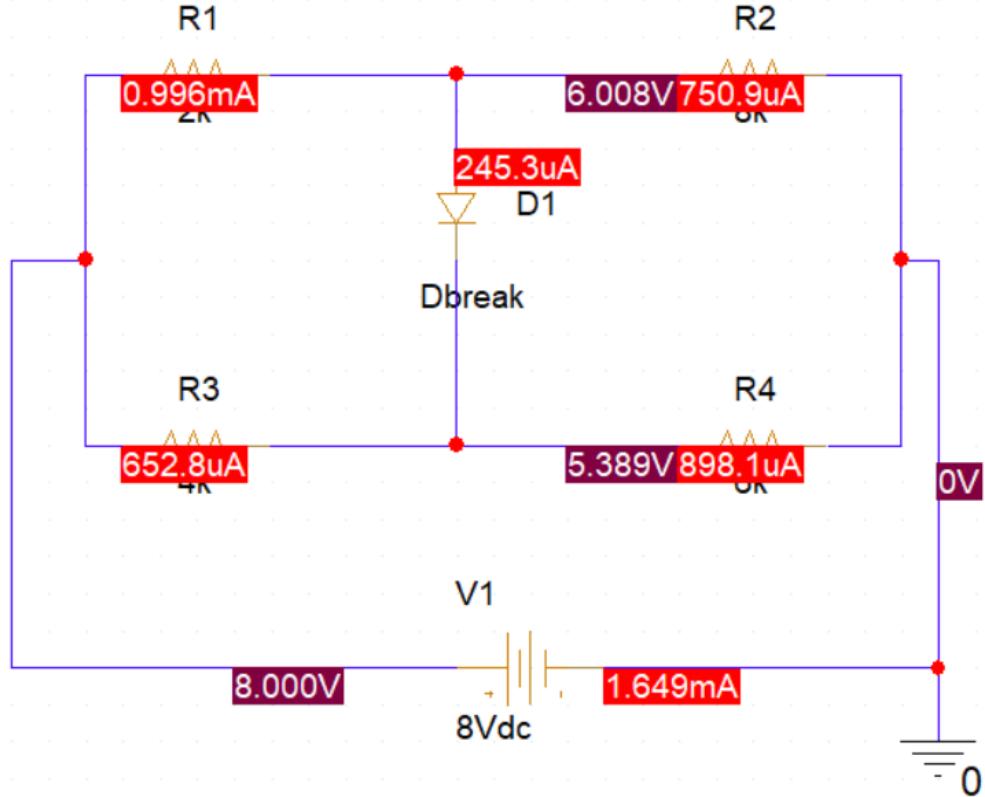


Figure 1.26: The bias point simulation when $V=8(V)$.

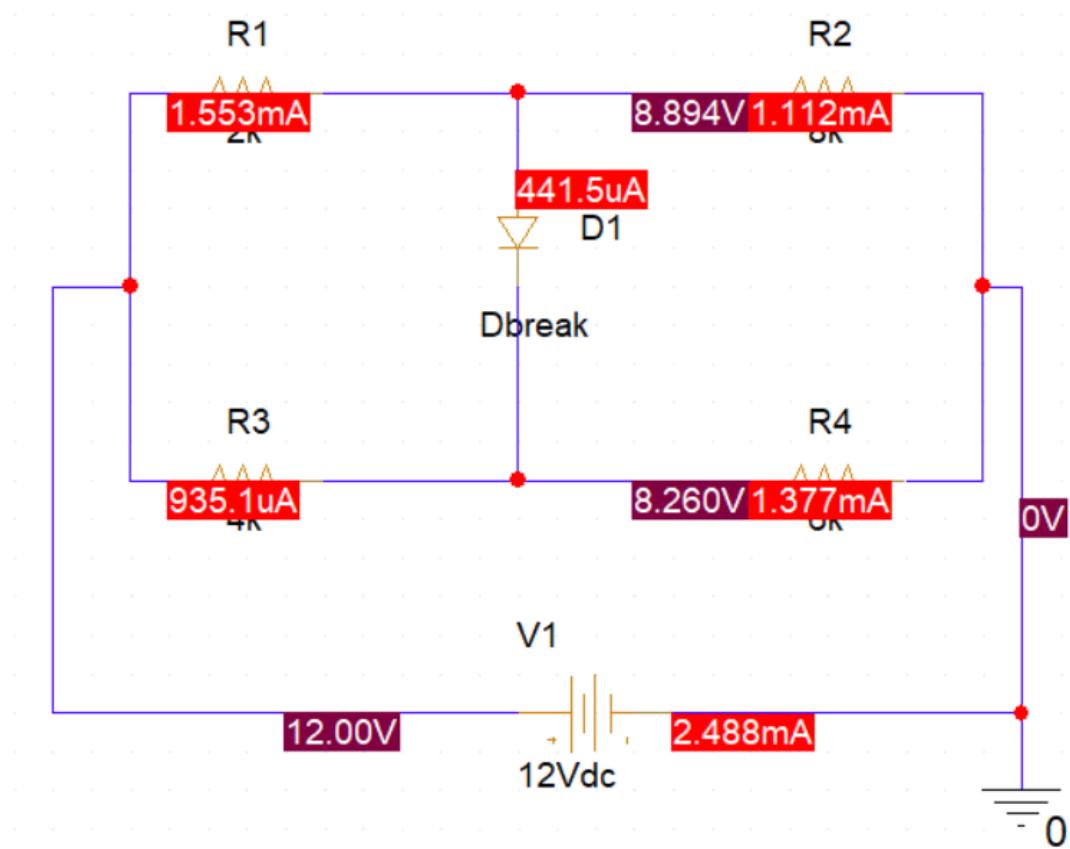


Figure 1.27: The bias point simulation when $V=12(V)$.

3.3.3 Comparison

Students are supposed to summarize the results from both theory calculations and PSpice simulations and fill in the table below.

Theory Calculation						PSpice Simulation						
I_{R1}	I_{R2}	I_{R3}	I_{R4}	V_1	V_2	I_{R1}	I_{R2}	I_{R3}	V_{R4}	V_1	V_2	
$V = 8V$	0.98(mA)	755(μ A)	665(μ A)	890(μ A)	6.04(V)	5.34(V)	0.996(mA)	750.9 (μ A)	652.8 (μ A)	898.1(μ A)	6.008(V)	5.389(V)
$V = 12V$	1.54(mA)	1.115(mA)	945(μ A)	1.37(mA)	8.92(V)	8.22(V)	1.553(mA)	1.112(mA)	935.1(μ A)	1.337(mA)	8.894(V)	8.26(V)

3.4 Clamper Diode Circuit

The circuits in the figure below are known as clampers or DC restorers. The simulation on PSpice is also shown in the figure below. These circuits clamp a peak of a waveform to a specific DC level (e.g. 0.7V). Students are supposed to implement the circuit in PSpice to verify their results from theory calculation.

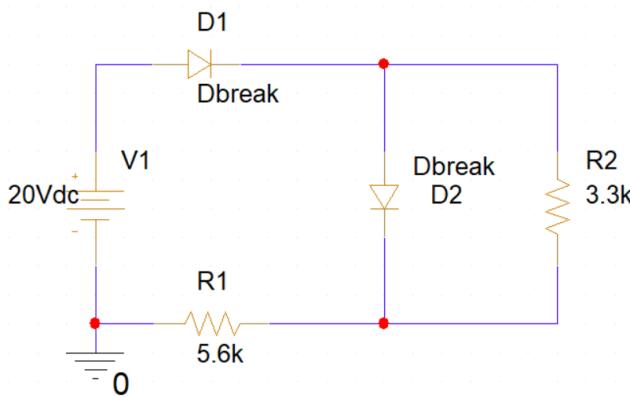


Figure 1.28: Clamper circuit using diode

3.4.1 Theory calculation

In this part, it is assumed that the practical diode model is used. Present your equations to calculate three different currents, including I_{R1} , I_{R2} , I_{D2} and the voltage V_{R2} .

$$I_{R2} = \frac{0.7}{R_2} = \frac{0.7}{3300} = 0.212(mA)$$

$$I_{R1} = \frac{V_{source} - 2 \times 0.7}{R_1} = \frac{20 - 2 \times 0.7}{5600} = 3.32(mA)$$

$$I_{D2} = I_{R1} - I_{R2} = 3.108(mA)$$

$$V_{R2} = I_{R2} \times R_2 = 0.6996(V)$$

3.4.2 PSpice simulation

The bias point simulation is run in PSpice. Capture your screen with voltage and current are enabled in the results.

Your image(s) goes here

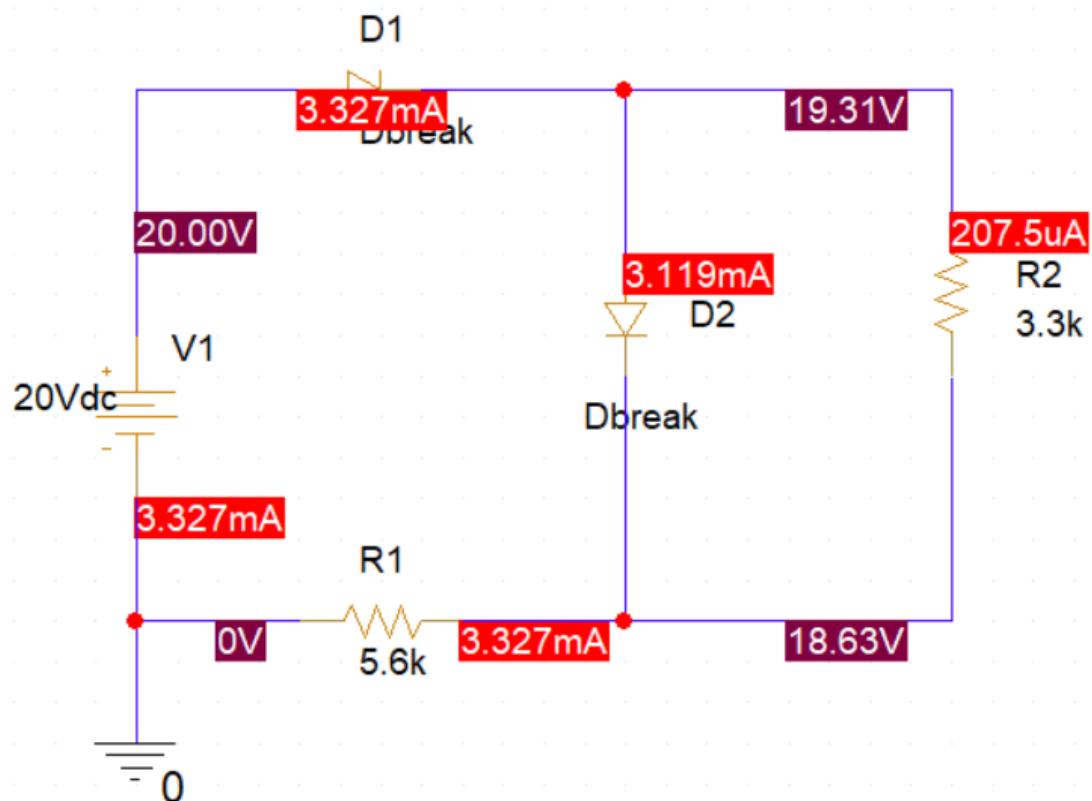


Figure 1.29: The bias point simulation when $V=20(\text{V})$.

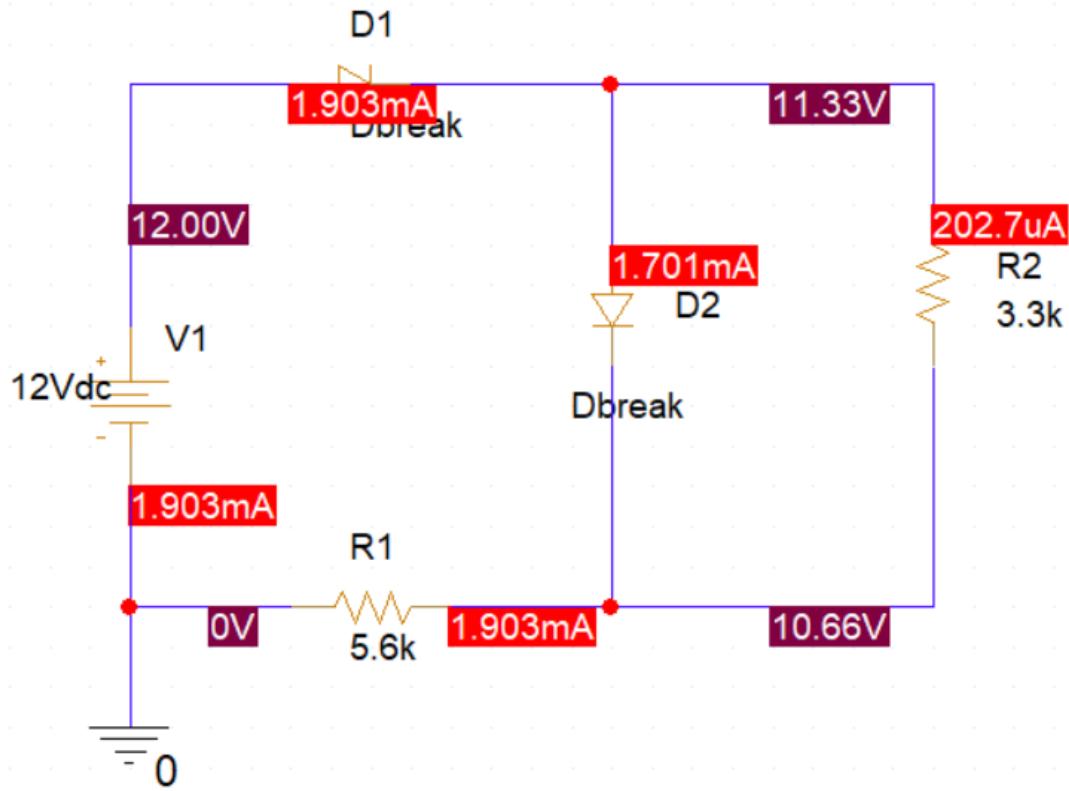


Figure 1.30: The bias point simulation when $V=20(V)$.

3.4.3 Comparison

Students are supposed to summarized the results from both theory calculations and PSpice simulations and fill in the table bellow.

	Theory calculation				PSpice simulation			
	I_{R1}	I_{R2}	I_{D2}	V_{R2}	I_{R1}	I_{R2}	I_{D2}	V_{R2}
$V = 12V$	1.89(mA)	0.212(mA)	1.678(mA)	0.6996(V)	1.903(mA)	0.2027(mA)	1.701(mA)	0.67(V)
$V = 20V$	3.32(mA)	0.212(mA)	3.108(mA)	0.6996(V)	3.327(mA)	0.2075(mA)	3.119(mA)	0.68(V)

What can be concluded from this table?

When the D2 diode is active, it will maintain a fixed voltage between the anode and cathode. So in any circumstances, the changes in voltage of source does not affect the value of I_{R2} and V_{R2}

3.5 Power switching circuit

The corruption of main power can be crucial in many different situations. For instance, when power is abruptly lost, you might want to save some backup data on a micro-controller. Such use cases need some form of automatic switching circuit to a secondary power source, such as a battery.

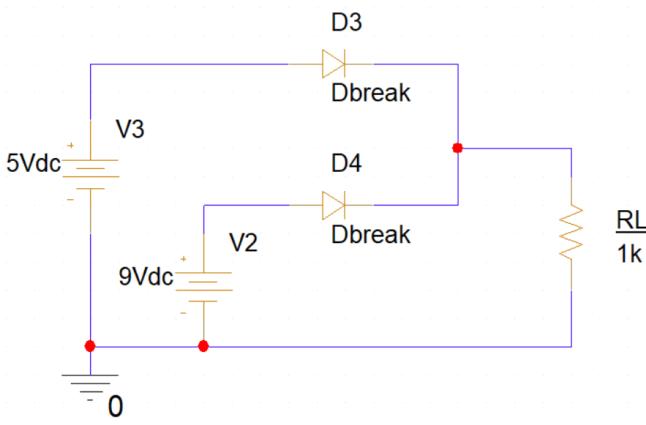


Figure 1.31: Power switching circuit

The simplest solution to this problem is simply to add a diode on each voltage source, as shown in the figure above. In this circuit, a 5V power supply can be used as a backup battery. Meanwhile, 9V is the main power supply for the system, which is demonstrated by a load resistor.

However, the main issue with this naive approach is that the voltage drop (forward voltage) across the diode might be too high for the system. Thanks to Schottky diode, this can be mitigated by using extremely low forward voltage one, which can be found on the market (e.g. Schottky diode having drop-down voltage around 250mV at 1A).

3.5.1 Theory calculation

In this part, it is assumed that the practical diode model is used. Present your equations to calculate the current I_{D3} , I_{D4} , I_{RL} and the volatage V_{RL} in three different cases: only 5V, only 9V and both 5V and 9V for the power supply.

For the case only 5V source, since the 9V source is disconnected from the circuit, there is no current through D4 so D4 is disconnected from the circuit

$$I_{D3} = \frac{5 - 0.7}{1000} = 4.3(mA)$$

$$I_{D4} = 0(A)$$

$$I_{RL} = I_{D3} = 4.3(mA)$$

$$V_{RL} = 5 - 0.7 = 4.3(V)$$

For the case only 9V source, since the 5V source is disconnected from the circuit, there is no current through D3 so D3 is disconnected from the circuit

$$I_{D4} = \frac{9 - 0.7}{1000} = 8.3(mA)$$

$$I_{D3} = 0(A)$$

$$I_{RL} = I_{D4} = 8.3(mA)$$

$$V_{RL} = 9 - 0.7 = 8.3(V)$$

When both sources are connected, whenever the 9V source still works without any abrupt loss, the 5V source's role is still a back-up so it will not be active. Then, D3 is disconnected from the circuit and the values are exactly same the case with only 9v source.

3.5.2 PSpice simulation

The bias point simulation is run in PSpice with both power sources are enabled. Capture your screen with voltage and current are enabled in the results.

Your image(s) goes here

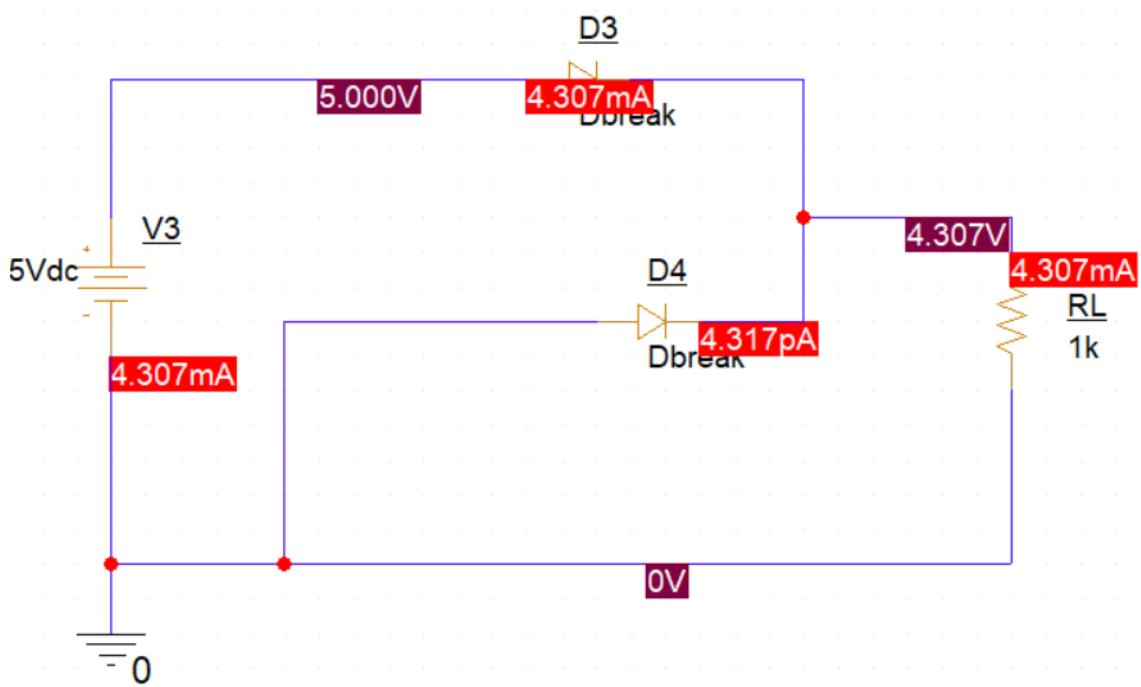


Figure 1.32: The bias point simulation when there is only 5V source.

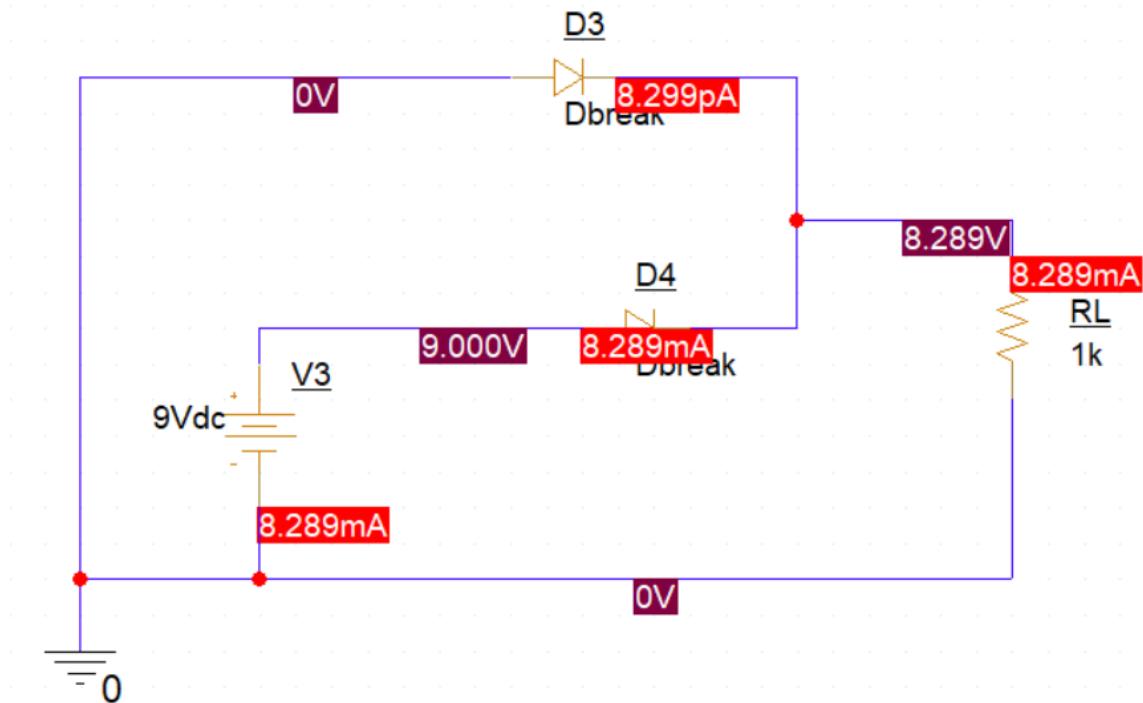


Figure 1.33: The bias point simulation when there is only 9V source.

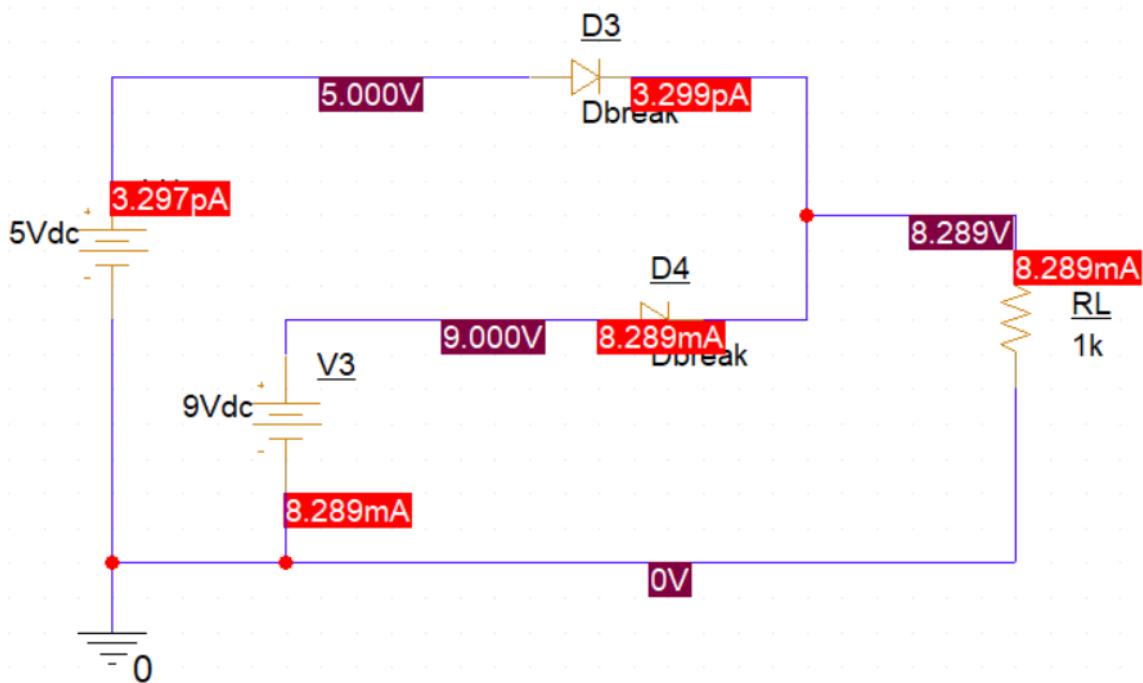


Figure 1.34: The bias point simulation when there are both sources.

3.5.3 Comparison

Students are supposed to summarize the results from both theory calculations and PSpice simulations and fill in the table below. Both power sources are connected to the circuit.

Theory calculation				PSpice simulation			
I_{D1}	I_{D2}	I_{RL}	V_{RL}	I_{D1}	I_{D2}	I_{RL}	V_{RL}
0(A)	8.3(mA)	8.3(mA)	8.3(V)	3.299(pA) \approx 0	8.289(mA)	8.289(mA)	8.289(V)

The last feature of the diodes in this circuit is to protect the reverse current in case both the power sources are switch on together. This use case is very popular when a micro-controller platform is programmed and powered by an USB port and also equipped with an adapter for external power supply.

3.6 Half-wave Rectifier

In this exercise, an alternating source is used to generate a half-wave rectifier output using a diode. The schematic of the simulation is given below:

Step 1: Create the schematic.

The new component in this schematic is the **VSIN - Sine Voltage Source**, which can be found in the Favorites list. This component has four different parameters required to configure, as follows:

- VOFF: Offset voltage of the source
- VAMPL: Amplifier voltage of the source
- FREQ: Frequency of the alternative current

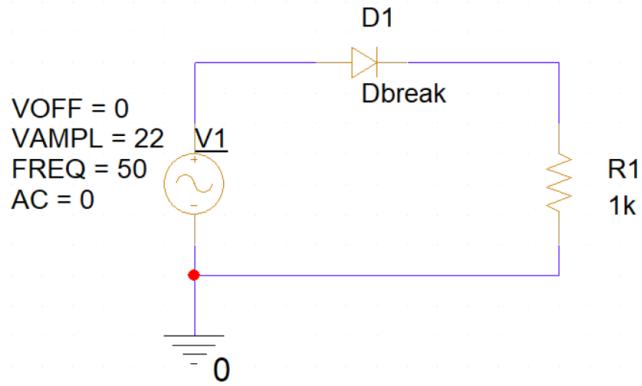


Figure 1.35: Half-wave Rectifier with Voltage Sin Source

- AC: The source type (having value 0 or 1), to switch between VAC and VSIN. In VSIN source, the frequency can be modified.

After setting these parameters are set to the values shown in the figure above, add a voltage probe to track the output by clicking on the **Voltage/Level Marker** on the PSpice Toolbar, as follows:

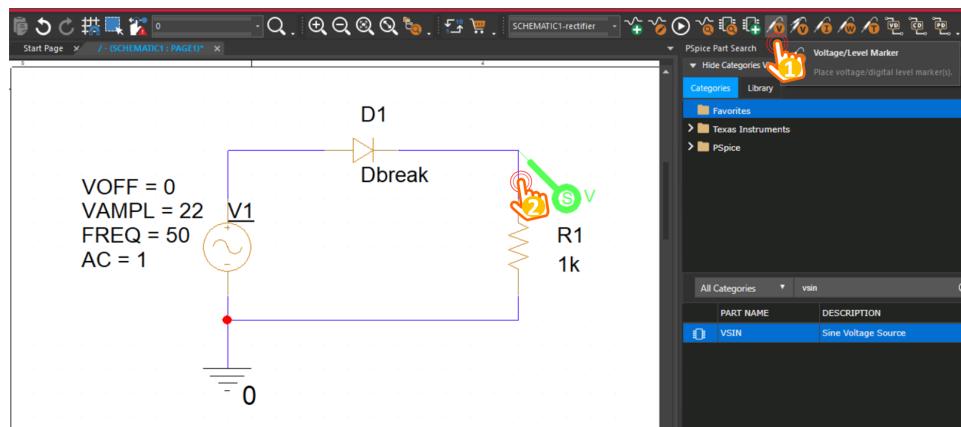


Figure 1.36: Add a voltage marker for simulation tracing

Step 2: Create a new simulation profile. In order to generate the output signal of the voltage probe, the analysis type is set to **Time Domain (Transient)**. Moreover, due to the frequency of the power source is 50Hz, the **simulation time is set to 100ms**, to depict 10 cycles of the output signal. Finally, the resolution is set to 0.1ms in the **Maximum Step size**. The configuration windows for this simulation profile is presented as follows:

Step 3: Run the simulation and observe the results Finally, run the simulation and the output on the simulation windows should be presented in the figure bellow:

3.6.1 Theory calculation

Notes:

Explanations, formulas, and equations are expected rather than only results.

Approximation: Diodes have $V_f = 0.78V$

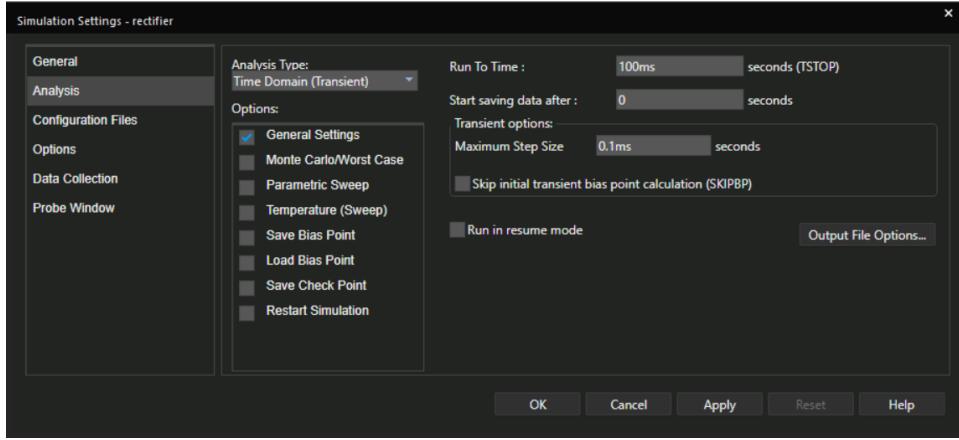


Figure 1.37: Create Time Domain simulation profile

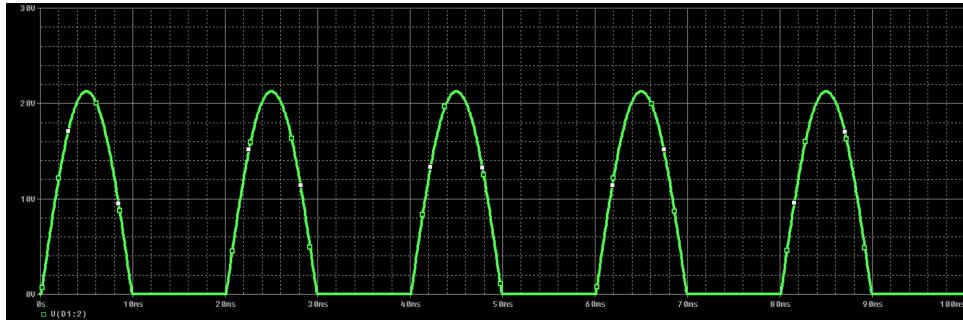


Figure 1.38: Wave form for the half-wave rectifier circuit

Half-wave Rectifiers transforms AC to DC only on the positive half-cycle so we have:

- Maximum Value of $V_{R1} = V_m - V_f = 22 - 0.78 = 21.22(V)$
- Minimum Value of $V_{R1} = 0 (V)$
- Duration (millisecond) for a cycle of V_{R1} : $T_{R1} = \frac{T}{2} = \frac{f}{2} = \frac{50}{2} = 10(ms)$

3.6.2 PSpice simulation

Export your simulation results to Notepad for instance, and find the minimum and the maximum point of the output voltage, then fill your answer to the section bellow:

- Minimum Value of $V_{R1} = -1.0276(\mu V) \approx 0(V)$
- Maximum Value of $V_{R1} = 21.263(V)$
- Duration (millisecond) for a cycle of $V_{R1} = 10(ms)$

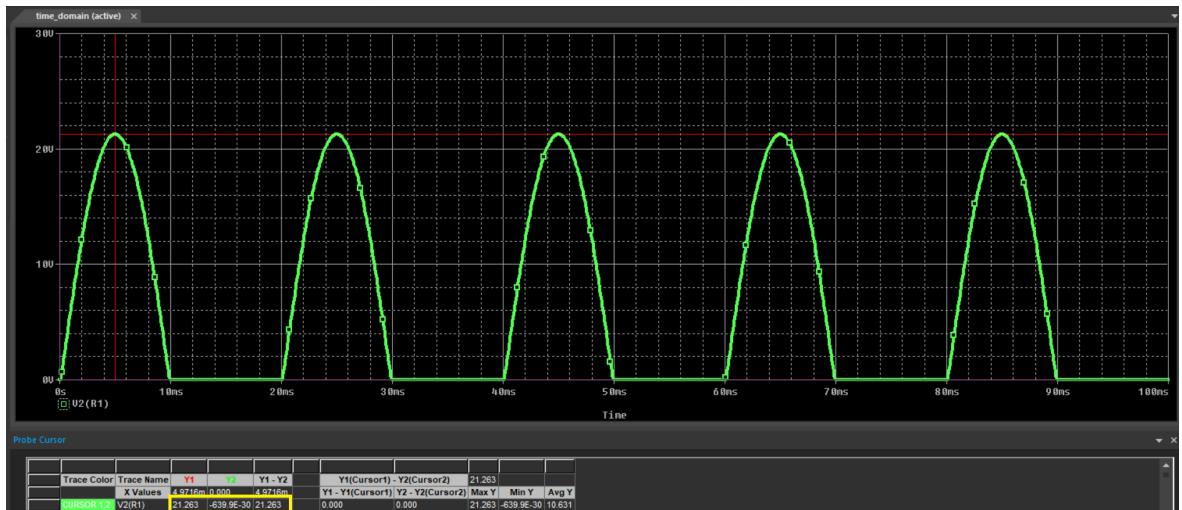


Figure 1.39: Maximum Value of V_{R1} .

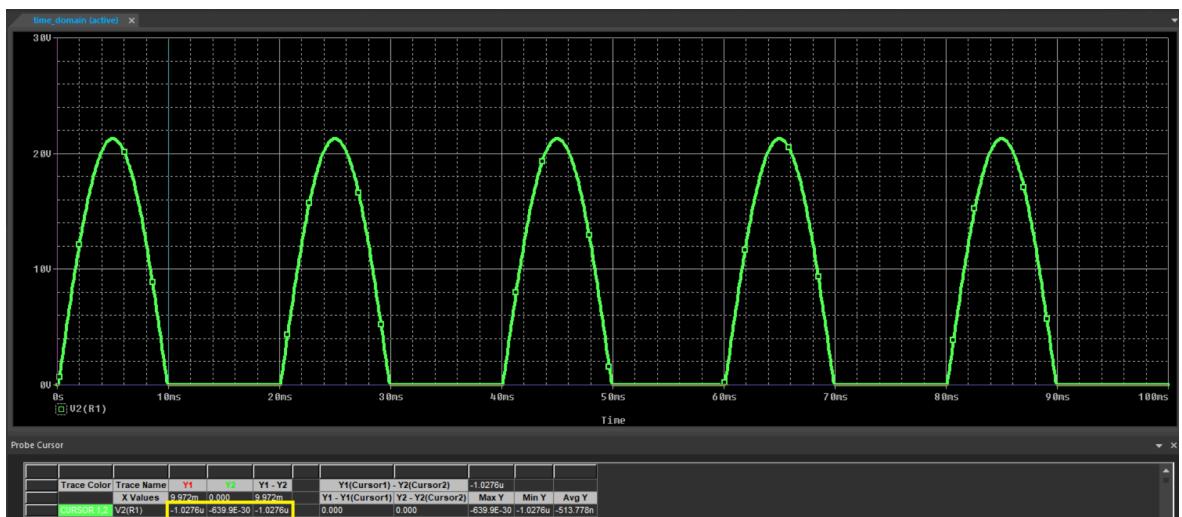


Figure 1.40: Minimum Value of V_{R1} .

The tracking point can also be used directly on the simulation output windows, by the **Toggle Cursor** option on the toolbar. A Probe Cursor window is opened to update a tracking point.

3.7 Full-wave Rectifier

The following circuit is known as a full-wave bridge diode rectifier. Given that the transformer has the ratio $N_1/N_2 = 10$. Write the voltage difference equation V_{AB} and V_{CD} . After that, perform a time-domain (transient) analysis to check the equation you've written.

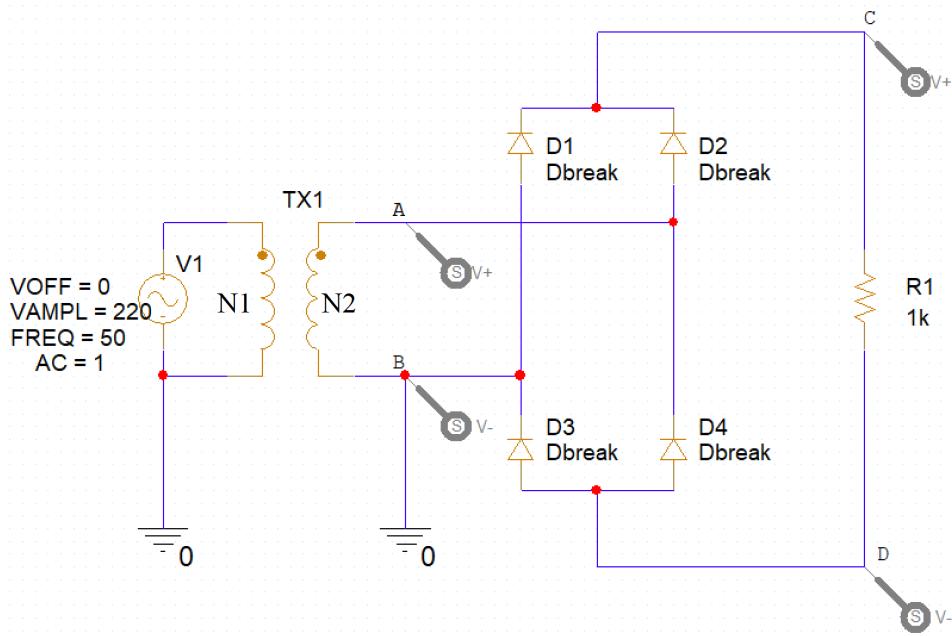


Figure 1.41: Full-wave bridge rectifier

Tips 1: To place the component **transformer**:

- **Step 1:** Go to **Place > PSpice Component > Modeling Application...**
- **Step 2:** Browse for the **Transformer** component under the **System Modules** category as shown in the following Figure.

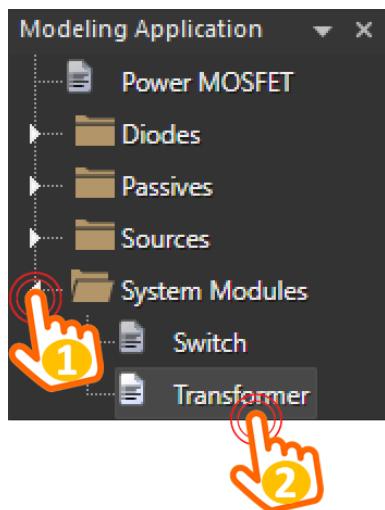


Figure 1.42: Browse for the component **Transformer**

- **Step 3:** Set the transformation ratio before placing as shown below.

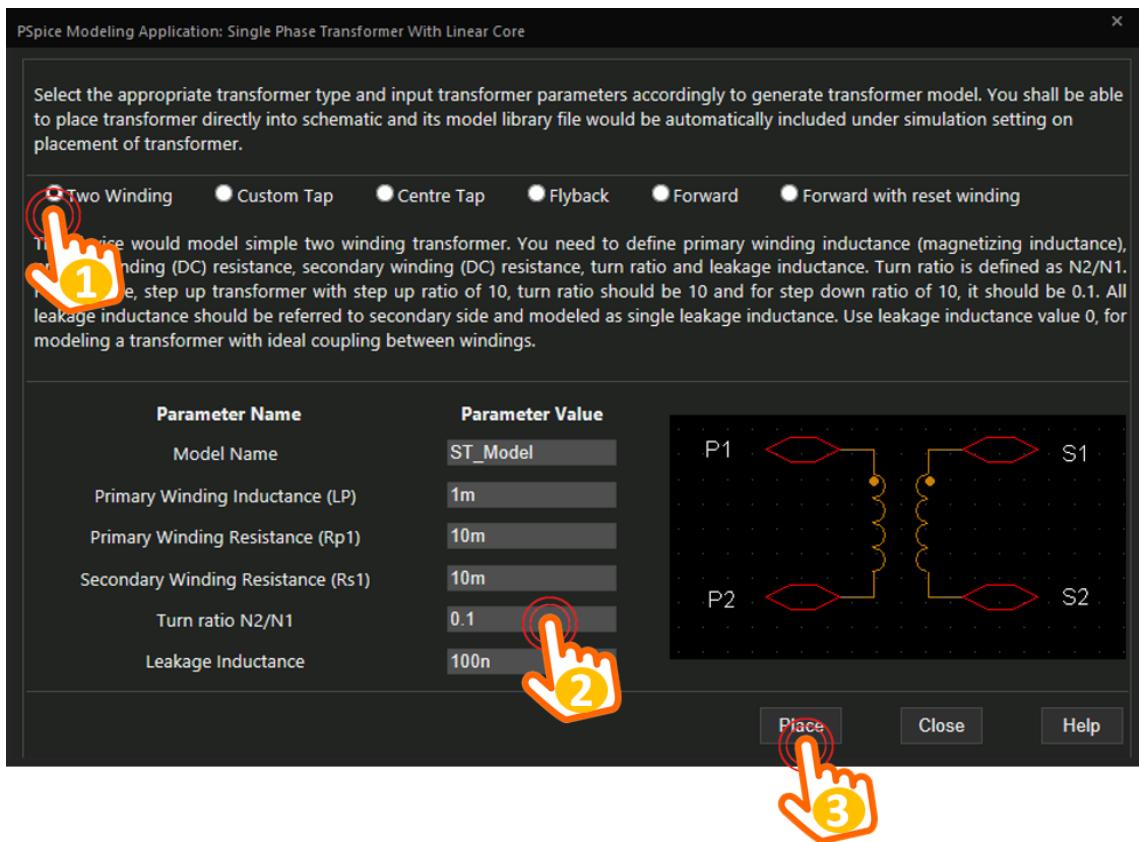


Figure 1.43: Setting the transformation ratio before placing the transformer

Tips 2: The Voltage Differential Markers in PSPICE for TI:



Figure 1.44: The Voltage Differential Markers in PSPICE for TI

3.7.1 Theory calculation

Notes:

Explanations, formulas, and equations are expected rather than only results.

Approximation: Diodes have $V_f = 0.7V$

$$\text{We have: } \frac{V_1}{V_2} = \frac{N1}{N2} \Rightarrow V_2 = \frac{V1 \times N2}{N1} = \frac{220}{10} = 22(V) \quad V_{AB} = V_2 = 22(V)$$

During positive half-cycle, the current will go on the path containing D1,D4 so D2,D3 are reverse-biased and conversely in negative half-cycle. Therefore, during the whole cycle there will be only 2 diodes are active.

$$\Rightarrow V_{CD} = V_{AB} - 2 \times V_f = 22 - 2 \times 0.7 = 20.6(V)$$

3.7.2 Simulation

The sinusoidal waveform of the voltage difference V_{AB} has the period $T = 20(ms)$

If we want to perform the transient analysis in 10 periods of the waveform V_{AB} , the required time would be:200(ms)

If we want the sampling rate to be as ten times higher than the frequency of the sinusoidal voltage difference V_{AB} , the time interval between two consecutive sampling time points should be:1(ms)

Now, create a simulation profile as guided in the **Exercise 3.6** and perform your simulation to check it out!

Simulation result: Show both V_{AB} and V_{CD} in a single plot window to illustrate the relationship between them. Don't forget to use the cursors to prove that the equation you've written is correct.

(Your image(s) goes here)

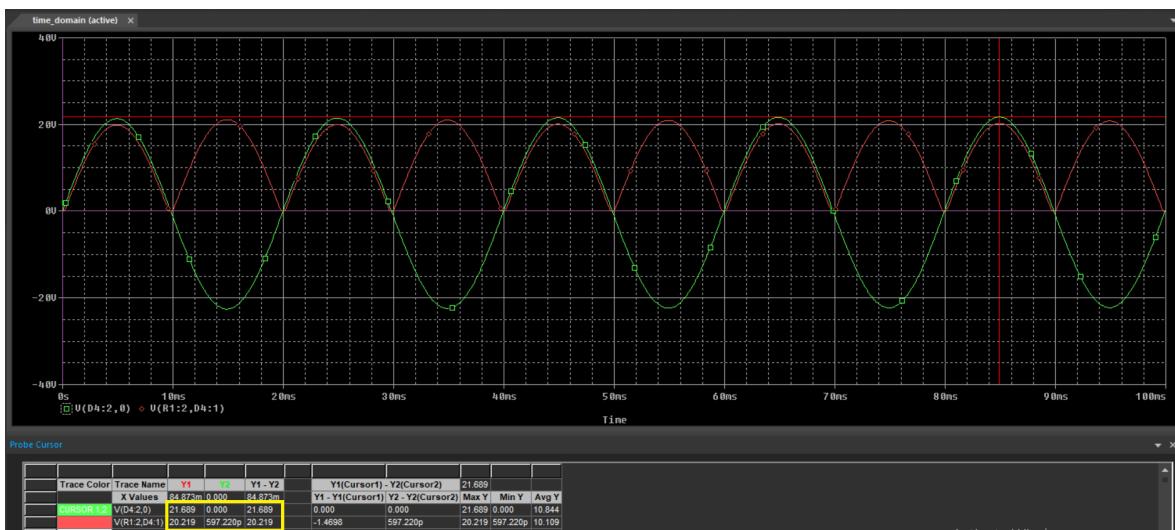


Figure 1.45: The time-domain simulation.

3.8 Zener Diode as a Regulator

The Zener diode has a well-defined reverse-breakdown voltage, at which it starts conducting current, and continues operating continuously in the reverse-bias mode without getting damaged. Additionally, the voltage drop across the diode remains constant over a wide range of voltages, a feature that makes Zener diodes suitable for use in voltage regulation.

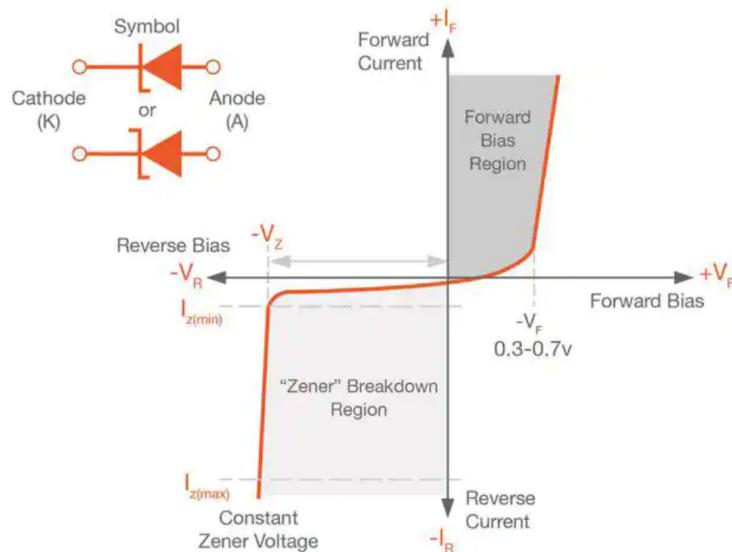


Figure 1.46: Electrical characteristic of Zener diode

In this exercise, a Zener diode is used to design a voltage regular circuit. The schematic in this exercise is given following:

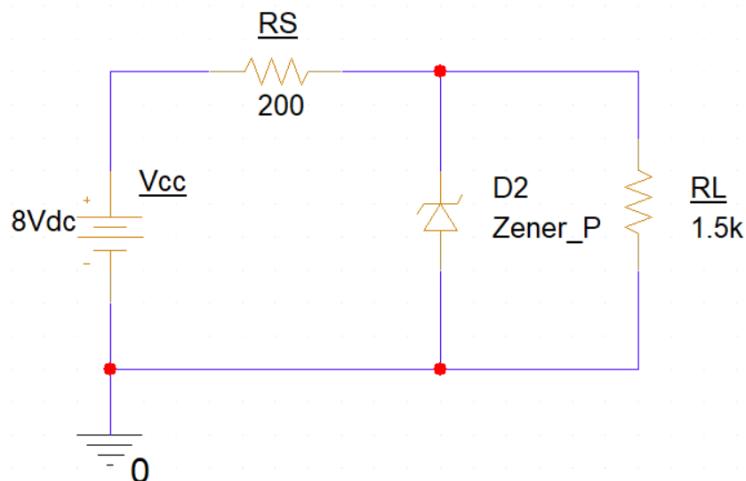


Figure 1.47: Voltage regulator using Zener diode

The Zener component in the circuit can be found in the Favourites list by search the keyword **Zener**. The full name of the component used in the circuit above is **Zenner_P - Zener Diode (parameterized)**. The default Zener voltage of this component is $V_Z = 5V$. However, this value can be changed in the properties of the component (right click and select Edit Properties) for other simulations.

For theory calculation, students are supposed to provide equations for these values

- $I_L = \frac{V_Z}{R_L} = \frac{5}{1500} = 3.33(mA)$
- $I_S = \frac{V_{source} - V_Z}{R_S} = \frac{8 - 5}{200} = 15(mA)$
- $I_Z = I_S - I_L = 11.67(mA)$
- $P_{RS} = R_S \times I_S^2 = 45(mW)$
- $P_Z = V_Z \times I_Z = 58.35(mW)$

Then, perform the calculation for the Zener diode voltage regulator with two different input voltage, including 8V and 12V power supply. Finally, run the simulations in PSpice (in **Bias Point** simulation profile) to confirm with the theory calculation.

The results are summarized in the table bellow.

	Theory Calculation						PSpice Simulation					
	I_S	I_L	I_Z	V_L	P_{RS}	P_Z	I_S	I_L	I_Z	V_L	P_{RS}	P_Z
Vcc = 8V	15(mA)	3.33(mA)	11.67(mA)	4.995(V)	45(mW)	58.35(mW)	15.04(mA)	3.329(mA)	11.71(mA)	4.993(V)	45.21(mW)	58.45(mW)
Vcc = 12V	35(mA)	3.33(mA)	31.67(mA)	4.995(V)	245(mW)	158.35(mW)	34.96(mA)	3.329(mA)	31.62(mA)	5.009(V)	244.4(mW)	158.4(mW)

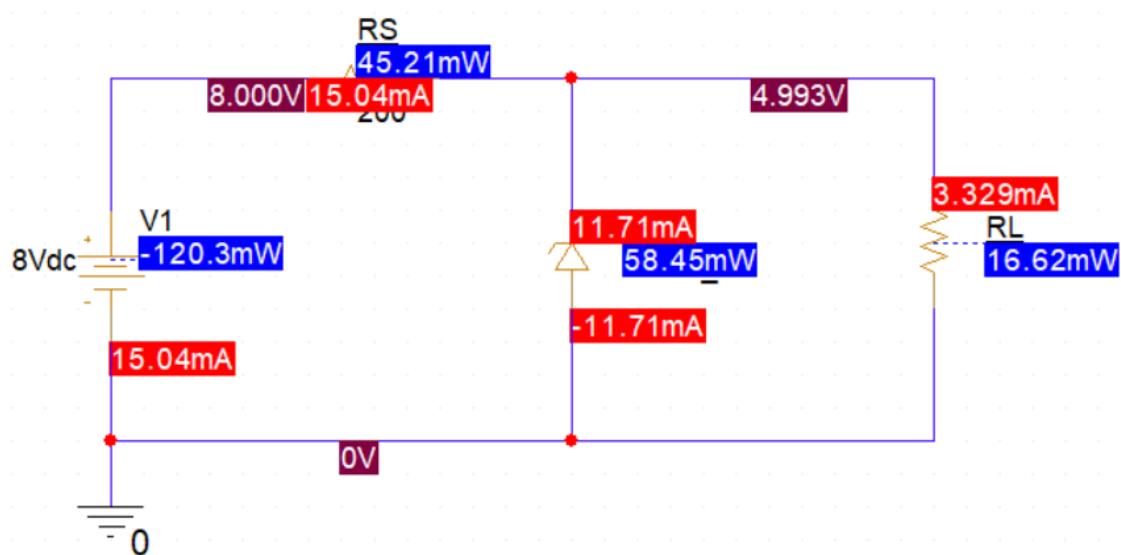


Figure 1.48: The bias point simulation when V=8(V).

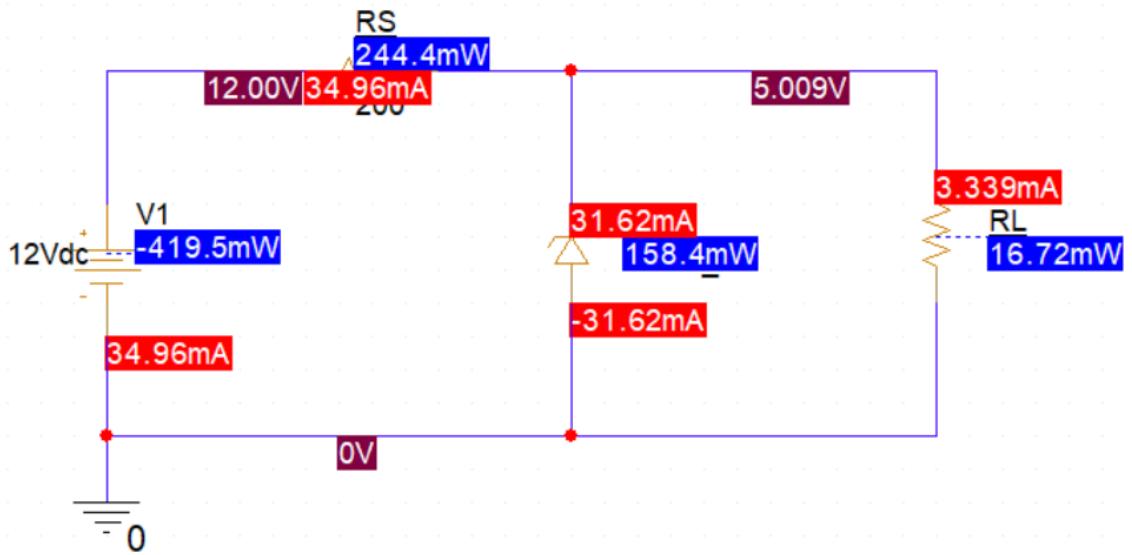


Figure 1.49: The bias point simulation when V=12(V).

3.9 AC/DC Power Circuit Application

In this exercise, we are building step by step an AC to DC voltage source transformation circuit. Students perform a time-domain simulation and write out comments and explanations for each step.

Tips: To place a capacitor, go to **Place > PSpice Component > Capacitor**

- **Step 1:** The rectified voltage without any filtering or being regulated.

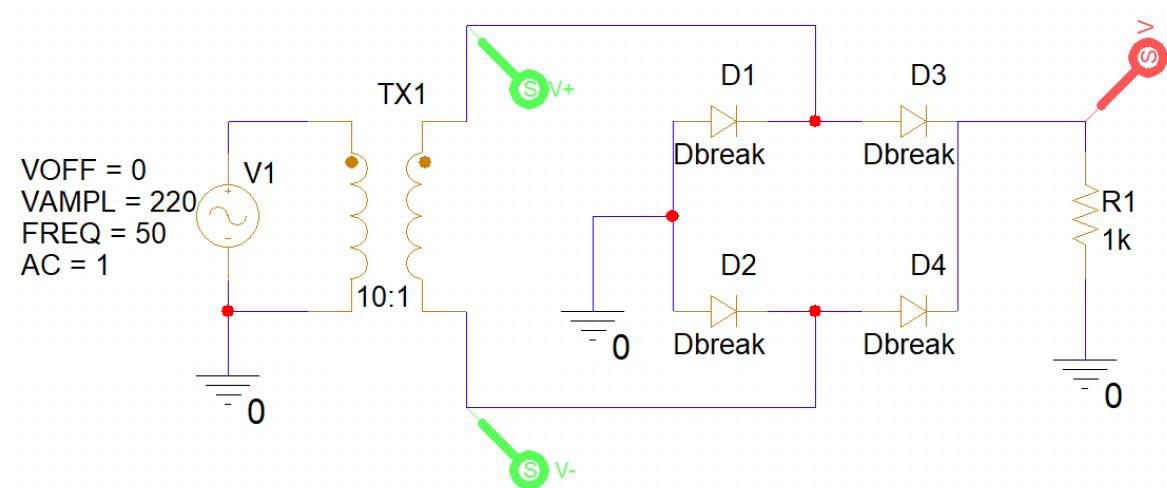


Figure 1.50: The rectified voltage without any filtering or being regulated

Simulation result: Your image goes here

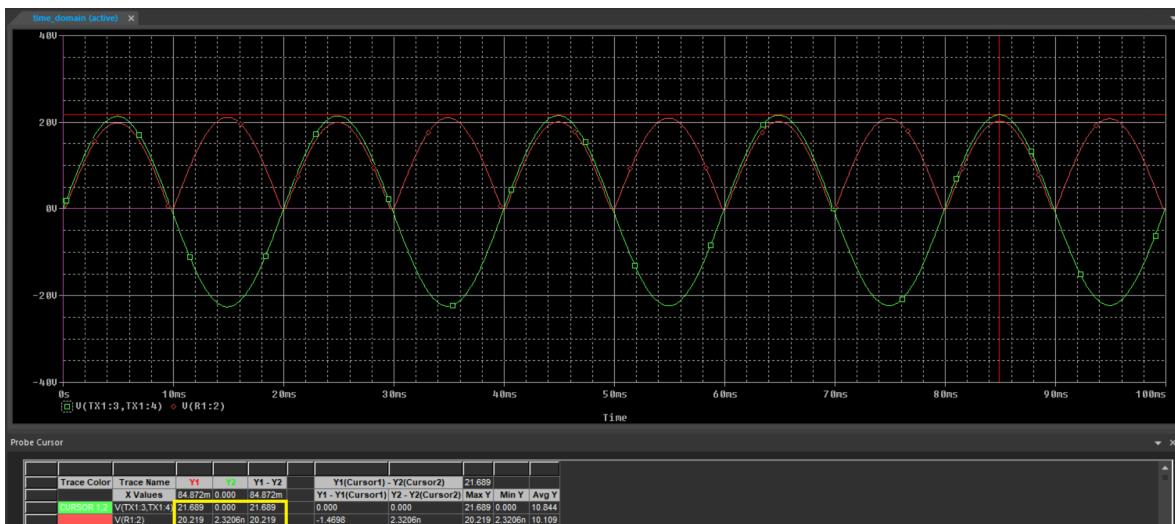


Figure 1.51: The rectified voltage without any filtering or being regulated.

Any comments or explanations: Without any filtering or being regulated, we obtain the wave-form same as which on Exercise 7.

- **Step 2:** Rectified voltage regulated with a $10\mu F$ capacitor.

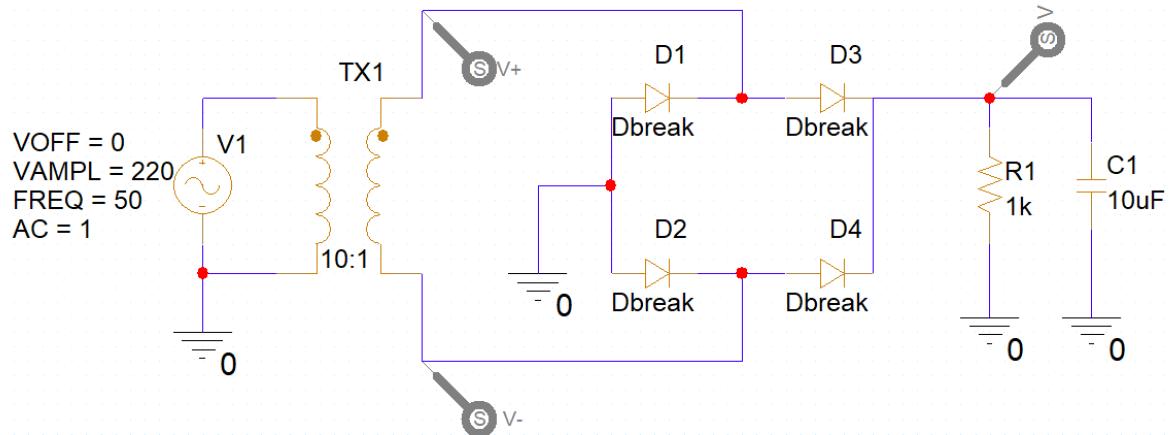


Figure 1.52: Rectified voltage regulated with a capacitor

Simulation result(s): Your image goes here

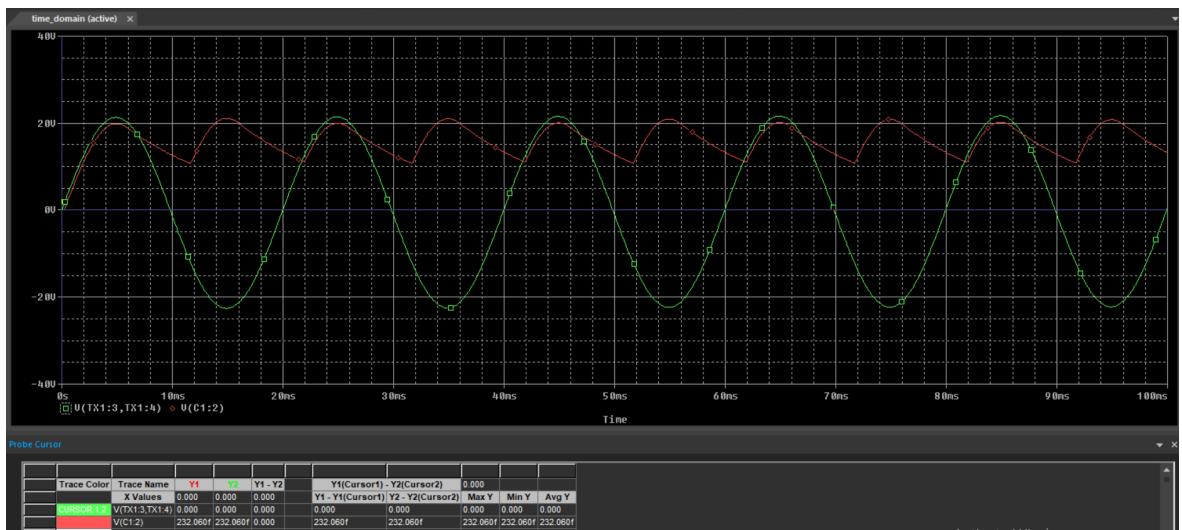


Figure 1.53: Rectified voltage regulated with a $10\mu F$ capacitor.

Any comments or explanations: The present of a $10\mu F$ capacitor has contributed to the converter from AC input voltage to DC output voltage. However, the output are still fluctuated.

- **Step 3:** Replace the $10\mu F$ capacitor with a $680\mu F$ one and re-run the simulation, recognize the change in the result and explain.

Simulation result(s): Your image goes here

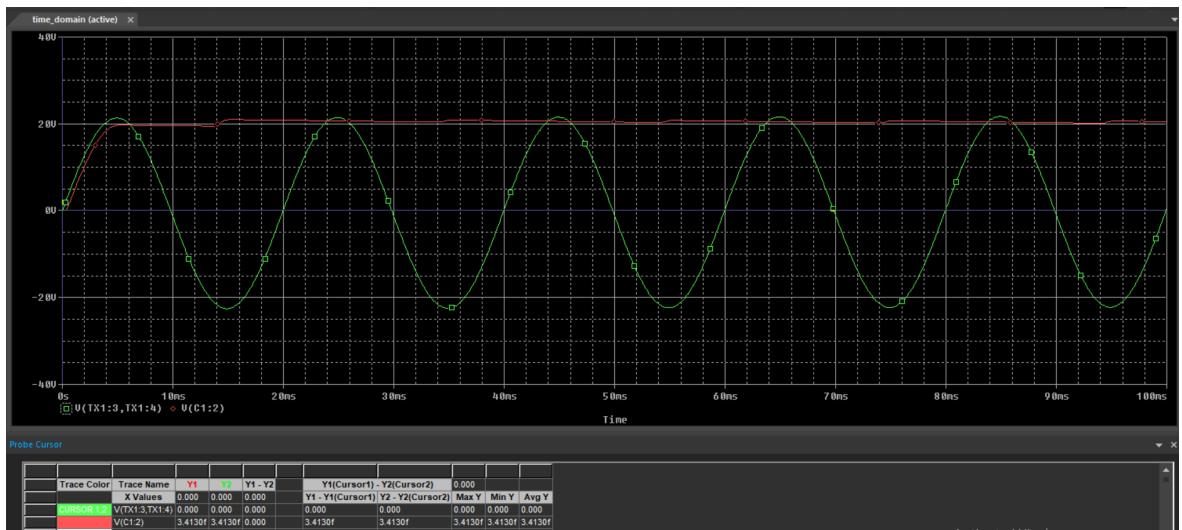


Figure 1.54: Rectified voltage regulated with a $680\mu F$ capacitor.

Any comments or explanations: The significant increase in the capacitance has made the output voltage's wave-form more stable but there are still some small ripples over the time.

- **Step 4:** Add a zener diode as in Figure 1.55 with the *zener voltage* properties set to 22 volt then simulate the circuit and comment or explain the result.

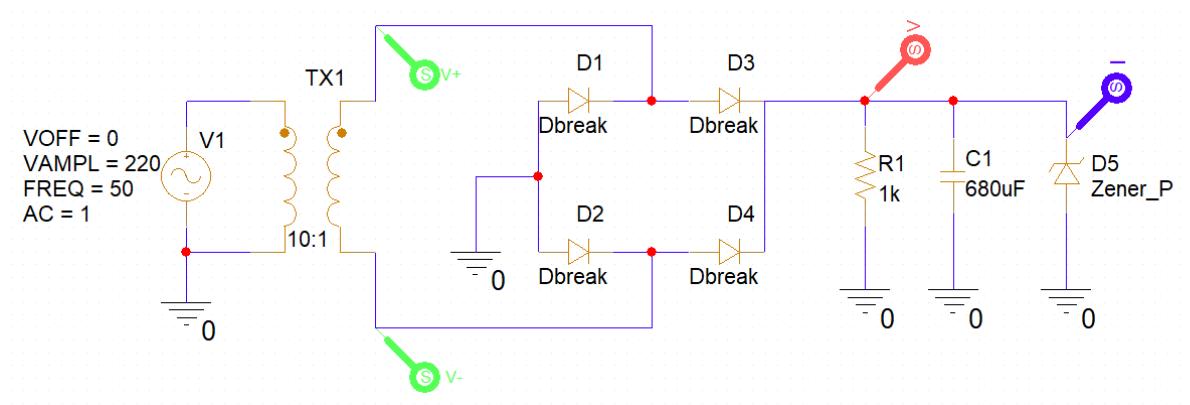


Figure 1.55: Rectified voltage regulated with a capacitor and a zener diode

Simulation result(s): Your image goes here

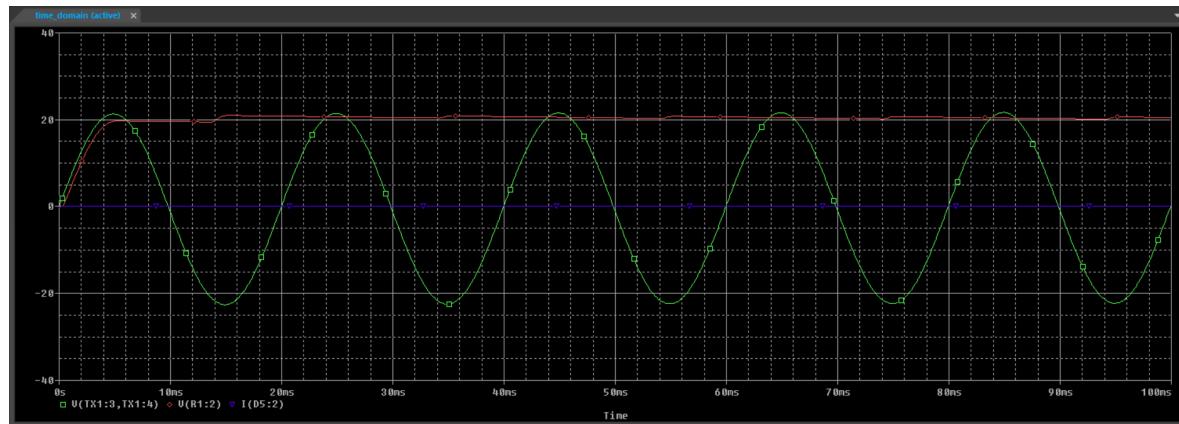


Figure 1.56: Rectified voltage regulated with a capacitor and a zener diode.

Any comments or explanations: The 22V Zener diode helps reducing the ripples.

- **Step 5:** Change the *zener voltage* properties of the zener diode to 20 voltage and then re-run the simulation. Comment and explain any changes in the result.

Simulation result(s): Your image goes here

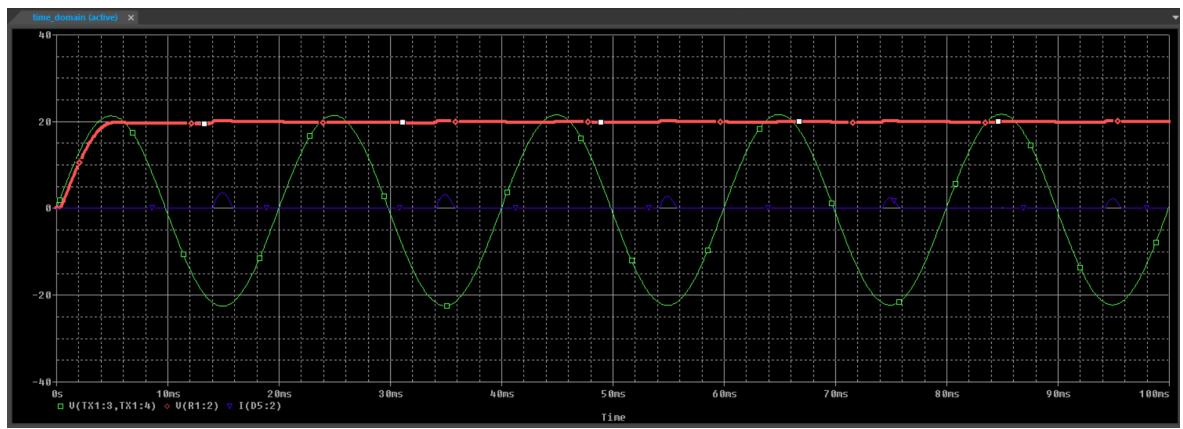


Figure 1.57: Rectified voltage regulated with a capacitor and a Zener diode.

Any comments or explanations: Decreasing the voltage of Zener diode makes the wave-form smoother without ripples.

3.10 Exercise 8: AC/DC Power Circuit Application With LM2596_5P0_TRANS

Figure 1.58 describes an incomplete Texas Instrument LM2596 - 5.0 Switching Power Supply circuit. It lacks a Zener diode voltage regulator and an inductor reducing the voltage variation. At first, let perform a time-domain (transient) simulation with this incomplete circuit and figure out the problem with the output voltage (the voltage marker at R1).

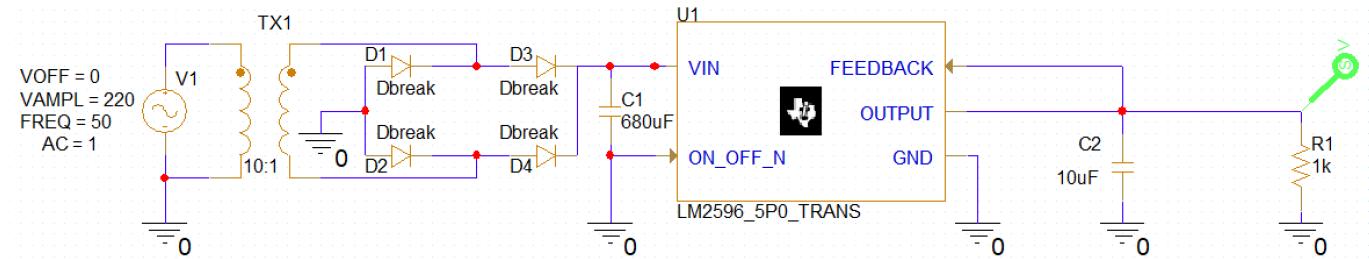


Figure 1.58: Incomplete switching power supply circuit

Tips:

To place the switching power supply IC LM2596 - 5.0, go to **Place > PSpice Component... > Search...** then search for *LM2596_5P0_TRANS*.

But before you can perform a transient simulation and analysis with this circuit, we will need to pay attention to some minor settings on the simulation profile.

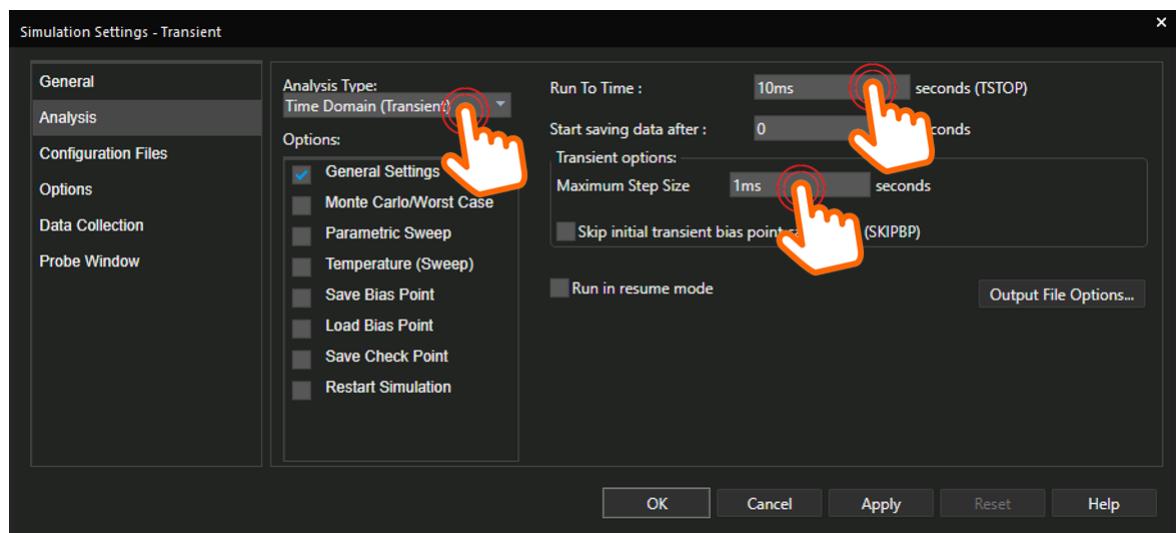


Figure 1.59: Set the transient simulation duration

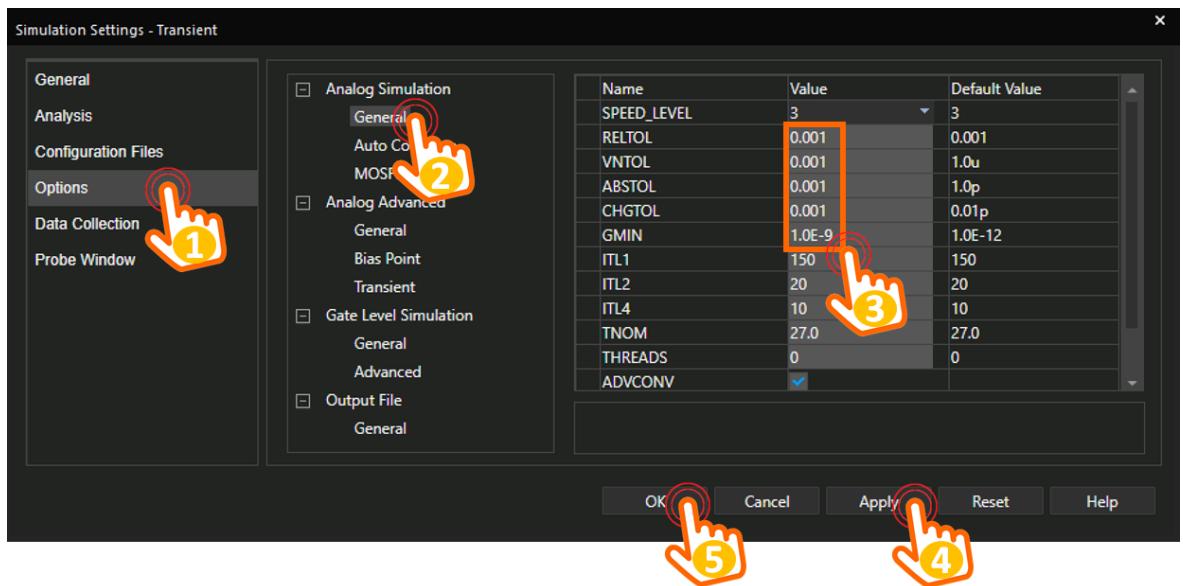


Figure 1.60: Set the transient calculation accuracy

Simulation result(s): Your image goes here

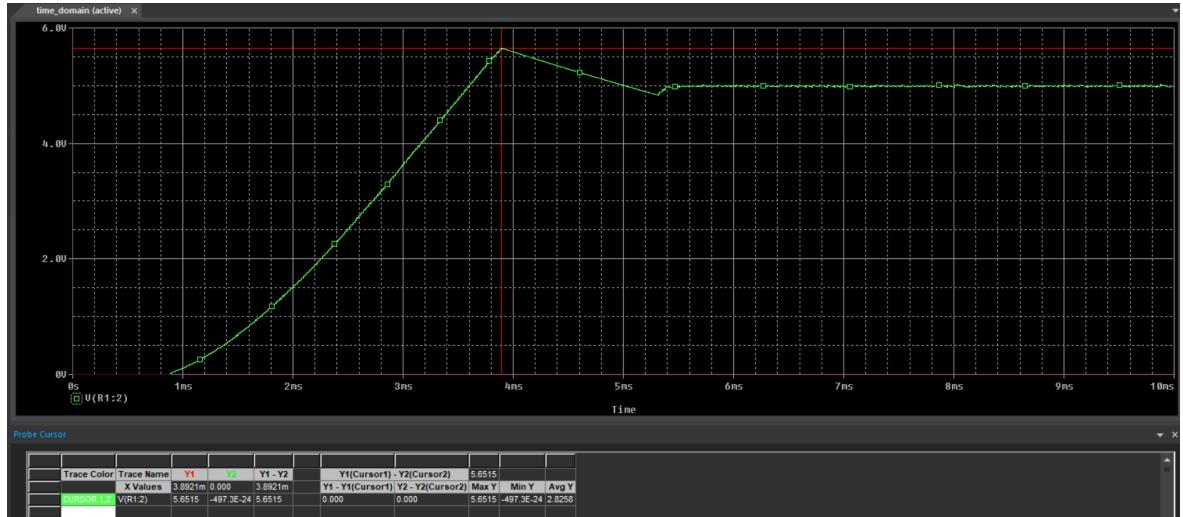


Figure 1.61: Incomplete switching power supply circuit.

Any comments or explanations: The DC output voltage increases slowly and takes nearly 3 periods to reach its peak. Then, it needs more than 1 periods to fall back before being stable for the rest of the time.

Next, add an inductor $33\mu H$ to the circuit as shown in Figure 1.62 then re-run the simulation and explain any improvements.

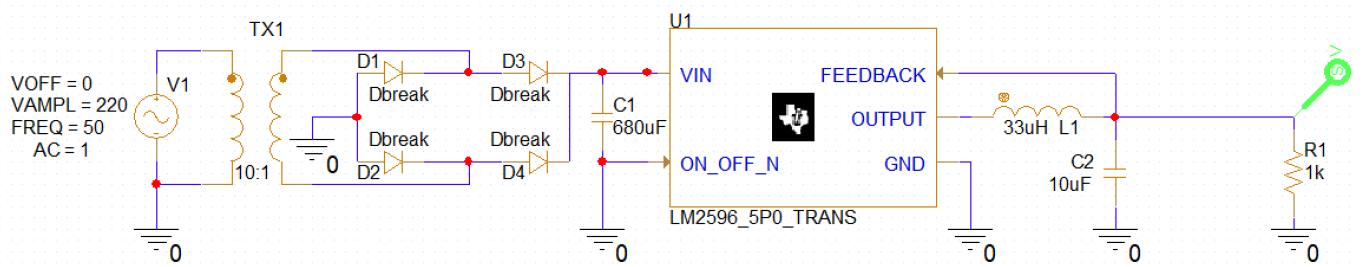


Figure 1.62: A 33 μH inductor added to the circuit

Simulation result(s): Your image goes here

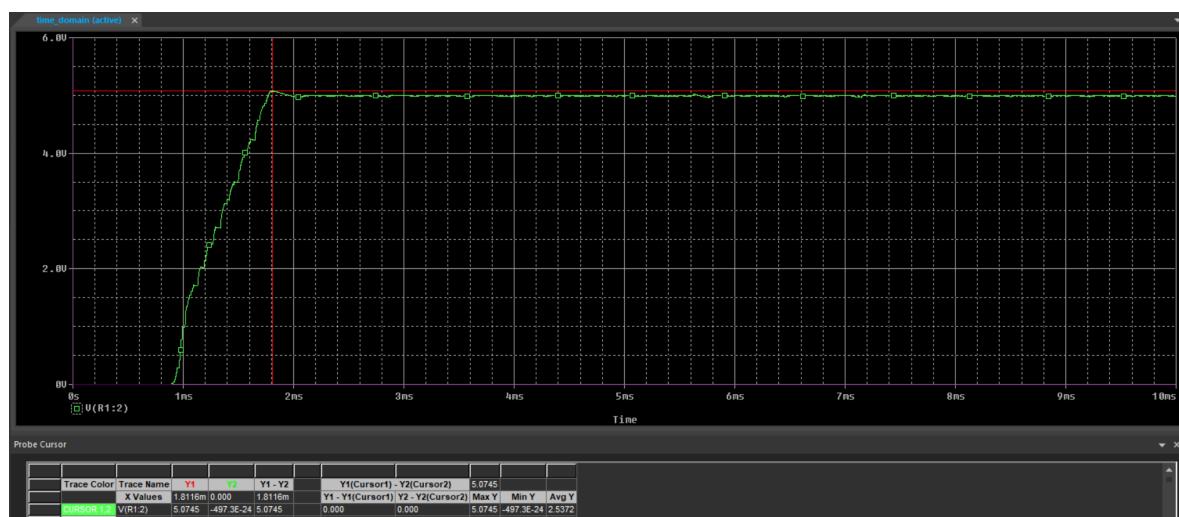


Figure 1.63: Adding inductor to the circuit.

Any comments or explanations: It is obvious that the DC output voltage only need 1 period of time to reach the maximum value which is 5.07 (V), a lot lower than 5.65 (V) in the previous stage. However there is still a quick decrease about 0.2 (ms) before it becomes steady.

Continue, add a 5V Zener diode to the circuit as shown in Figure 1.64, change the capacitor to 220 μF , add a current marker to the Zener diode, re-run the simulation and explain the role of the Zener diode in the circuit.

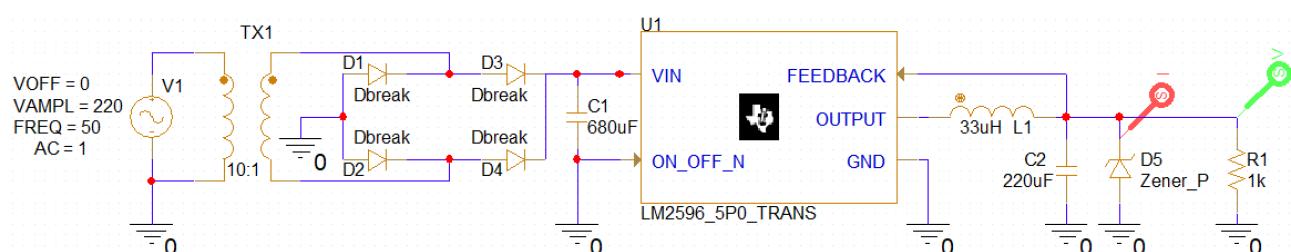


Figure 1.64: A 5V Zener diode added to the circuit

Simulation result(s): Your image goes here

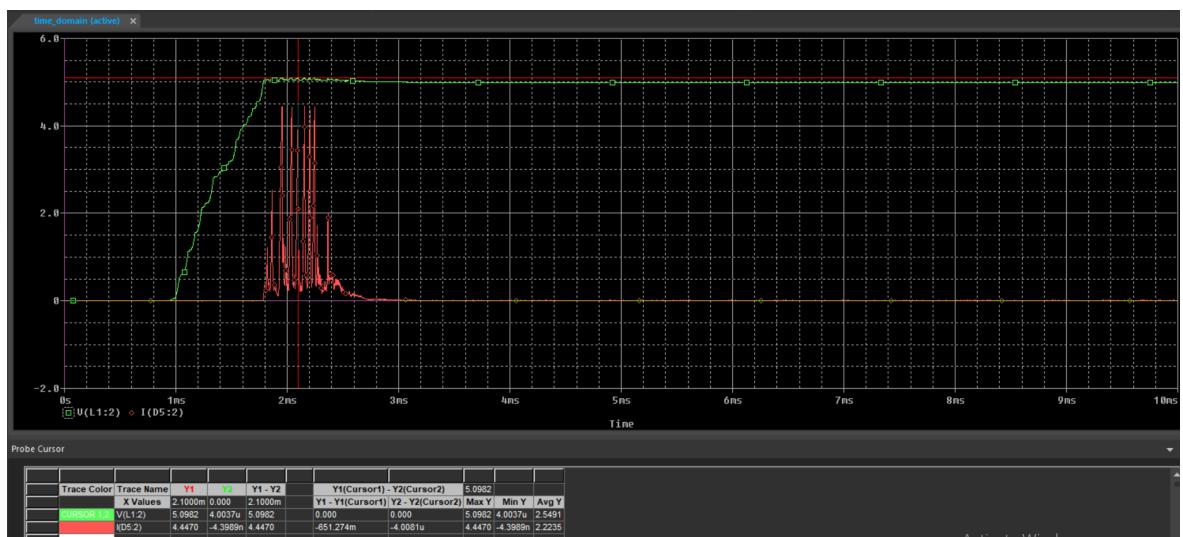


Figure 1.65: Adding a 5V Zener diode to the circuit.

Any comments or explanations: The Zener diode make the DC output voltage rise significantly to its maximum value which is also a stable level and immediately remain unchanged.

This is the consequence of the property of the Zener diode. It is designed to maintain a fixed voltage across itself when in reverse biased. This probability is used to provide known reference voltages and also to clip or limit wave-forms preventing them from exceeding voltage limits.

*****THE END*****