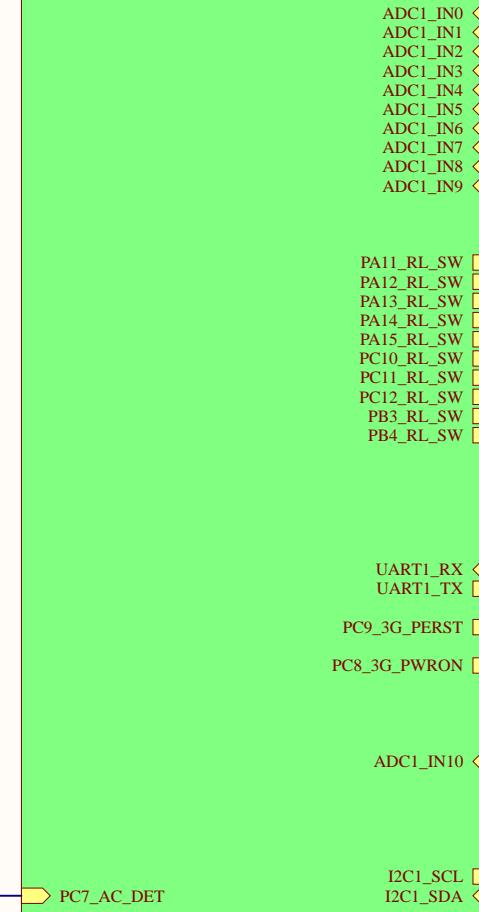
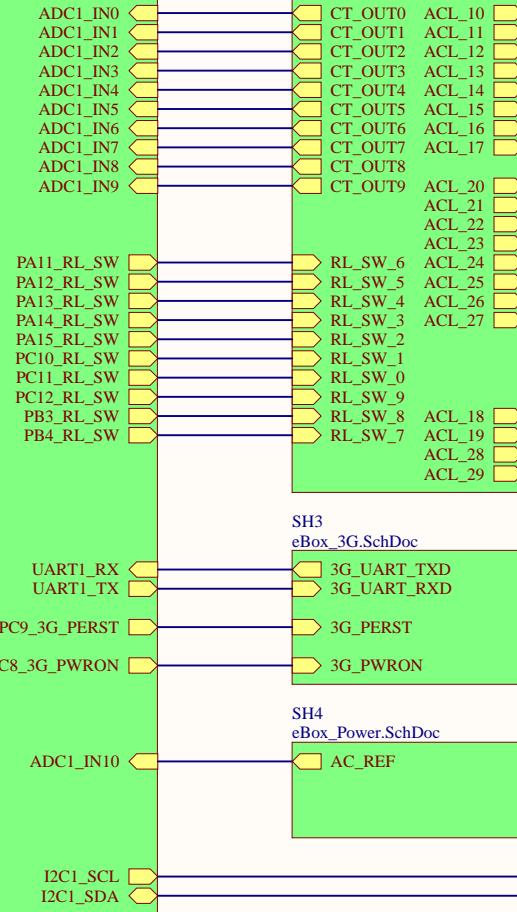
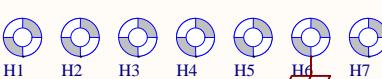
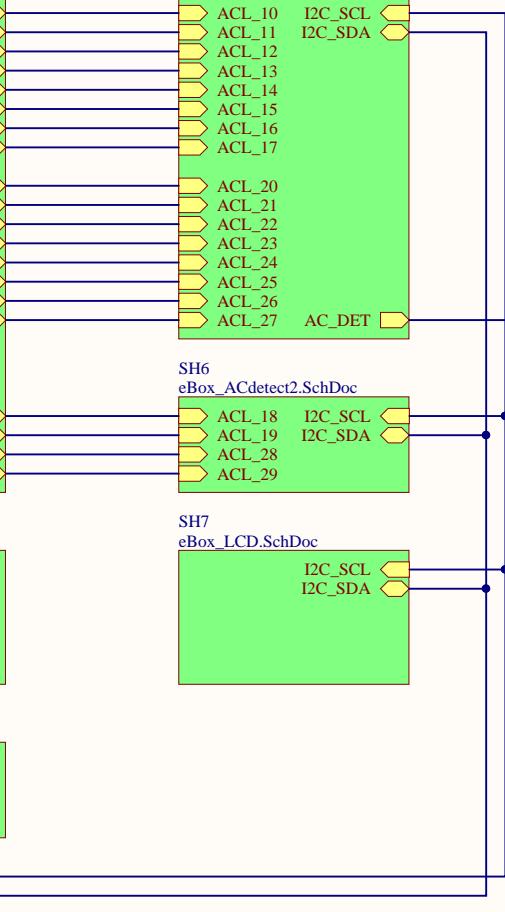
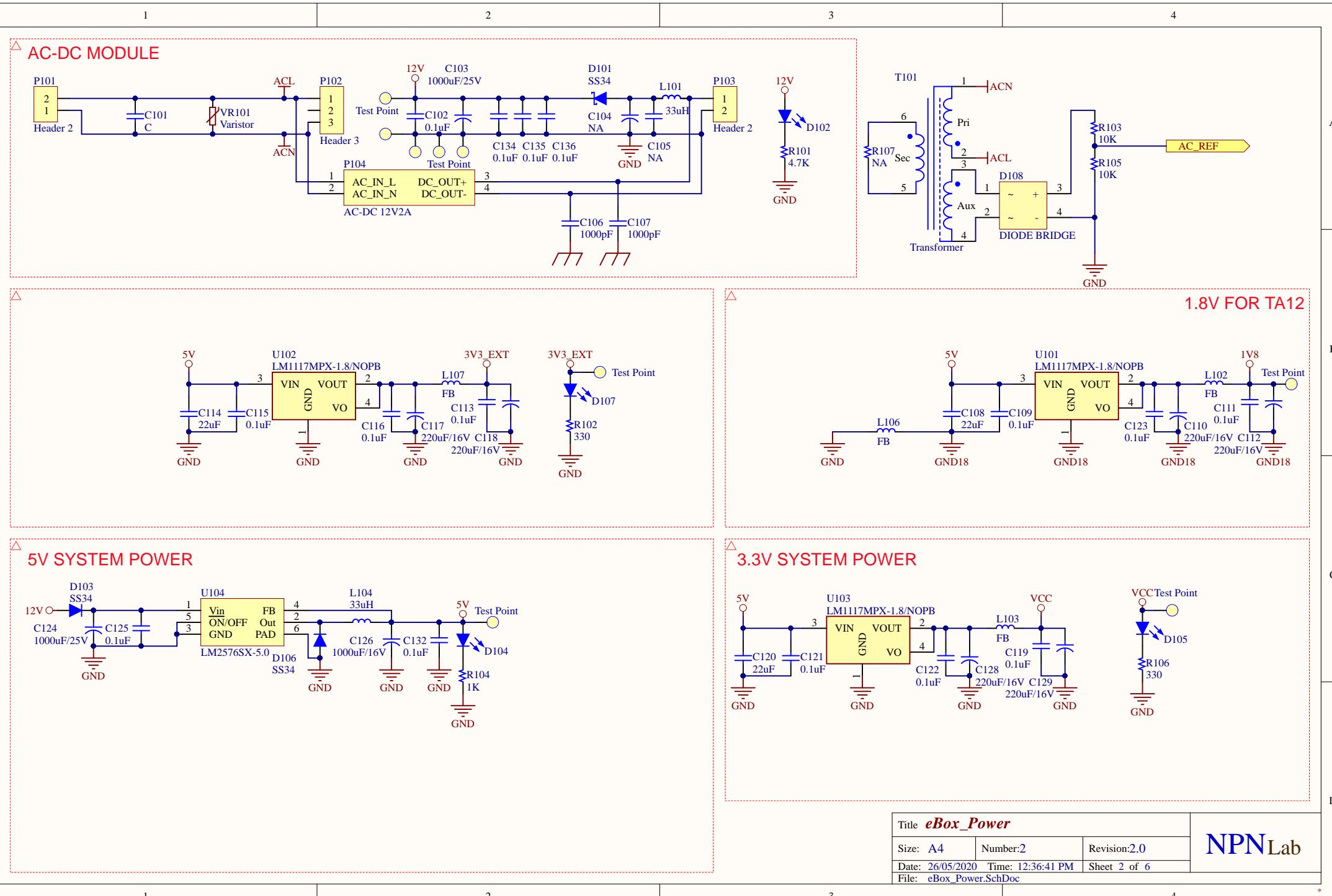


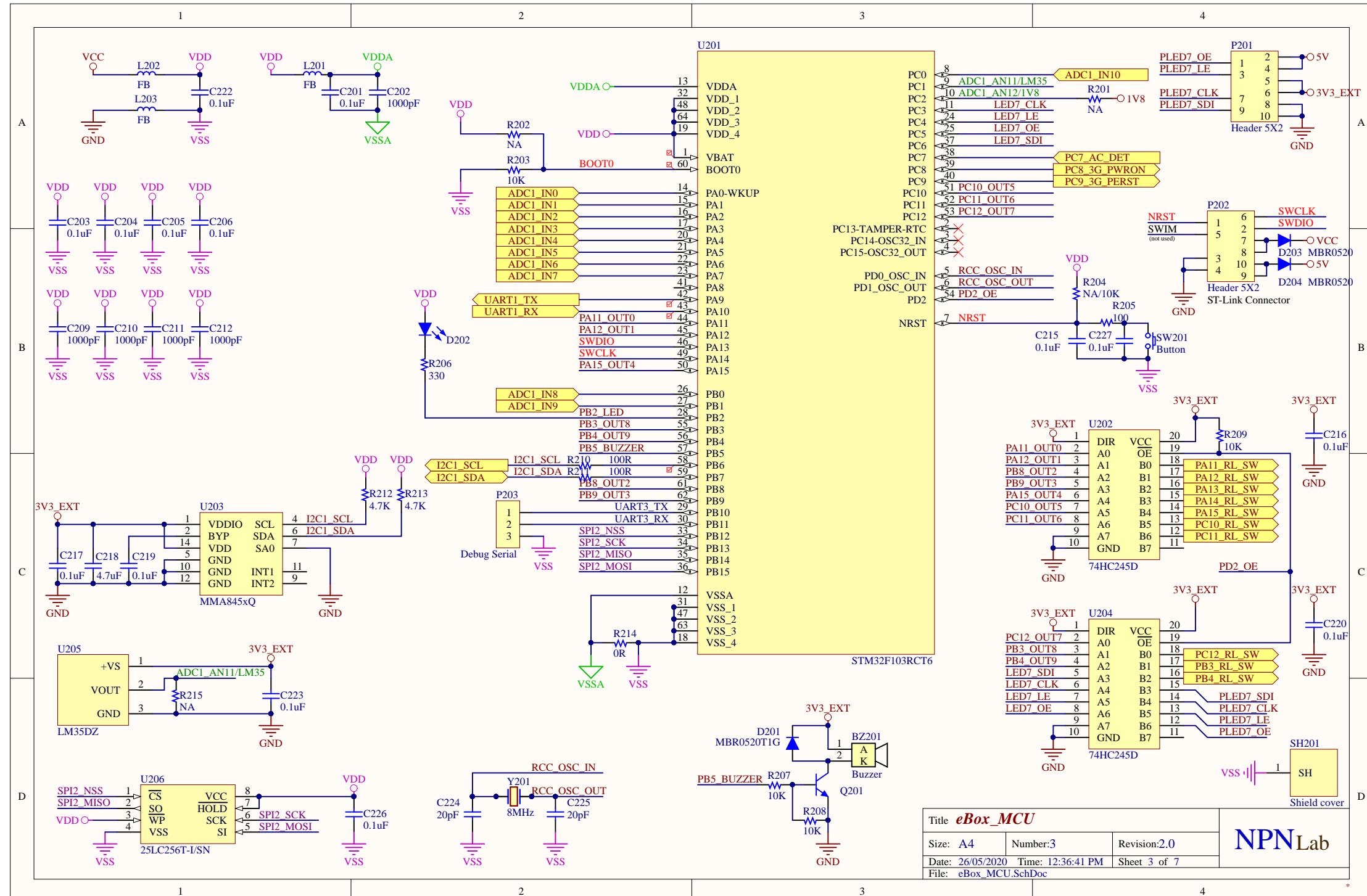
History		
Date (d/m/y)	Version	Note
02/12/2019	0.1	Draft version
04/12/2019	0.2	- Add resistors for CTs - Add 1.8V power for CTs - Add buffer ICs for MCU - Replace single transistors by darlington transistors array. - Fixbug
07/12/2019	0.3	- Add LED signal for relay - Change CTs - Add feedback signals from AC ports - Add WAKE-UP pin for 3G module
09/12/2019	0.4	- Add Buzzer - Add PCB document - Fixbug
12/12/2019	0.5	[x] Thêm chân nạp cho STM32 [x] Chính số cho nguồn 3G (R1,R2) hoặc recommend cái nào thì tò đậy [x] Thêm test point cho máy chở nguồn 12v, 3v3, 1v8 [x] Đèn led blink trạng thái chân B2 [x] Sim 5320 cũng ra hàng rào hoặc test point uart để test
24/12/2019	0.6	- Add PCB layout
29/12/2019	0.7	- Fix bug
27/02/2020	1.0	-

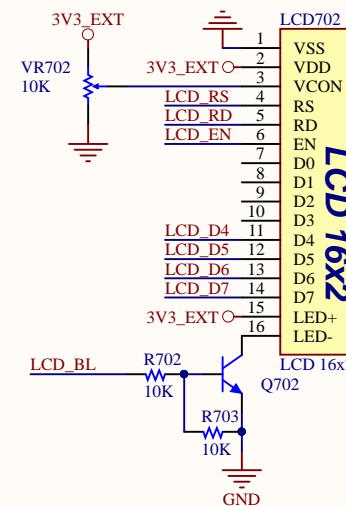
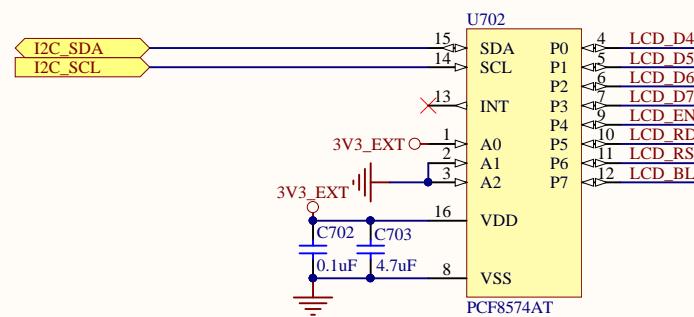
SH1
eBox_MCU.SchDocSH2
eBox_Peripherals.SchDocSH5
eBox_ACdetect.SchDocTitle **eBox_Notes**

Size: A4	Number: 1	Revision: 2.0
Date: 26/05/2020	Time: 12:36:41 PM	Sheet 1 of 7
File: eBox_Notes.SchDoc		

NPN Lab





Title **eBox_LCD**

Size: A4	Number: 4	Revision: 2.0
Date: 26/05/2020	Time: 12:36:41 PM	Sheet 4 of 7
File: eBox_LCD.SchDoc		

NPN Lab

A

A

B

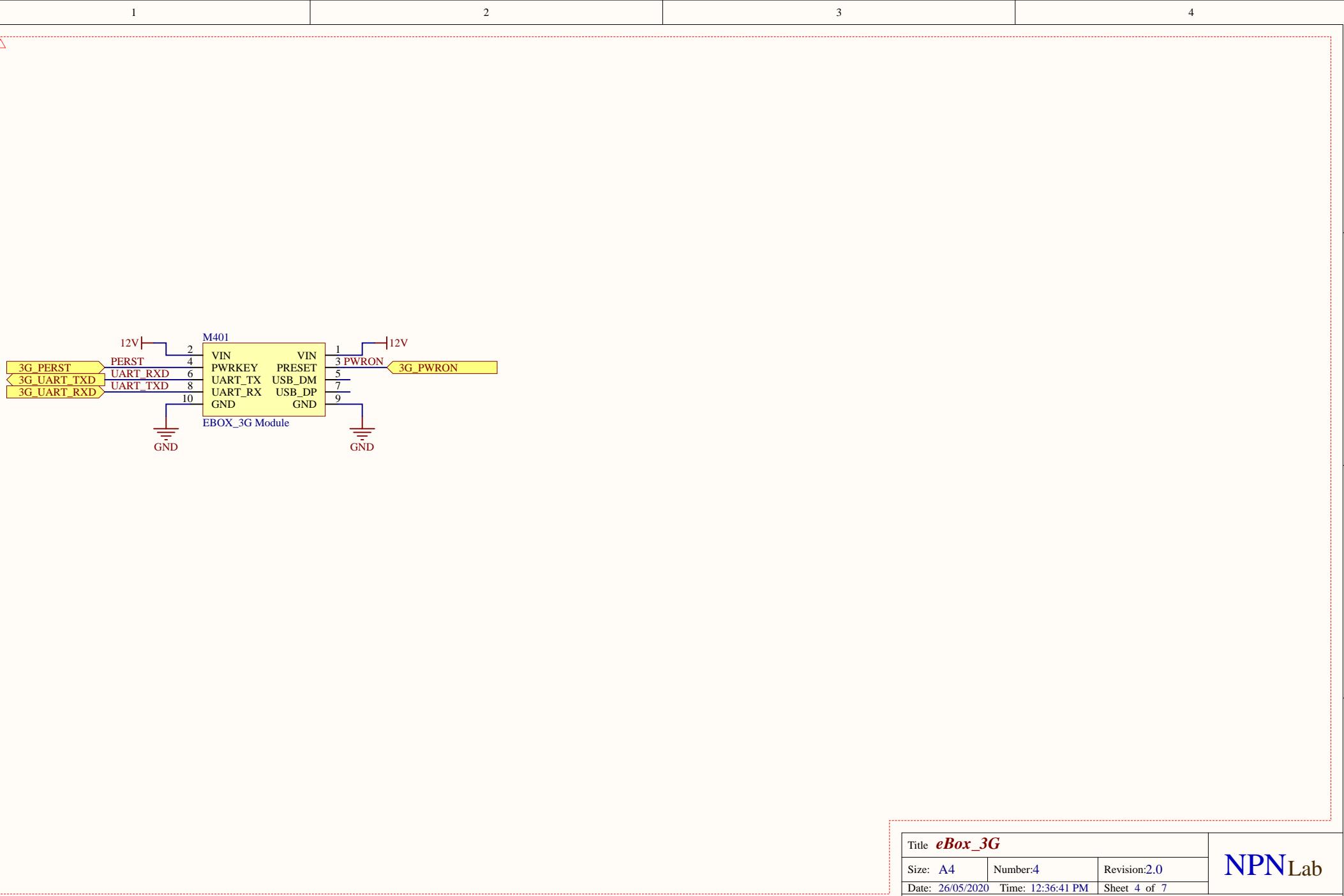
B

C

C

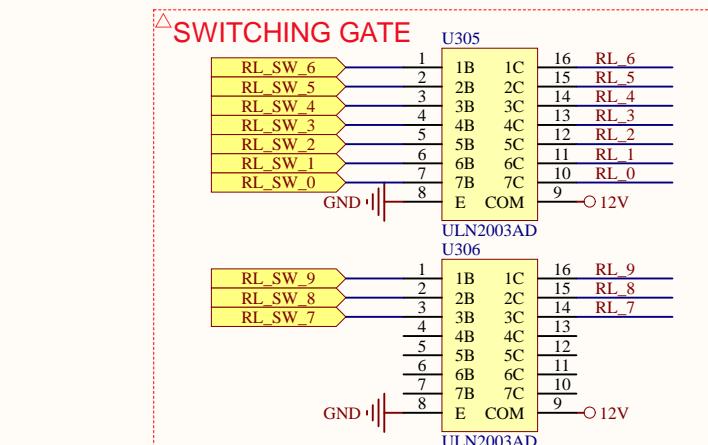
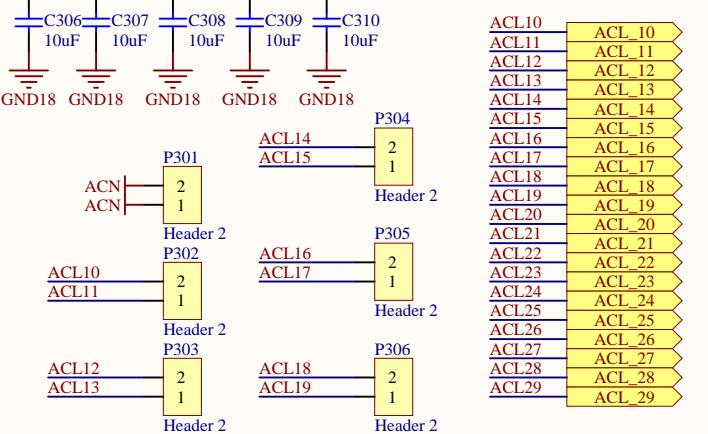
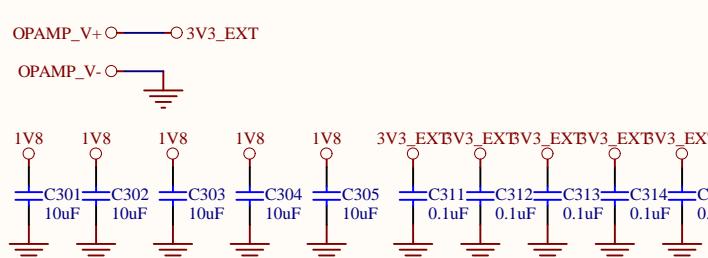
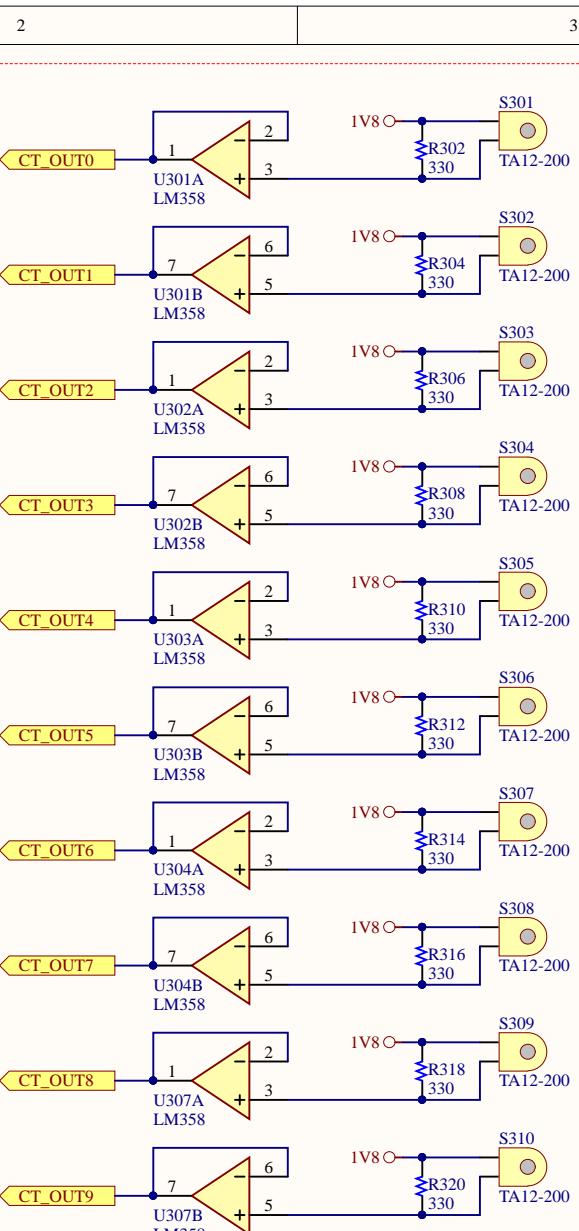
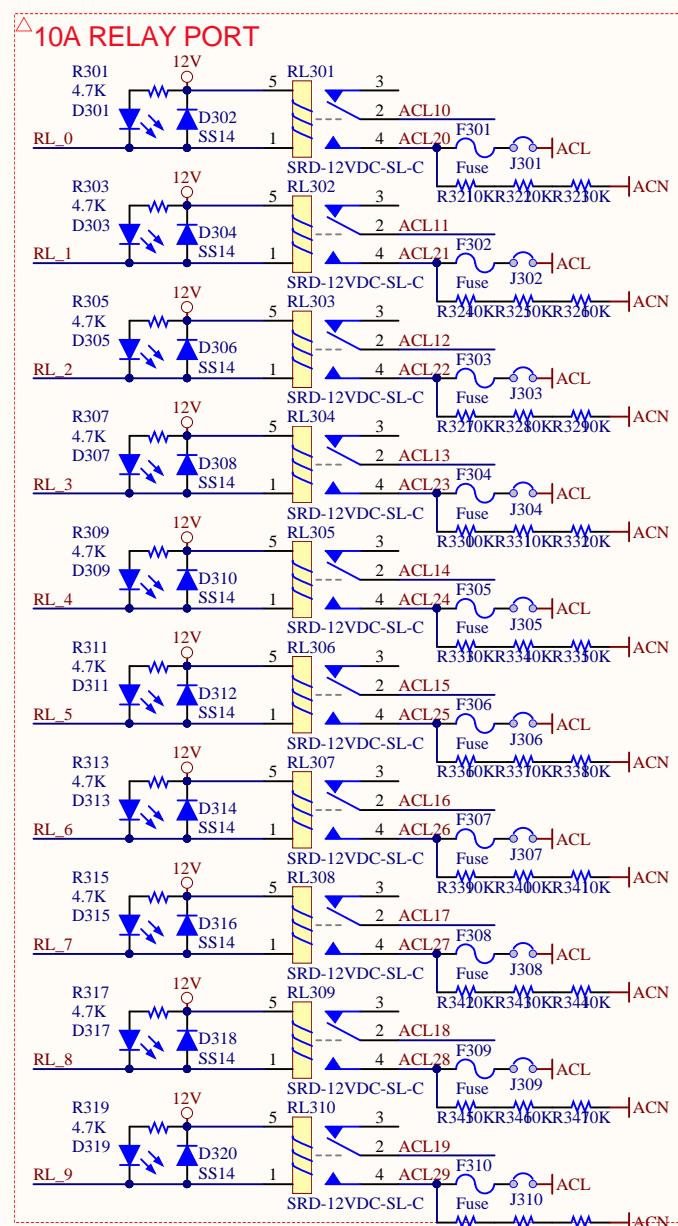
D

D

Title *eBox_3G*

Size: A4	Number: 4	Revision: 2.0
Date: 26/05/2020	Time: 12:36:41 PM	Sheet 4 of 7
File: eBox_3G.SchDoc		

NPN Lab



Title **eBox_Peripherals**

Size: **A4** Number: **5**

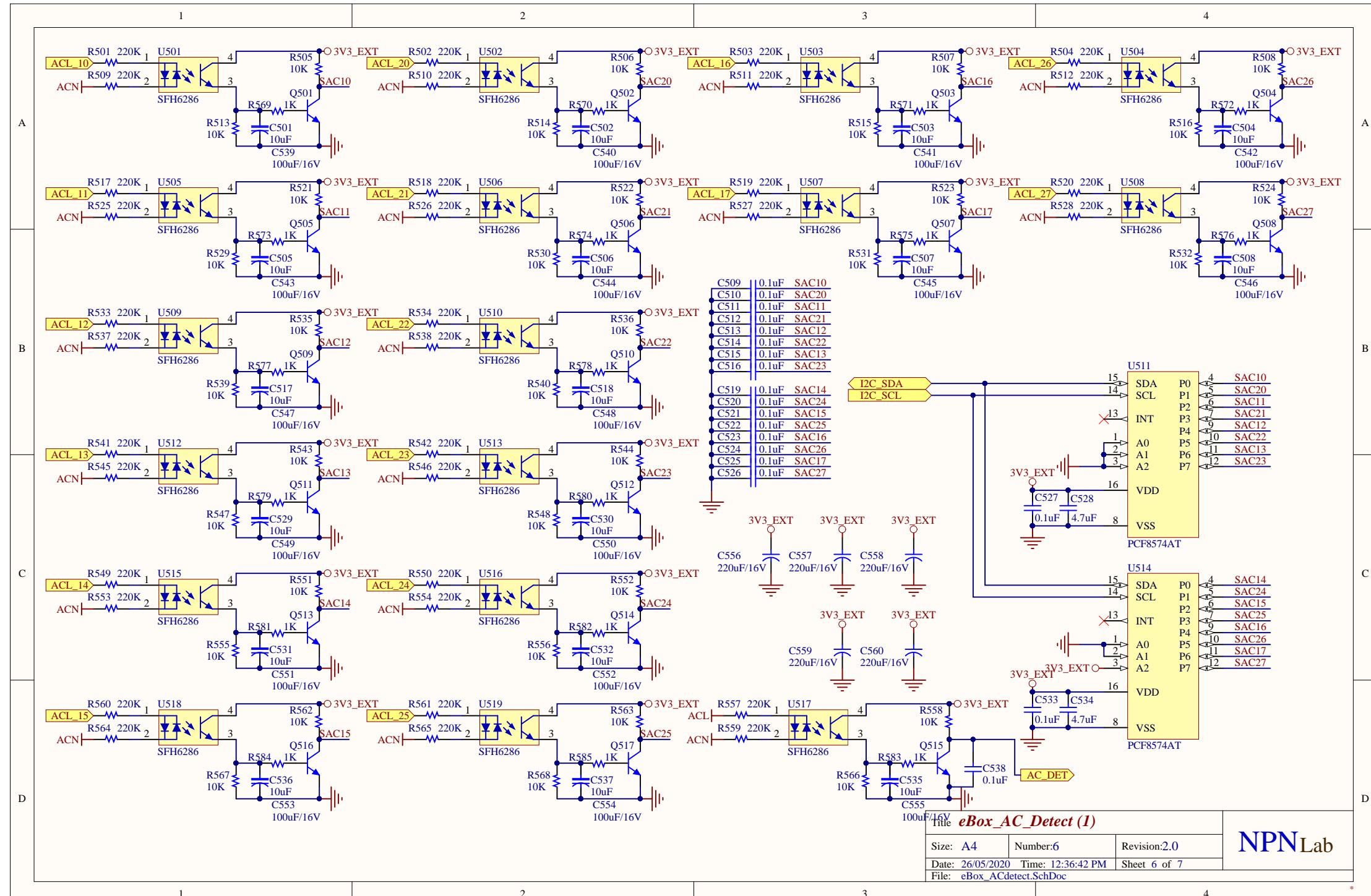
Revision: **2.0**

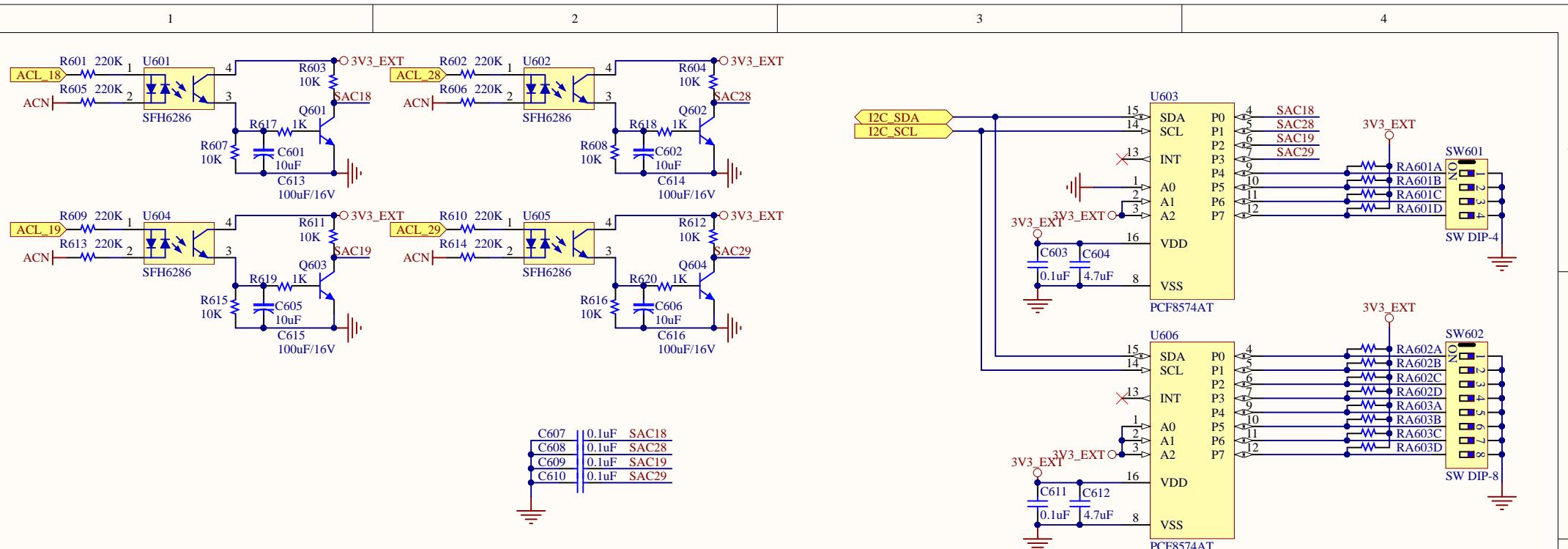
Date: **26/05/2020** Time: **12:36:41 PM**

Sheet **5** of **7**

File: **eBox_Peripherals.SchDoc**

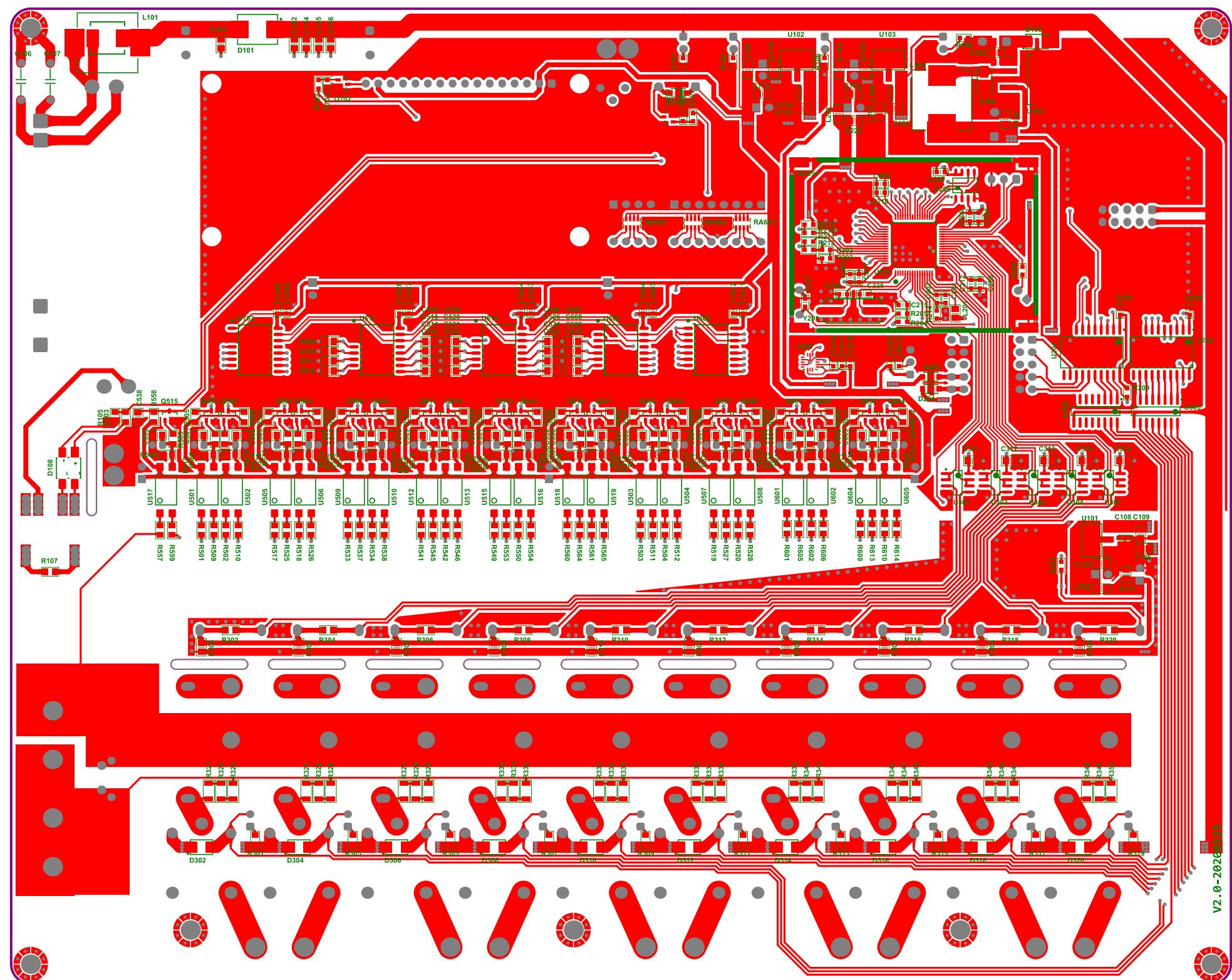
NPN Lab





Title eBox_AC_Detect (2)		
Size: A4	Number: 7	Revision: 2.0
Date: 26/05/2020	Time: 12:36:42 PM	Sheet 7 of 7
File: eBox_ACdetect2.SchDoc		

NPN Lab



W2.0-20

