欢迎第一次加入的伙伴（开会时请从下一页开始展示）

- 开放编辑，直接点击 request for edit 然后在东亚时区群里at吴伟
- 如果没有找到自己的内容分类，可以添加1-2页在最开始或中间
- 欢迎在开始的前5分钟进行自我介绍
- 日常八卦在东亚时区RISC-V双周同步微信群中，欢迎加入
- 东亚时区Slides会公开到
  :https://github.com/cnrv/RISCV-East-Asia-Biweekly-Sync/tree/main/biweekly-meetings仓库，并且默认了CC协议

东亚时区RISC-V双周会

2025年04月03日·第 100 次

https://github.com/cnrv/RISCV-East-Asia-Biweekly-Sync

Host: 聂雨婷

Organizer: PLCT Lab plct-oss@iscas.ac.cn

会议议程（15:00 - 16:00）

- 自我介绍、等待参会者接入、非技术话题八卦（没有的话就直接跳过）
- RVI 的更新和八卦（基本上跟东亚双周会群内消息同步）
- 东亚地区小伙伴的项目更新
- 自由讨论

RISC-V International 同步、全球开源社区八卦（陈逸轩）

- [apps-tools-software]公布了Performance Events Task Group评选结果，Rivos 的 Beeman Strong 和 MIPS 的 Bruce Ableidinger 当选。
- [sig-vector]提交了vector absolute difference扩展Zvabd到ARC等待投票
- [tech-p-ext]开始招募Task Group主席
- [allmem] RISC-V 批准 RISC-V IO 映射表 (RIMT) 规范版本 1.0

RVI online hackathon 即将开赛，点击链接报名参加

欢迎大家到线下参加Hackathon hub
北京：https://mp.weixin.qq.com/s/Rq3OXQJOU4_F8TALnG335Q
武汉：https://mp.weixin.qq.com/s/WXSBARjAUIG5n6m-TivGew

RISC-V 中文社区的同步与八卦(杨延玲)

- 香港投资机构注资StarFive：RISC-V芯片开启中国自主创新新篇章

- 超睿科技发布UR-DP1000高性能桌面级RISC-V CPU
  里程碑进展！openKylin成功适配超睿科技RISC-V高性能CPU
  里程碑突破！芯瞳携手超睿科技完成首款国产GPU与自主RISC-V桌面CPU全栈适配

- 中国首款全自研高性能RISC-V服务器芯片！深圳造出

- Andes晶心科技携手proteanTecs, 为RISC-V内核带来性能和可靠性监测

- 中国科学院软件研究所团队推动 Cloud Hypervisor 官方支持 RISC-V

- RISC-V Hackathon来袭！PLCT携deepin (深度) 社区邀你来武汉共筑开源未来！
  2025 RISC-V Hackathon 北京站 | 北京开源芯片研究院携openEuler 社区邀你来挑战！
  亮点剧透 | openEuler Developer Day 2025来了

  欢迎大家参加！

RISC-V 韩语社区的同步与八卦

RISC-V 德语社区的同步与八卦（罗云翔）

- The Automotive Grade Linux (AGL) All Member Meeting, 9 – 10 JULY, 2025 BERLIN, GERMANY

  Automotive Grade Linux (AGL) 夏季全体成员会议（All Member Meeting, AMM），由 Linux 基金会主办。AGL 是一个开源项目，专注于为汽车行业构建标准化操作系统平台。

- LF Energy Summit Europe , 10-11 SEPTEMBER 2025, AACHEN, GERMANY

  主办方Linux 基金会能源（LF Energy），专注于推动能源行业开源技术创新。目标是促进开源技术在能源系统数字化转型中的应用，加速清洁能源转型。

- Cloud Foundry Day Europe, 7 OCTOBER 2025, FRANKFURT, GERMANY

  主办方Linux 基金会旗下 Cloud Foundry 项目组，致力于推动开源云原生平台技术的创新与协作。目标是促进企业应用现代化，加速云原生开发与部署，分享最佳实践及开源工具的应用场景。

- Design and implementation of a hardened cryptographic coprocessor for a RISC-V 128-bit core

  利用RISC-V 28位提供更大的字长空间优势，自定义指令优化，设计专用加密协处理器，提升密码学运算效率，支持物理攻击防护，并降低主CPU负载。

- Neural networks and human-to-human interfaces at Embedded World 2025

  Codasip在Embedded World 2025参展亮点：RISC-V性能加速方案，Codasip Studio创新功能，CHERI联盟首秀

- Towards adaptive RISC-V based systems for non-terrestrial sub-THz communication

  RISC-V 6G通信 解决方案, IHP - Leibniz Institute for High Performance Microelectronics, Frankfurt (Oder), Germany

RISC-V 日语社区的同步与八卦

-

RISC-V 中国峰会进展（吴伟）

Clang/LLVM 上游进展

- [SelectionDAG][RISCV] Avoid store merging across function calls ([#130430](#))
  - https://github.com/llvm/llvm-project/commit/f138e36d522e

- [RISCV] Add late optimization pass for riscv ([#133256](#))
  - https://github.com/llvm/llvm-project/commit/d8e44a9ab290

- Xqcibi, Xqcisim, Xqcilb, Xqcisync MC Layer 支持
  - https://github.com/llvm/llvm-project/commit/036c6cb37c56
  - https://github.com/llvm/llvm-project/commit/467e5a1d41d6
  - https://github.com/llvm/llvm-project/commit/0744d4926a0c
  - https://github.com/llvm/llvm-project/commit/3840f787a21a

- Zilsd,Zclsd,Zvqdotq MC Layer 支持
  - https://github.com/llvm/llvm-project/commit/480202f0d16f
  - https://github.com/llvm/llvm-project/commit/eb77061a428c

GCC 进展

- Support Smrnmi extension in Binutils

  [RISC-V: add Smrnmi 1.0 instruction support](#)

- Fixed Bitmanip regression test problem

  [RISC-V: Fixbug for slli + addw + zext.w into sh[123]add + zext.w](#)

- Support ssnpm, smnpm and smmpm extensions

  [RISC-V: Ssnpm, smnpm and smmpm imply zicsr.](#)

- Fix broken testsuite error of zicbop

  [RISC-V: Fix broken testsuite error of zicbop](#)

- Issue for -mcmodel=large for newlib

  https://github.com/riscv-collab/riscv-gnu-toolchain/issues/1699

QEMU/Spike 进展（呼唤志愿者）

Sail/ACT进展 (PLCT)

- Sail
  - Implement Zvbc extension #550 Merged
  - Implement Zimop and Zcmop extensions #723 Merged
  - Fix incorrect behaviour with INT_MAX pmpaddr #773
- ACT
  - [ACT] Split several large test cases in zfinx #628
  - Speedup riscv_ctg #623

V8 for RISC-V 更新（邱吉、陆亚涵）

1. 6418607: [riscv] Replace t2 with t6 in RISC-V scratch registers to avoid Maglev scratch conflicts | https://chromium-review.googlesource.com/c/v8/v8/+/6418607
2. 6349926: [riscv] Implement VisitUnaligned | https://chromium-review.googlesource.com/c/v8/v8/+/6349926
3. 6333395: [risccv] Delete arch code RiscvCtz/RiscvPopcnt | https://chromium-review.googlesource.com/c/v8/v8/+/6333395
4. 6397656: [riscv] Port RegisterAllocator in builtins | https://chromium-review.googlesource.com/c/v8/v8/+/6397656
5. 6406777: [riscv][wasm][growable-stacks] Define a safe point after stack grow event | https://chromium-review.googlesource.com/c/v8/v8/+/6406777
6. 6397654: [riscv32][turbofan] Inline Adapter's Atomic views into instruction | https://chromium-review.googlesource.com/c/v8/v8/+/6397654
7. 6396583: [riscv] Skip flush-denormals tests | https://chromium-review.googlesource.com/c/v8/v8/+/6396583
8. 6403654: [riscv] Optimize MulOverflow32 by moving sign-extension to codegen | https://chromium-review.googlesource.com/c/v8/v8/+/6403654
9. 6386577: [riscv][wasm][jspi] Run the unwinder on the central stack | https://chromium-review.googlesource.com/c/v8/v8/+/6386577
10. 6386317: [riscv] Fix kWasmTrapHandlerFaultAddressRegister in uc_mcontext | https://chromium-review.googlesource.com/c/v8/v8/+/6386317

Review PATCH:
11. 6298393: [riscv] more scratches for AssertSmiOrHeapObjectInMainCompressionCage | https://chromium-review.googlesource.com/c/v8/v8/+/6298393
12. 6403654: [riscv] Optimize MulOverflow32 by moving sign-extension to codegen | https://chromium-review.googlesource.com/c/v8/v8/+/6403654

Spidermonkey for RISC-V更新（邱吉、陆亚涵）

OpenJDK on RISC-V (PLCT 杨飞)

1. Authored/Co-authored JDK-mainline PRs:
- https://github.com/openjdk/jdk/pull/23723 (8350480: RISC-V: Relax assertion about registers in C2_MacroAssembler::minmax_fp)
- https://github.com/openjdk/jdk/pull/23879 (8351101: RISC-V: C2: Small improvement to MacroAssembler::revb)

2. Proposed JDK-24u mainline PRs:
- https://github.com/openjdk/jdk24u/pull/90 (8346832: runtime/CompressedOops/CompressedCPUSpecificClassSpaceReservation.java fails on RISC-V)

3. Reviewed JDK-mainline PRs:
- https://github.com/openjdk/jdk/pull/23844 (8345298: RISC-V: Add riscv backend for Float16 operations - scalar)
- https://github.com/openjdk/jdk/pull/23509 (8349632: RISC-V: Add Zfa fminm/fmaxm)
- https://github.com/openjdk/jdk/pull/24047 (8352022: RISC-V: Support Zfa fminm_h/fmaxm_h for float16)
- https://github.com/openjdk/jdk/pull/23171 (8347981: RISC-V: Add Zfa zli imm loads)
- https://github.com/openjdk/jdk/pull/23614 (8349908: RISC-V: C2 SelectFromTwoVector)
- https://github.com/openjdk/jdk/pull/23580 (8321003: RISC-V: C2 MulReductionVI)
- https://github.com/openjdk/jdk/pull/23633 (8350095: RISC-V: Refactor string_compare)
- https://github.com/openjdk/jdk/pull/23793 (8350723: RISC-V: debug.cpp help() is missing riscv line for pns)
- https://github.com/openjdk/jdk/pull/23890 (8351140: RISC-V: Intrinsify Unsafe::setMemory)

https://adoptium.net/temurin/releases/?version=24

JDK 24u

Created by Robert Mckenna, last modified by Sean Coffey on Feb 12, 2025

Git source repository: https://github.com/openjdk/jdk24u

Timelines

JDK 24.0.1 timeline
- Feb 3rd RDP2
- Mid Apr 2025 GA

JDK 24.0.2 timeline
- Apr 22nd RDP2
- Mid Jul 2025 GA

Eclipse Temurin™ Latest Releases

Eclipse Temurin is the open source Java SE build based upon OpenJDK. Temurin is available for a wide range of platforms and Java SE versions. The latest releases recommended for use in production are listed below, and are regularly updated and supported by the Adoptium community. Migration help, container images and package installation guides are available in the documentation section.

Use the drop-down boxes below to filter the list of current releases.

Operating System	Architecture	Package Type	Version
Any	Any	Any	24

| 24.0.0+36 Temurin March 21, 2025 | Linux | riscv64 | JDK - 137 MB Checksum | ⬇ .tar.gz |
| | | | JRE - 57 MB Checksum | ⬇ .tar.gz |

Go community work update (PLCT 蒙卓)

TL;DR Summary：
- race detector upstreaming
- RVV asm support upstreaming, runtime optimazation reviewing(Performance regression)
- RVB asm support, runtime support completed,waiting for SSA optimization review
- RISC-V ELF attributes support upstreaming
- RV K extension is planning (RISE)

1. Authored/Co-authored Go-mainline CLs:
- 647596: runtime: unify C -> Go ABI transitions on riscv64 | https://go-review.googlesource.com/c/go/+/647596
- all: add race support for riscv64 | https://github.com/mengzhuo/go/commit/a1b9b0d4faae07a31c599e00ee73aa6b4f882068
  https://github.com/golang/go/issues/64345
- 659175: cmd/link: generate proper attributes for riscv profile | https://go-review.googlesource.com/c/go/+/659175
- 657036: internal/bytealg: vector implementation of count 1 byte for riscv64 | https://go-review.googlesource.com/c/go/+/657036

2. Reviewed Go-mainline CLs:
- [merged] 631937: cmd/internal/obj/riscv: implement vector load/store instructions |
  https://go-review.googlesource.com/c/go/+/631937
- [merged] 646775: cmd/internal/obj/riscv: add support for vector integer arithmetic instructions |
  https://go-review.googlesource.com/c/go/+/646775
- 646736: internal/bytealg: vector implementation of equal for riscv64 | https://go-review.googlesource.com/c/go/+/646736
- 646737: internal/bytealg: vector implementation of compare for riscv64 | https://go-review.googlesource.com/c/go/+/646737
- 646777: cmd/internal/obj/riscv: add support for vector floating-point instructions |
  https://go-review.googlesource.com/c/go/+/646777
- [merged] 646776: cmd/internal/obj/riscv: add support for vector fixed-point arithmetic instructions |
  https://go-review.googlesource.com/c/go/+/646776
- 660855: cmd/compile: intrinsify math/bits.Bswap on riscv64 | https://go-review.googlesource.com/c/go/+/660855
- 652717: doc, cmd/internal/obj/riscv: document the riscv64 assembler | https://go-review.googlesource.com/c/go/+/652717
- 637317: cmd/internal/obj/riscv: fix the encoding for REV8 and ORCB | https://go-review.googlesource.com/c/go/+/637317
- 660856: cmd/compile,internal/cpu,runtime: intrinsify math/bits.OnesCount on riscv64 |
  https://go-review.googlesource.com/c/go/+/660856
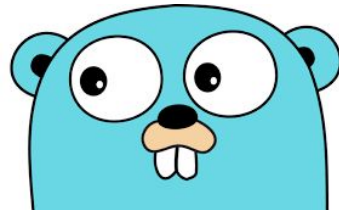
RuyiSDK (Xi Jing, PLCT)

- RuyiSDk 包管理器发布 v0.30 版本：
  - RuyiSDK 包管理器：
    - 修复了 `ruyi list --category-contains` 不工作的问题。
    - 修复了全新安装 `ruyi` 之后直接进行 `ruyi install` 会崩溃的问题。
  - RuyiSDK 软件源格式更新：
    - 基于目前的设备安装器配置数据，新增 实验性的结构化设备型号描述数据 库，初期支持 为每种设备型号描述其 CPU 与微架构能力。
  - 工程化：
    - 将 AArch64 架构的构建任 务迁移至 GitHub Actions 公开免费提供的实例上，以降低 RuyiSDK 团队的维护成本。
    - 修复了 CI 开源许可证检查任务。
- RuyiSDK IDE：RuyiSDK IDE 正在招聘插件开 发实习生，欢迎有兴趣的小伙伴加入开 发团队，详情参考 J159 RuyiSDK IDE 开发实习生，期待您的加入。
- 操作系统支持矩阵
  - CONTRIBUTING: Add CONTRIBUTING.md and report-templates
  - Add Ubuntu testing reports for MilkV DuoS
  - Fix: broken Zephyr link
  - Add/Update mangopi_mq_pro / dongshanpistu (2)
  - Add Mars Deepin 25 test reports and Ubuntu LTS reports.
  - Web 前端
    - https://verforte.vercel.app/
    - https://github.com/panglars/VeRForTe
    - 开发中
- 第一次 RuyiSDK Office Hours 线上会议召开

详见RuyiSDK双周进展报告：https://github.com/ruyisdk/wechat-articles

openEuler RISC-V (周嘉诚)
Status / 20250320

- openEuler 25.03:
  - Official (RVA20): Released 🎉 [DL Link]
  - Preview (RVA22+V): Mass-rebuilding
- Updates
  - RVCK (RV Common Kernel Project)
    - 🎉Merged: basic support for SpacemiT K1
      - Adding support for other parts of K1
    - Finished drafting of testing procedures
  - OpenSSL: upstreaming RVV support for SHA256/512
  - ISA-L: upstreaming RVV support for the RAID module
  - CI: Fixing building issues caused by oE infra changes
  - kata-containers: upstreaming RISC-V enablement for Go runtime

Following releases in 1H'25

- Late Q1 - openEuler 25.03
- Late Q2 - openEuler 24.03 SP2

Features:

- 6.6-based common kernel for QEMU, SG2042 (Pioneer) & TH1520 (LPi4A)
- UEFI-supported Hardware & QEMU images
- Penglai TEE-enabled firmware variants

Images:

- UEFI ISO
- UEFI qcow2 Image w/ Penglai TEE
- Legacy-boot Images for Pioneer & LPi4A

Gentoo for RISC-V 的情况更新（Gentoo 小队）

Arch Linux RISC-V (Felix & PRZ)

- [core] 261 / 265 (98.49%)
- [extra] 13909 / 14201 (97.94%)

- Refreshed patches for Bazel 7 & 8
- Backported upstream libatomic fix for LDC.
- Still working on ROCm 6.3.x
- Rust 1.85 update with musl target's crt_static_default fix.
- Chromium 135.0.7049.52 updated. Now buildable on SG2042.
- Getting new Arch Linux member on board and may make use of GitHub org.

Arch Linux RISC-V (Felix & PRZ) - Electron

-

Fedora on RISC-V status update（20250403）

- **RPM packaging**
  - Koji Status: **F41, GA on Nov 12**
    - **F41: 23952/24320[98.48%] srpm**
    - **Rawhide/F42: 21508 [90.67%] srpm**
    - **https://www.fedoravforce.com**
- **main package version:**
  - Toolchain:
    - gcc-15.0.1-0.11**[upgrading]**
    - glibc-2.40.9000-27.0
    - binutils-2.44-3
  - libffi-3.4.6-5
  - java-21-openjdk
  - java-latest-openjdk(22.0.2.0.9)**[PR]**
  - perl-5.40.1-515
  - python3.13-3.13.2-2
  - llvm-19.1.7-5.0
  - golang-1.23.3-1**[issue]**
  - **rust-1.85.0-1**

- Desktop support Fedora 42:
  - **DONE：XFCE/LXDE/GNOME/KDE/Sugar/i3**
  - Building: LXQT/Cinnamon/Sway/Budgie/Mate/Deepin
  - **Key Desktop App**
    - firefox-131.0-2
    - libreoffice-24.8.3.2-2
    - **Thunderbird-128.8.0-1**
    - chromium-126.0.6478.182-2

- Image and REPOs :
  - **https://images.fedoravforce.com**
  - Images: rsync://mirror.iscas.ac.cn/fedora-riscv/releases/41/Spins/
  - REOP: rsync://mirror.iscas.ac.cn/fedora-riscv/releases/41/Everything

- ROS/ROS2 upgraded to F42
- Sail for rawhide[UPSTREAMING]
- function testing for F41:
  - **Podman**, **Image：fedorariscv/base**
  - Ceph[ONGOING]
  - K8s[ONGOING]

Debian for RISC-V（于波）

- Official port update

  0. Debian Trixie start freeze

  1. Next milestone 04/15 soft freeze

- Debci

  0. Adding redundant site

- Some works

  1. pygubu[upload], ipmitool[upload], tksheet[upload]

  2. release-note [[MR](MR)], lintian [[MR](MR)], sail[[0.19](0.19) packaging]

RevyOS (郑景坤)

- Notable updates
    - TH1520 (Lichee Pi 4A & Milk-V Meles)
        - New image: 20250323 w/kernel 6.6.82
        - Download Link: [LPi4A](#) | [Meles](#)
        - Notable Changes
            - Fixed: some files missing in sdcard image
            - Enable PWM fan in U-Boot
    - SG2042 / Milk-V Pioneer new image
        - Linux kernel 6.14.y: [revyos/sg2042-vendor-kernel](#)
        - Download Link: [ISCAS mirror](#)
        - Screenshot on the next page
        - Currently in testing, we're still squashing bugs ;)
    - (Important) revyos-keyring update
- Community Additions
    - Known Issues
        - [Known compatibility issues](#)
        - some nav2 packages missing in revyos-ros2 ([#119](#), WIP)
        - HDMI display compatibility issues ([#106](#), [#117](#), WIP, root cause found)
- ROS2
    - RevyOS maintains two ROS2 distributions: Humble and Jazzy.
    - jazzy build: 1397/1539 > 1414/1539 (91.88%)
    - humble build: 1619/1785 -> 1658/1787 (92.78%)
    - CI test results:
        - Pass: 38039/38470 -> 38120/38551 (98.88%)
        - Failed: 417, Skipped: 14
        - Total time: 7.83 hours

Supported devices

[Image download directory](#)

1. Lichee Pi 4A (w/kernel 6.6)
2. Milk-V Meles (w/kernel 6.6)
3. Milk-V Pioneer (w/kernel 6.6 & 6.14)
4. Lichee Cluster 4A
5. BeagleV-Ahead
6. Lichee Console 4A
7. RISC-V Book
8. Lichee Book

SD card support

1. Lichee Pi 4A (w/kernel 6.6)
2. Milk-V Meles (w/kernel 6.6)
3. Lichee Console 4A
4. BeagleV-ahead

Guix on RISC-V(郑俊杰)

https://ci.z572.online/jobset/master

1. gnu: gts: Fix cross-compiling.
2. gnu: marisa: Fix build on riscv64-linux.
3. gnu: xnnpack: Fix build on riscv64-linux.
4. gnu: ogre-next: Fix build on riscv64.
5. gnu: gst-plugins-bad: Skip fail test on riscv64-linux.
6. gnu: gts: Fix cross-compiling.
7. [PATCH] gnu: luajit: Add riscv64 support.

success	failed	schuled
24326	5279	216

https://ci.guix.gnu.org/			https://bordeaux.guix.gnu.org/		
aarch64-linux	22.7%		aarch64-linux	96.3%	
armhf-linux	11.3%		armhf-linux	85.1%	
i586-gnu	0.0%		i586-gnu	0.0%	
i686-linux	85.8%		i686-linux	88.8%	
powerpc64le-linux	86.7%		powerpc64le-linux	78.6%	
riscv64-linux	0.0%		riscv64-linux	61.2%	
x86_64-linux	96.6%		x86_64-linux	98.5%	

https://github.com/Z572/guix-riscv-channel/
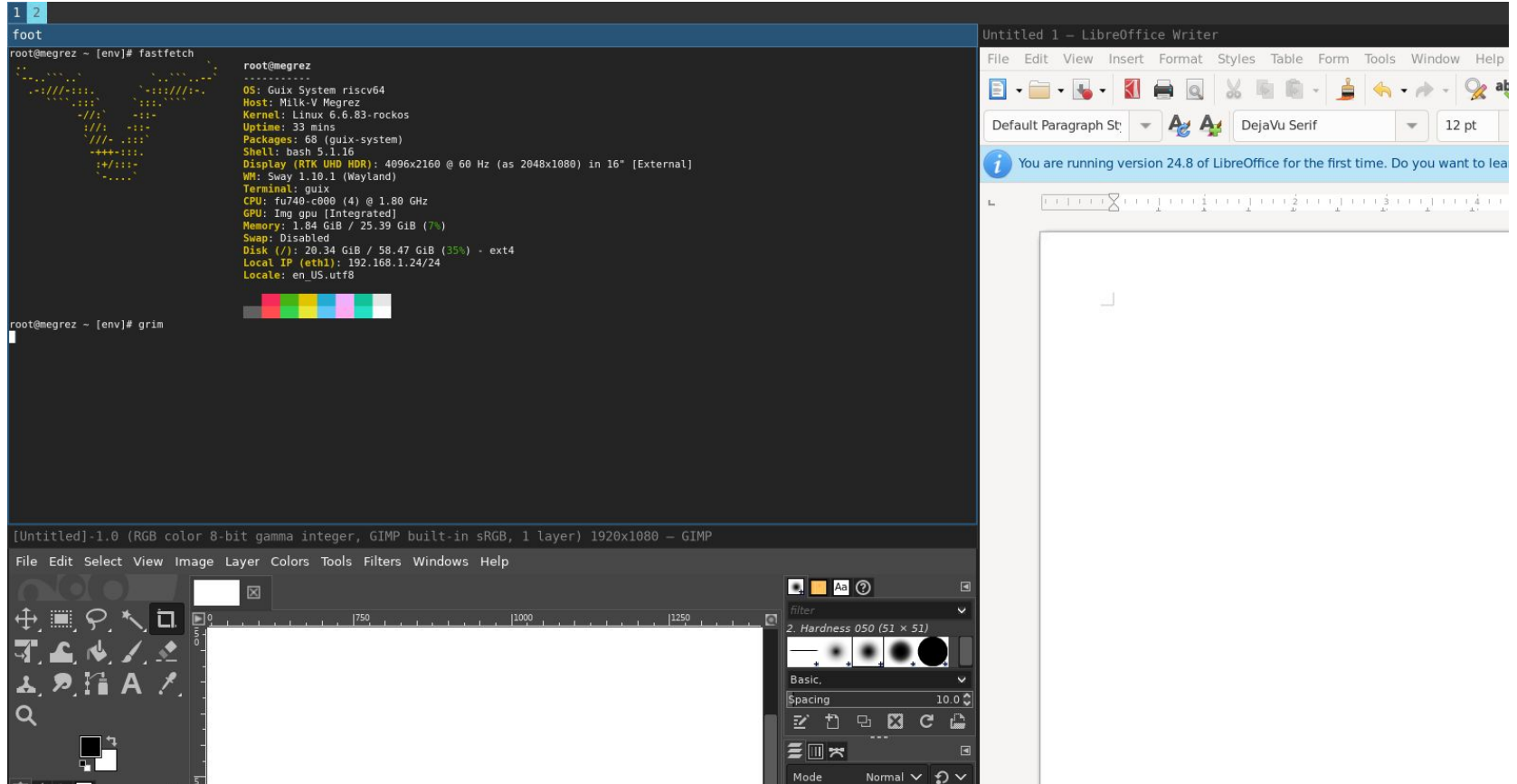
Sophgo Linux Upstream Status Update（汪辰）

https://github.com/sophgo/linux/wiki [Last updated: Mar/19/2025]

v6.14 released and v6.15 merge window opened.

- CV18XX Series
    - N/A
- SG2042
    - N/A
- SG2044
    - N/A

RT-Thread (RISC-V) Upstream Status Update（汪辰）

PR list：

- bsp: cvitek: add howto configure pinmux in README:
  https://github.com/RT-Thread/rt-thread/pull/10142
- bsp: cvitek: fix build warnings for spi: https://github.com/RT-Thread/rt-thread/pull/10140
- bsp: cvitek: fix spi driver build error : https://github.com/RT-Thread/rt-thread/pull/10133
- 主线 5.2.0 发布: https://github.com/RT-Thread/rt-thread/releases/tag/v5.2.0

RFC discussion

- POSIX（Portable Operating System Interface, 可移植操作系统接口）是由 IEEE 制定的跨平台操作系统接口标准，旨在统一 UNIX-like 系统的应用程序接口（API），确保软件在不同系统间的可移植性。
  RT-Thread 建议成立 POSIX SIG, 对 POSIX 进行持续改善。详情见
  :https://github.com/RT-Thread/rt-thread/issues/10156。
- RT-Thread 社区发起讨论，收集比较通用和官方的针对 RISC-V 的 toolchain, 方便后续 RISC-V 平台整理和使用比较统一的版本。详情见:https://github.com/RT-Thread/rt-thread/issues/10161。

OpenCloudOS SIG 进展（孙敏）

2025年2月 完成 BaseOS 、AppStream适配

主要参与者:腾讯内部+实习生

沟通渠道:腾讯会议、腾讯文档

打包/测试平台: Kojihub on SG2042

待跟进Issue: 142个(kernel+userspace)
https://gitee.com/organizations/opencloudos-stream/issues?issue_search=RISC-V
https://doc.weixin.qq.com/sheet/e3_AUcAgQaQAL0CN8GyWWEnPSY0oT99A?scode=AJEAIQdfAAoT7SjyXEAUcAgQaQAL0&tab=ss_cyfm9n&viewId=vRoYPz

甲辰计划 开发测试实习生: 招募名额 5/18
https://github.com/rv2036/weloveinterns/blob/master/open-internships.md J155

Todo：

外部可使用的koji基础设施--寻求开发板资助
优化内核补丁以优化适配SG2042

内核适配更多开发板

预览镜像地址: http://43.139.5.209/ 支持 SG2042和qemu-system-riscv64

1. 安装yum源(http://43.139.5.209，镜像rpm软件包安装地址)

2. 测试yum源(http://43.139.5.209/ocs-build，用作koji构建的外部源)

3. 做镜像的ks文件(http://43.139.5.209/kickstarts)

4. ocs developer镜像(http://43.139.5.209/img/)

5. ocs sdcard镜像(http://43.139.5.209/sdcard-img/)

6. 一些修改过后的diff文件
(http://43.139.5.209/task/diff/modify-patch/)

7. 修改kernel的patch(http://43.139.5.209/task/diff/kernel/)

Box64 RISC-V 进展

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OpenSBI（王翔）

➢ 在获取核心扩展字符串时，添加错误检查防止缓冲区溢出。
https://lists.infradead.org/pipermail/opensbi/2025-March/008206.html

➢ 修正pmu代码允许监控Andes的cycle/instret。
https://lists.infradead.org/pipermail/opensbi/2025-March/008220.html

➢ 重构platform支持代码为fdt_driver，在初始化方法中修改
generic_platform_ops。
https://lists.infradead.org/pipermail/opensbi/2025-March/008264.html

➢ 修改domain_context_priv中的hart上下文数组为动态分配。
https://lists.infradead.org/pipermail/opensbi/2025-March/008276.html

RustSBI团队进展（洛佳）

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RustSBI团队进展（洛佳）

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香山开源RISC-V处理器 - ICT / PCL

- 前端
  - 修复指令预取流水线对于高位地址异常的 处理 (#4423)
  - 进行了一系列 SRAM 拆分 (#4468) 、(#4360)、 (#4485)
- 后端
  - 修复 trigger 触发以及异常产生时, 指令融合未 被停止的问题 (#4439)
  - 进一步修复 amocas.q 指令卡死的问题 (#4435)
  - 修复指令融合 译码单元在被无效 时, 仍然输出有 效融合信息的问题 (#4456)
  - 修复 reset 后, non-reg 中断等待 difftest 框架同 步错误的问题 (#4449)

- 访存与缓存
  - 修复 MMU 中, 与高位地址 检查、异常处理、Svnapot 扩展相关 的一系列问题 (#4422)、(#4448)、(#4453)、(#4454)、(#4455)、 (#4471)、(#4472)、(#4473)
  - 修复 NEMU 作为多核 ref 时, 开启虚拟化场景下的 Bug (NEMU #835)
  - 修复若干 Uncache、非对齐、异常混合场景的 Bug (#4426)、 (#4441)、(#4442)
  - 修复 NEMU 在 PBMT 将内存属性由 cacheable 覆盖为 IO/NC 时, 原子指令和非 对齐访存没有报异常的 Bug (NEMU #833)
  - 修复若干 CoupledL2 违背 CHI 协议的一致性问题 (CoupledL2 #390)、(CoupledL2 #392)、(CoupledL2 #395)
  - 添加 L2 Cache 退出维护一致性状态前对 WFI(Wait For Interrupt) 状态的检查 (CoupledL2 #385)
  - 修复若干 CHI 协议中 RespErr 字段处理的相关问题 (CoupledL2 #384)、(CoupledL2 #394)
  - 调整 L2 Cache SRAM(tag) 规格 (CoupledL2 #388); 添加 SRAM 控制接口 (CoupledL2 #389)

banshanjdk-8 让你的 java8 程序在 RISC-V 平台极限加速

Chisel and Additional Technology / Sequencer

OpenHW & OpenHW Aisa Working Group

甲辰计划进展（吴伟）

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自由讨论 / AOB

BACKUP

准备加入更多的国际开源组织进行同步观测

欢迎追加或提议