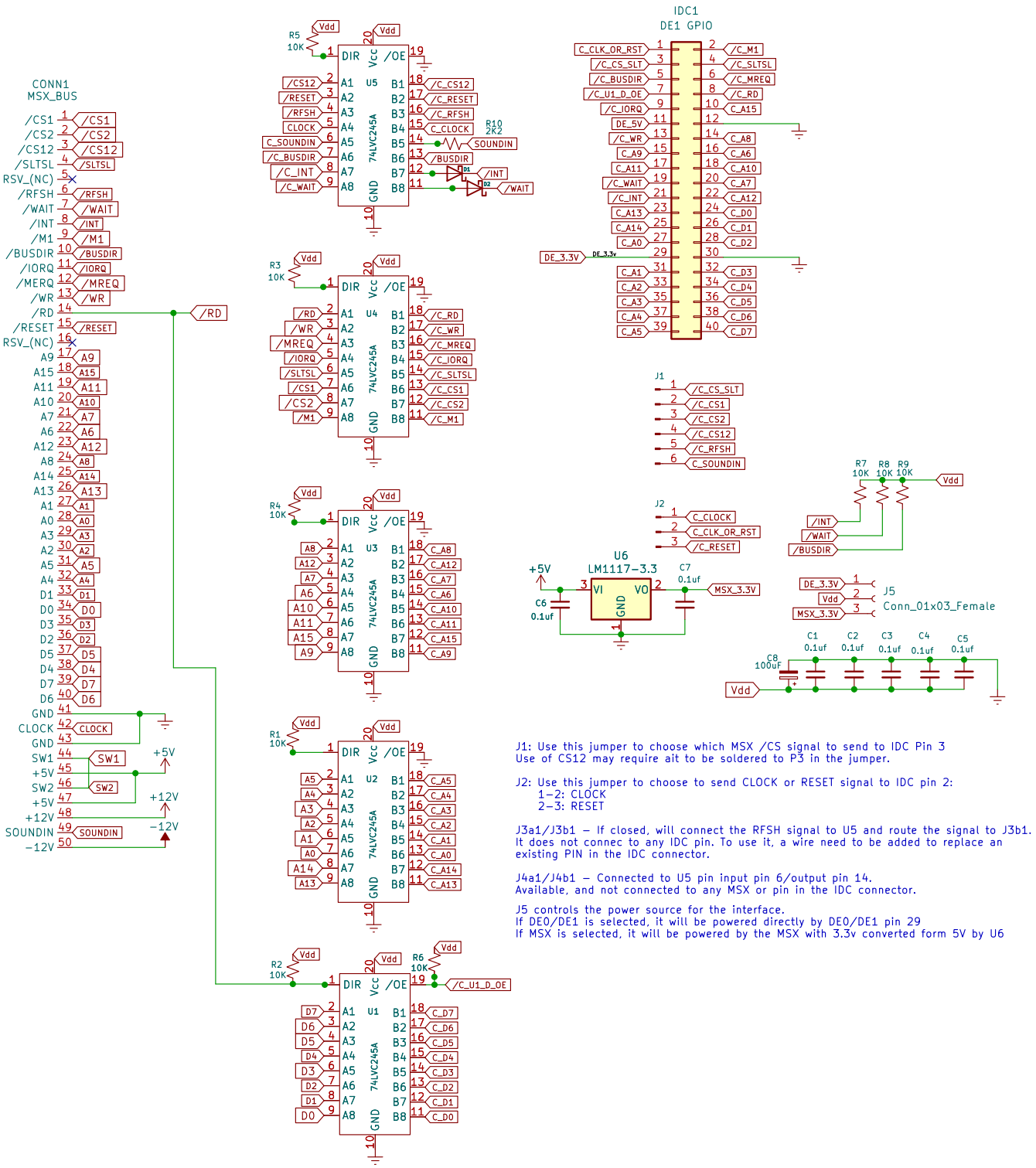

Designed for Terasic DE0 & DE1
IDC connector pinout mapped for Altera DE1 FPGA Development Board
Cartridge for MSX Computers that expose the MSX BUS
to 3.3v systems with voltage conversion

RCC

Sheet: /
File: MSX_FPGA_Hat.kicad_sch

Title: MSX FPGA Hat

Size: A4	Date: 2023-03-01	**Rev: 1.3**
KiCad E.D.A. kicad (6.0.7)		Id: 1/1