



SRAM Addon for Terasic DE0 board.

This Add-on is a 8 bits data bus only. 2 x 256KB banks.
LB_n and UB_n enable the banks (only one should be enabled at any time),
because both banks shares the same GPIO pins in the DE0 board.

The Raspberry Pi GPIO header can be used when CE_n is HIGH (1),
because it also shares the GPIOs with the SRAM expansion.

RCC 2023

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