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3.6. Lag Compensation

Lecturer

Set up MATLAB

```
%cd matlab
pwd
clear all
imatlab_export_fig('print-svg') % Static svg figures.
format compact

ans =
    '/Users/eechris/code/src/github.com/cpjobling/eglm03-textbook/03/6'
```

3.6.1. Introduction

Lead compensation is used to improved the transient response of a closed-loop control system.

In most cases it is also important to ensure that the steady-state accuracy of the closed-loop system is controlled.

In order to reduce the steady-state errors in a system we can use integral action (see PID compensation) which has the affect of increasing the system type number by 1 or increase the system gain.

Unfortunately gain compensation, because it acts at all frequencies, usually has the effect of reducing stability.

What is needed is a form of compensation that acts in a frequency dependent way: adding gain at low frequencies while maintaining mid-to-high frequency gains at the values needed to guarantee stable transient behaviour.

A dynamic compensator that has this effect is the "lag compensator".

The lag compensator has the transfer function:

$$D_{\text{lag}}(s) = K_c \left(\frac{s - z_0}{s - p_0} \right)$$

where $|z_0| > |p_0|$.\$

Considering the frequency response of $D_{\text{lag}}(s)$

$$D_{\text{lag}}(j\omega) = K_c \left(\frac{j\omega - z_0}{j\omega - p_0} \right)$$

The low and high-frequency gains are:

$$D_{\text{lag}}(j\omega)|_{\omega\to 0} = K_c\left(\frac{z_0}{p_0}\right)$$

$$D_{\text{lag}}(j\omega)|_{\omega\to\infty} = K_c$$

Because the low frequency gain is higher than the high frequency gain (the compensator has a low-pass characteristic). If the high frequency gain is designed to match the gain needed for transient performance, the low frequency gain can be independently designed to achieve steady-state performance requirements.

3.6.2. Properties of the Cascade Lag Compensator

As $|z_0| > |p_0|$ (a ratio of $z_0/p_0 \le 10$ is usual), the angle contributed by the compensator to some arbitrary point s_1 at on the s-plane, is illustrated in Figure 1.

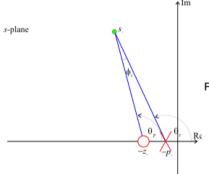


Figure 1 Angle contribution of a lag compensator

The net contribution is

$$\phi_c = -\theta_p + \theta_z < 0$$

so that the lead compensator always makes a negative contribution to the angle criterion.

This has the effect of allowing the closed-loop poles to move to the right in the s-plane.

The design approach seeks to keep this negative contribution as small as possible so that the pole must be placed physically close to the zero. Typically $|\phi_c| < 2^\circ$.

For convenience, the DC gain of the lag compensator is made unity:

$$D_{\text{lag}}(s)|_{s=0} = K_c \left(\frac{z_0}{p_0}\right) = 1$$

Since $|z_0| > |p_0|$ then

$$K_c = \frac{p_0}{z_0} < 1.$$

Now, the uncompensated (or lead-compensated) closed-loop characteristic equation is

$$1 + K_0 G(s)H(s) = 0$$

SO

$$K_0 = -\frac{1}{G(s)H(s)}.$$

The gain K_0 is the root-locus gain required to place the required dominant closed-loop poles at $s=s_1$.

The values of the zero and pole are chosen such that

$$\left. D_{\mathrm{lag}}(s) \right|_{s=s_1} = K_c \left(\frac{s-z_0}{s-p_0} \right) \cong K_c.$$

This equation requires that the pole and zero be close together and also that

$$z_0 \ll |s_1|$$
.

For the lag compensated system, the new root-locus gain will be

$$K = -\frac{1}{D(s)G(s)H(s)} \ \cong -\frac{1}{K_cG(s)H(s)} = \frac{K_0}{K_c}.$$

The gain K is chosen to satisfy the steady-state error requirements and the compensator gain K_c is used to ensure that the effective gain at $s=s_1$ is unchanged.

3.6.3. Design Procedure

- 1. Choose K_0 to ensure that the desired dominant closed-loop poles satisfy the transient response requirements.
- 1. Calculate the gain K required to give the desired steady-state error.
- 1. Assuming that the lag compensator has unity DC gain choose $K_c = K_0/K$.
- 1. Choose $|z_0|$ small with respect to $|s_1|$.

A value of $z_0 = |s_1|/10$ is often taken as a reasonable first approximation.

The lag compensator pole is then $p_0 = K_c z_0$.

3.6.4. Example

The plant (type 1)

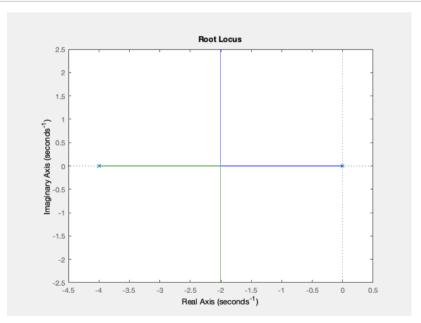
$$G(s) = \frac{1}{s(s+4)}$$

The feedback is

$$H(s) = 1$$

hence

root-locus



In common with previous examples, we chose closed-loop poles at $s=-2\pm j2$ and determine K_0 :

As an aside, we calculate the step response for comparison later:

```
Gc1 = feedback(K0*G,H);
[y1,t1]=step(Gc1,10);
```

The system is type 1 so that the static velocity error

$$K_v = sG(s)H(s)|_{s=0} = s\frac{K_0}{s(s+4)}\Big|_{s=0} = \frac{K_0}{4} = 2.$$

We would, for the purposes of illustration, like to increase the static velocity error to 10 which will make the steady-state velocity error equal to 10%.

Thus we need K = 40.

```
K=40;
Kc = K0/K
Kc = 0.2000
```

Now

```
abs(s1)

ans = 2.8284
```

so let the zero be

```
z0 = -3/10
z0 = -0.3000
```

and

so

```
Dlag = zpk([z0],[p0],Kc)

Dlag =

0.2 (s+0.3)

-----
(s+0.06)

Continuous-time zero/pole/gain model.
```

Let us see how we have done.

```
Go2 = Dlag*GH

Go2 =

0.2 (s+0.3)

-----

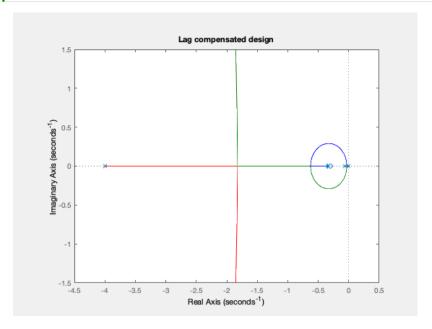
s (s+0.06) (s+4)

Continuous-time zero/pole/gain model.

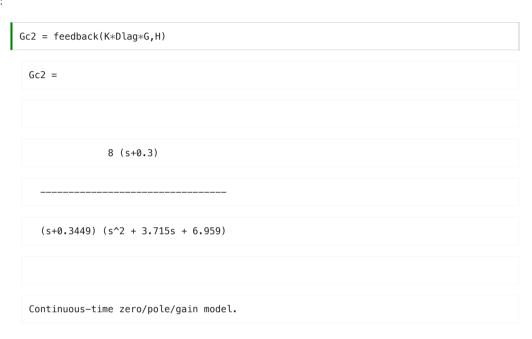
[poles] = rlocus(Go2,K)

poles =
-1.8576 + 1.8730i
-1.8576 - 1.8730i
-0.3449 + 0.0000i
```

```
rlocus(Go2),hold on,plot(poles,'*'),...
    title('Lag compensated design'),...
    hold off
```



The closed-loop system:

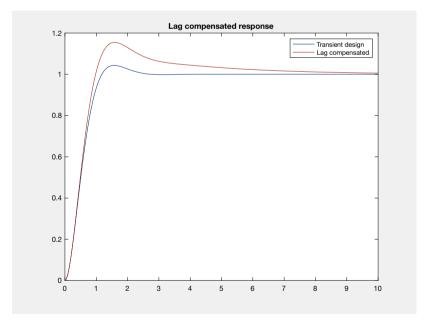


has step response

```
[y2,t2]=step(Gc2,10);
```

Finally we compare the lag-compensated and gain compensated responses:

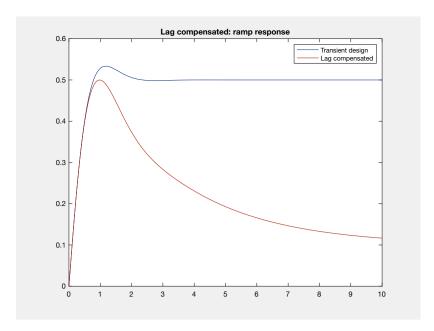
```
plot(t1,y1,t2,y2),title('Lag compensated response'),...
legend('Transient design','Lag compensated')
```



3.6.5. Comments

The lag compensator has a significantly longer settling time and a raised overshoot. These effects can be explained by examination of the closed-loop transfer function.

The benefits of the lag compensated repsonse can only be really seen when we compare the ramp responses. To determine the ramp response, we need to add an integrator to the plant and find the error responses:



The error is certainly reduced, but the settling time is rather long. To improve the design, a strategy would be to reduce the size of the lag pole to reduce still further the dynamic effects of the compensator on the settling time. However, the zero will still have an effect on the overshoot.

3.6.6. Resources

 $An \ executable \ version \ of \ this \ document \ is \ available \ to \ download \ as \ a \ MATLAB \ Live \ Script \ file \ cclag.mlx.$

You can simulate this design after defining the parameters:

```
%file params.m
\mbox{\% PARAMS} — parameters for Lag Compensator Simulation
K0 = 8
Kc = 8/40
K = 8/Kc
G = tf(1,conv([1,0],[1,4]))
Clag = Kc*tf([1, 0.3],[1, 0.06])
F = 1;
  K0 =
      0.2000
  G =
        1
    s^2 + 4 s
  Continuous—time transfer function.
  Clag =
    0.2 s + 0.06
      s + 0.06
  Continuous—time transfer function.
```

using this Simulink model lag_sim.slx [params.m].

By Dr Chris P. Jobling

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