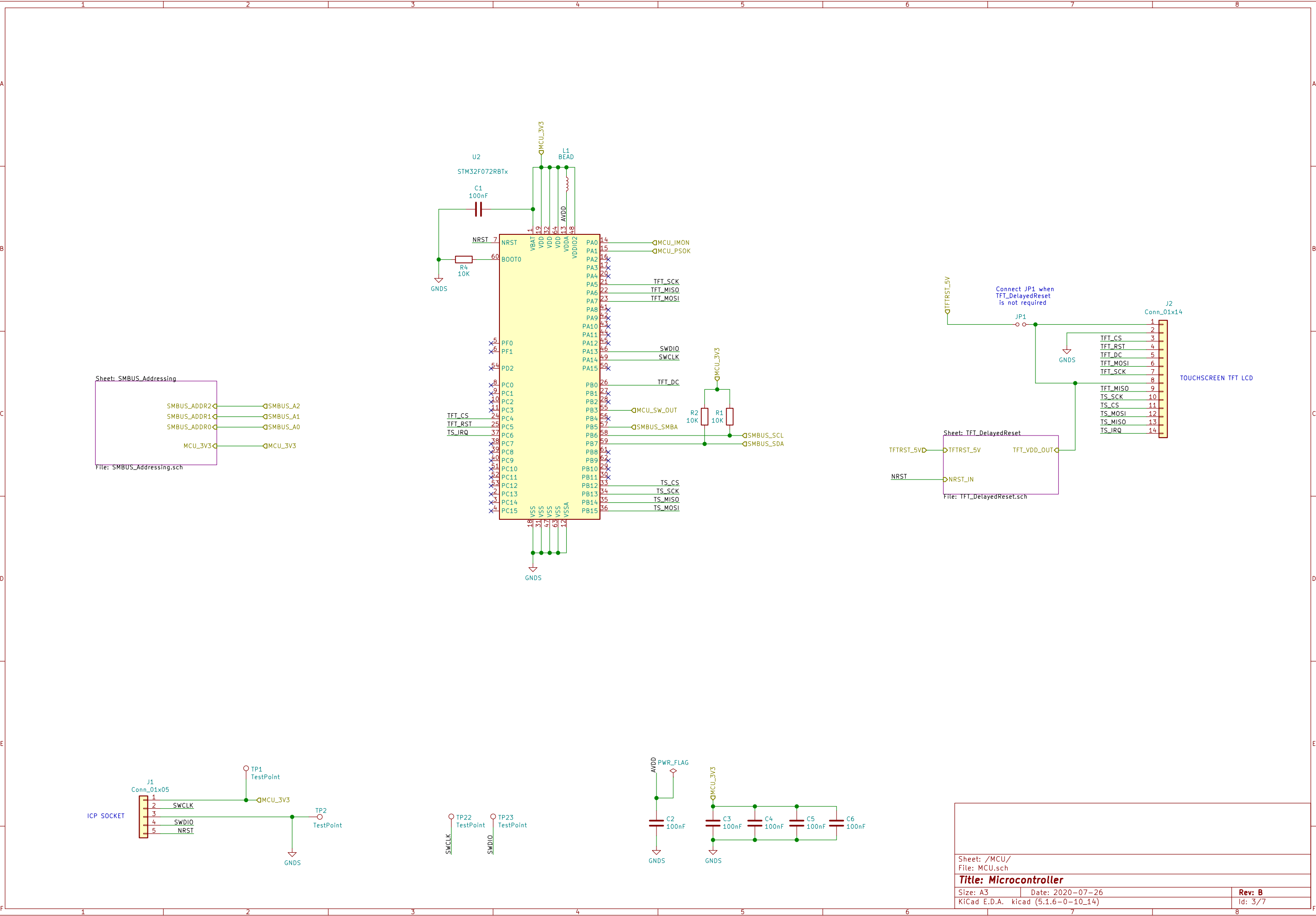


Sheet: /OutputConnectors/ File: OutputConnectors.sch		
Title: <b>High current output connctors</b>		
Size: A4	Date: 2020-07-26	Rev: <b>B</b>
KiCad E.D.A. kicad (5.1.6-0-10_14)		Id: 2/7



Sheet: SMBUS\_Addressing

File: SMBUS\_Addressing.sch

SMBUS\_ADDR2 → SMBUS\_A2

SMBUS\_ADDR1 → SMBUS\_A1

SMBUS\_ADDR0 → SMBUS\_A0

MCU\_3V3 → MCU\_3V3

Connect JP1 when TFT\_DelayedReset is not required

J2 Conn\_01x14

TOUCHSCREEN TFT LCD

TFT\_CS

TFT\_RST

TFT\_DC

TFT\_MOSI

TFT\_SCK

TFT\_MISO

TS\_SCK

TS\_CS

TS\_MOSI

TS\_MISO

TS\_IRQ

Sheet: TFT\_DelayedReset

File: TFT\_DelayedReset.sch

TFTRST\_5V → TFTRST\_5V

NRST → NRST\_IN

TFT\_VDD\_OUT

J1 Conn\_01x05

ICP SOCKET

TP1 TestPoint

TP2 TestPoint

SWCLK

SWDIO

NRST

MCU\_3V3

GNDS

TP22 TestPoint

TP23 TestPoint

SWCLK

SWDIO

AVDD

PWR\_FLAG

C2 100nF

GNDS

MCU\_3V3

C3 100nF

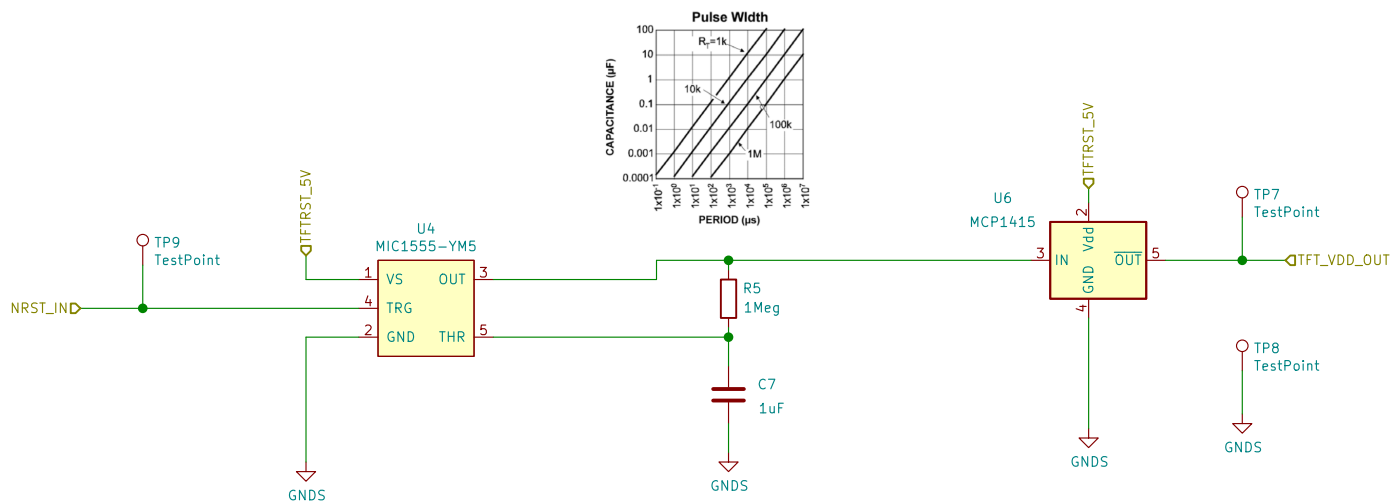
C4 100nF

C5 100nF

C6 100nF

GNDS

Sheet: /MCU/		
File: MCU.sch		
Title: <b>Microcontroller</b>		
Size: A3	Date: 2020-07-26	Rev: <b>B</b>
KiCad E.D.A. kicad (5.1.6-0-10_14)		
Id: 3/7		



Hard reset of TSC2046 touch controller IC. Needed for debug  
 U3 may be omitted if buffer isn't required  
 Fit only Q2 and U5....or only U6

Sheet: /MCU/TFT\_DelayedReset/  
 File: TFT\_DelayedReset.sch

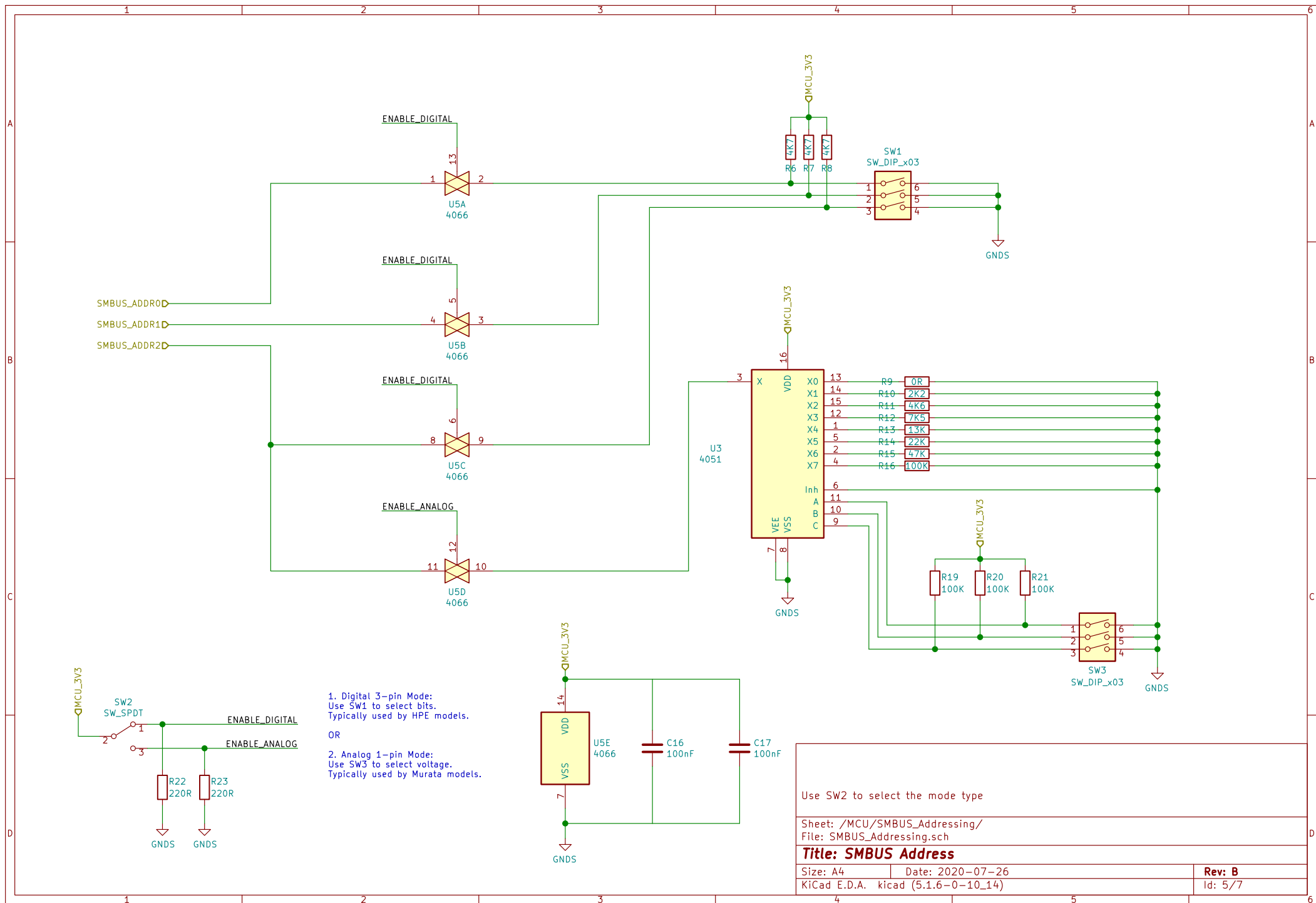
### Title: TFT Delayed Reset

Size: A4 Date: 2020-07-26

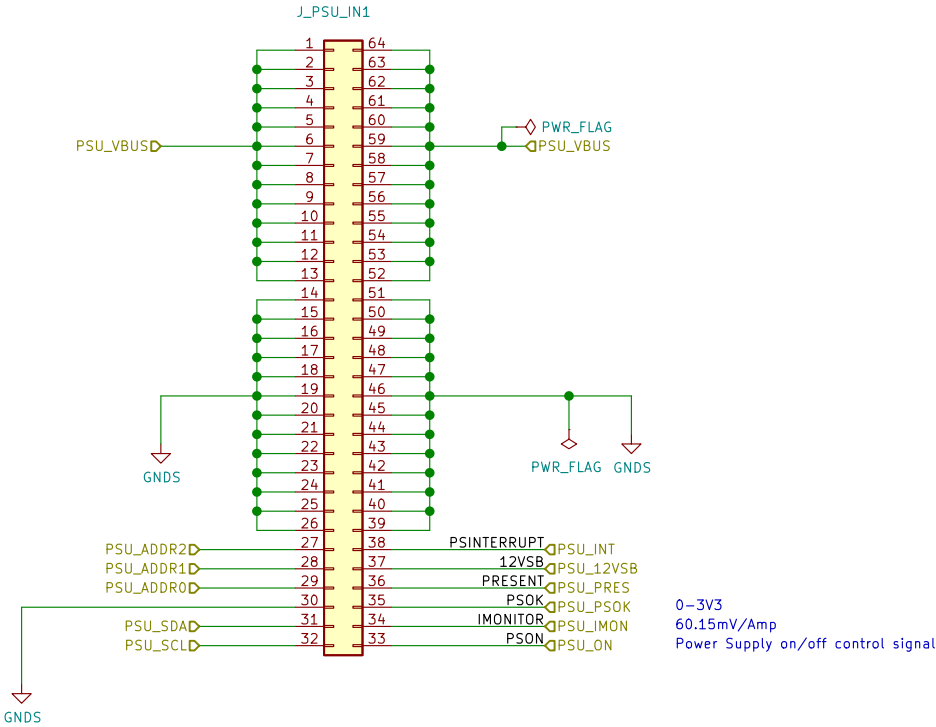
KiCad E.D.A. kicad (5.1.6-0-10\_14)

Rev: B

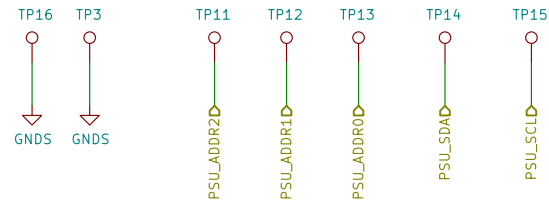
Id: 4/7



Card Slot Connector



Testpoints



Sheet: /PSUConnector/ File: PSUConnector.sch		
Title: Card slot connector		
Size: A4	Date: 2020-07-26	Rev: B
KiCad E.D.A. kicad (5.1.6-0-10_14)		Id: 6/7

PSU Power

PSU ENABLE REQUIRES TWO CONDITIONS:  
1. PULLUP PRESENT PIN; TELL PSU IT IS INSERTED INTO ACTIVE SERVER  
2. GND ENABLE PIN

PSU\_PRES

1. To "enable" the Main 12Vdc output  
the PRESENT signal requires to be pulled "high"  
with respect GND.

PSU\_ON

2. The PS\_ON signal can be permanently  
connected to GND to "enable" the Main 12Vdc output

Q1  
DMG2302U

MCU\_SW\_IN

PSU\_12VSB

R3

MCU/TFT Power

12VSB PIN CAN PROVIDE 2.5A WHILE PSU IS DISABLED

PSU\_12VSB  
PWR\_FLAG

C11  
1uF X5R 25V

GND

U1  
LMR14206

C14  
220nF

L2  
18uH 150mOhm

D1  
SS14L

R17  
5.6K

R18  
1K

C15  
47uF X5R 10V

TestPoint  
TP6

TP5  
TestPoint

PWR\_FLAG  
TFT\_5V

U7  
AZ1117CR-3.3

C8  
100nF

C9  
330nF

MCU\_3V3

Sheet: /HighVoltageControl/  
File: HighVoltageControl.sch

Title: Power Supply

Size: A4 Date: 2020-07-26

KiCad E.D.A. kicad (5.1.6-0-10\_14)

Rev: B

Id: 7/7