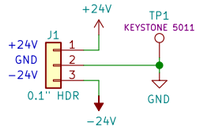
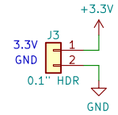
POWER & INPUTS

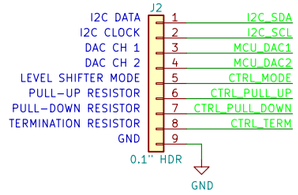
+/−24V POWER IN

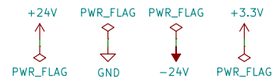


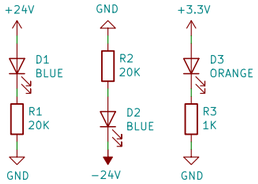
3.3V POWER IN



MCU CONFIG SIGNALS

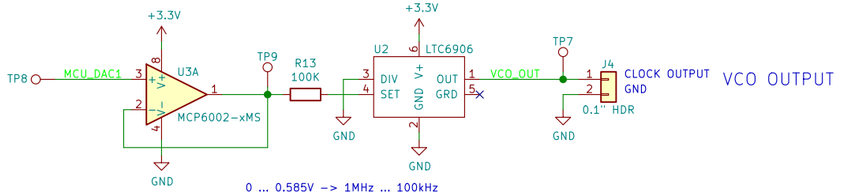






VOLTAGE−CONTROLLED OSCILLATOR (VCO)

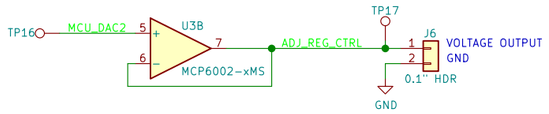
NOTE: DECOUPLING CAP FOR OP−AMP



VCO OUTPUT

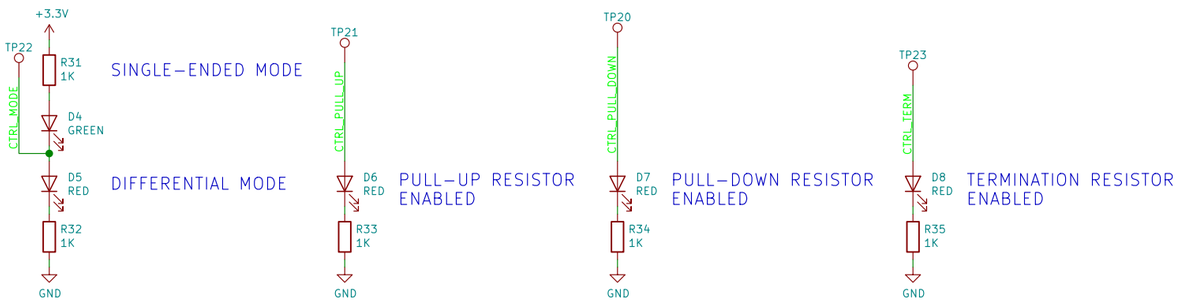
0 ... 0.585V −> 1MHz ... 100kHz

ADJ REGULATOR CONTROL (BUFFERED)



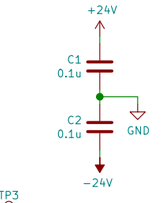
ADJ REG CONTROL OUTPUT

CONTROL SIGNAL LEDs

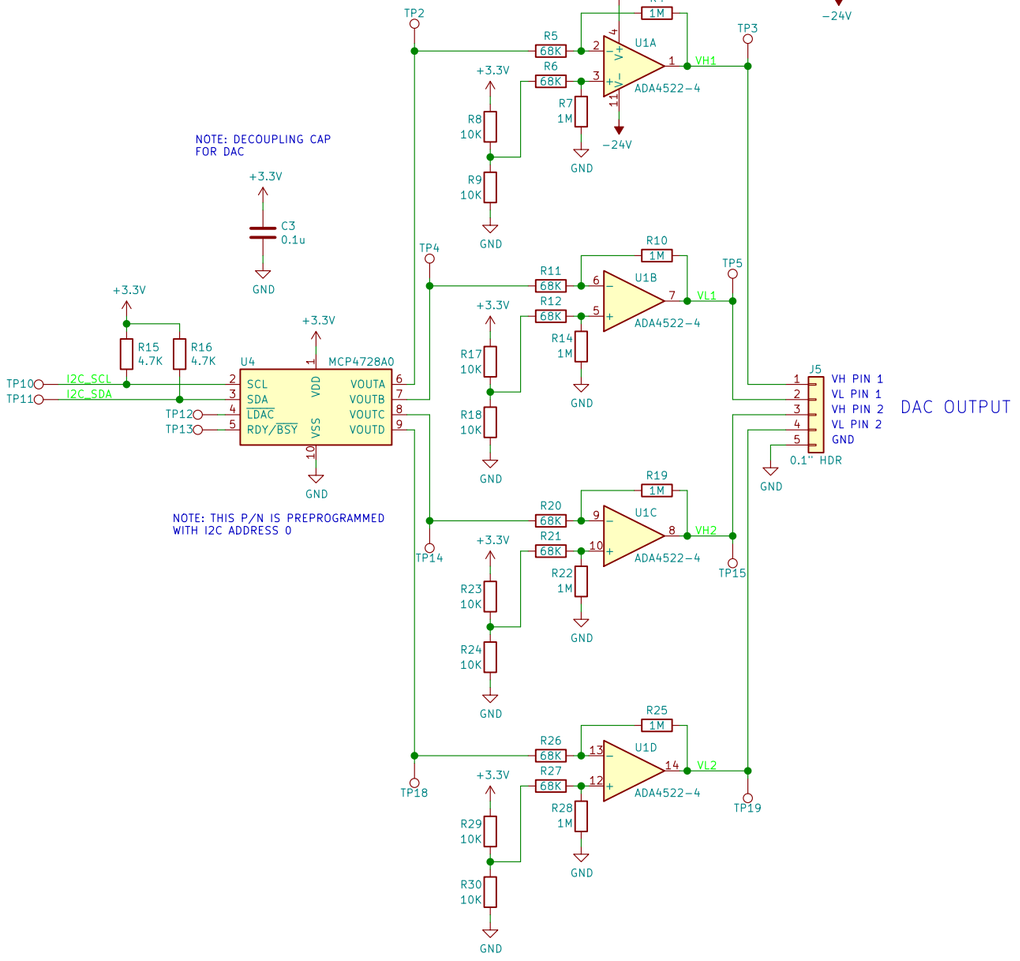


LOGIC LEVEL GENERATOR

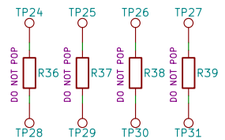
NOTE: DECOUPLING CAPS FOR OP−AMP



VH1 = 1000/68 * (1.65V − Vdac)



DAC OUTPUT



Sheet: /	
File: device−config−demo.sch	
Title: DEVICE CONFIGURATION DEMO BOARD	
Size: B	Date: 2019−11−05
KiCad E.D.A. kicad 5.1.4	Rev:
	Id: 1/1