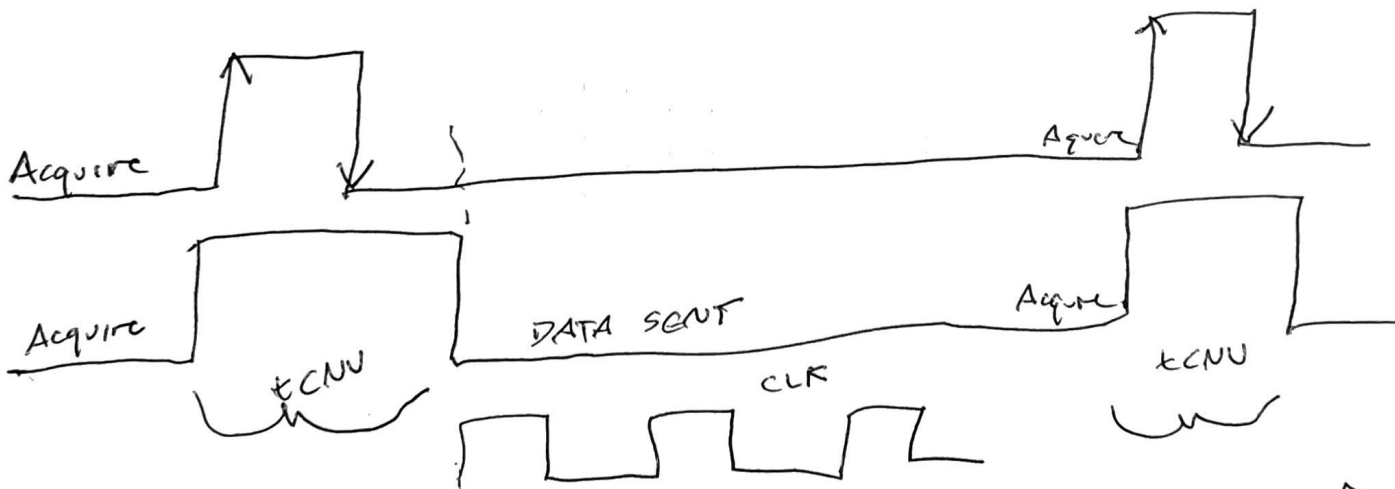


"BUSY MODE" SPI held HIGH



DATA SENT AT CS↓  
BIT 14 =  $\emptyset$

TCNV is fixed by IC  
(user can't change it)



DATA SENT AT TCNV FINISH. BIT 14 = 1

IF YOU TIE SPI LOW IT WORKS THE SAME BUT  
BITS 15, 14 can be ignored, they are always zero.