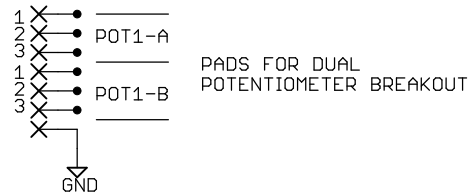


A circuit diagram showing two capacitors, C3 and C4, connected in parallel. C3 is labeled with a value of 0.1  $\mu\text{F}$  and C4 is labeled with a value of 4.7  $\mu\text{F}$ . Both capacitors are connected between a common VCC supply line and a common GND (ground) line.



**CYCFI**

Sheet: 1/1