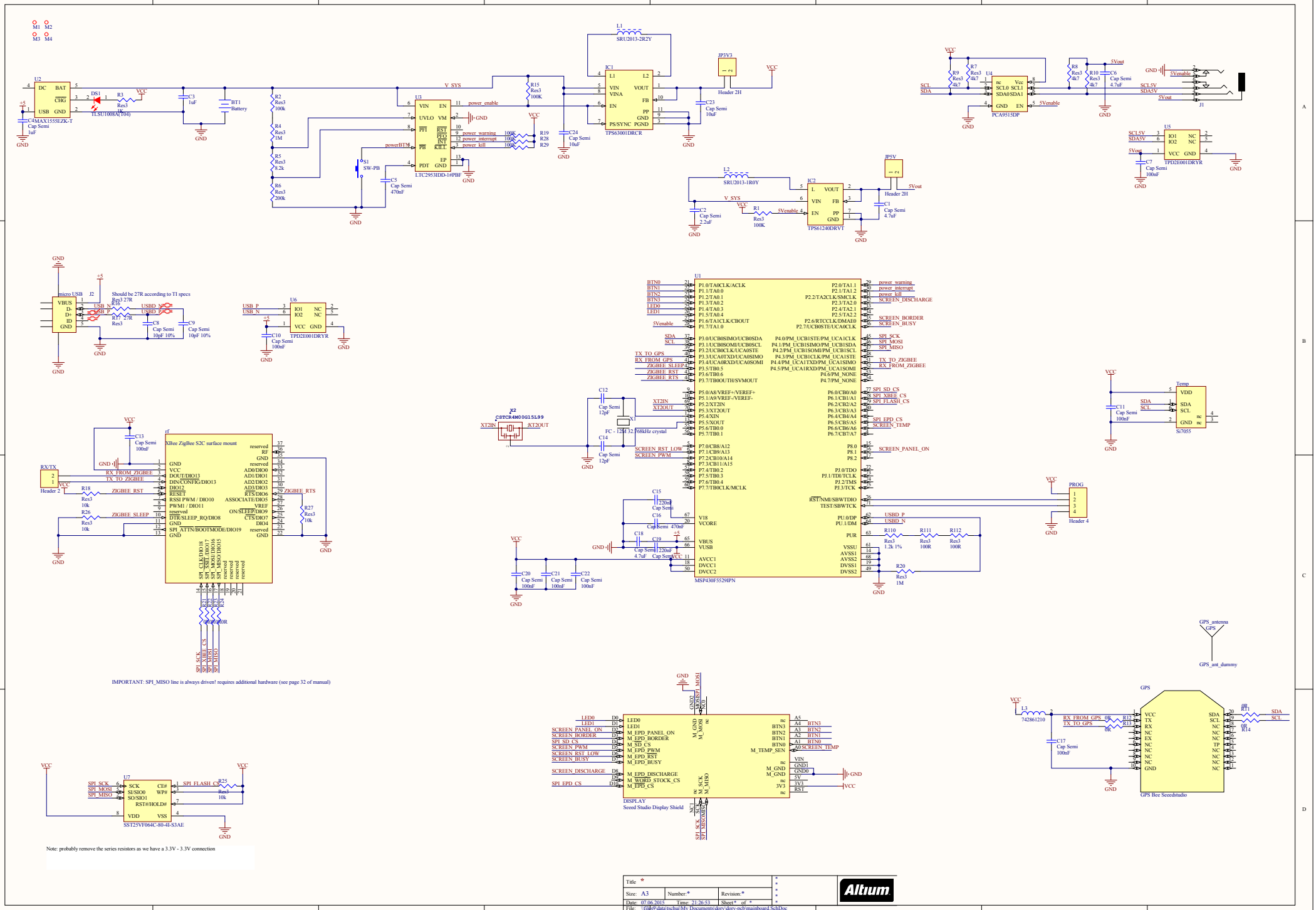
U2
MAX1555EZK-T
Cap Semi 1uF
DS1
TLSU1008A(104)
R3 Res3 1K
C3 1uF
BT1 Battery
R2 Res3 100k
R4 Res3 1M
R5 Res3 8.2k
R6 Res3 200k
U3
LTC2953DD-1#PBF
S1 SW-PB
C5 Cap Semi 470nF
V_SYS
power_enable
power_warning
power_interrupt
power_kill
R15 Res3 100K
R19 100k
R28 100k
R29 100k
C24 Cap Semi 10uF
L1 SRU2013-2R2Y
IC1
TPS63001DRCR
JP3V3
Header 2H
C23 Cap Semi 10uF
U4
PCA9515DP
R7 Res3 4k7
R9 Res3 4k7
R8 Res3 4k7
R10 Res3 4k7
C6 Cap Semi 4.7uF
SCL5V
SDA5V
5Venable
J1
U5
TPD2E001DRVR
C7 Cap Semi 100nF
L2 SRU2013-1R0Y
IC2
TPS61240DRVT
JP5V
C1 Cap Semi 4.7uF
C2 Cap Semi 2.2uF
R1 Res3 100K
micro USB J2
Should be 27R according to TI specs
R16 Res3 27R
R17 Res3 27R
C8 Cap Semi 10pF 10%
C9 Cap Semi 10pF 10%
U6
TPD2E001DRVR
C10 Cap Semi 100nF
U1
MSP430F55291PN
C12 Cap Semi 12pF
C14 Cap Semi 12pF
X1 FC - 12M 32.768kHz crystal
X2 CSTCE4M00G15L99
XT2IN
XT2OUT
C15 Cap Semi 220nF
C16 Cap Semi 470nF
C18 Cap Semi 4.7uF
C19 Cap Semi 220nF
C20 Cap Semi 100nF
C21 Cap Semi 100nF
C22 Cap Semi 100nF
R110 Res3 1.2k 1%
R111 Res3 100R
R112 Res3 100R
R20 Res3 1M
PROG Header 4
U7
SST25VF064C-80-4I-S3AE
R25 Res3 10k
Temp
Si7055
C11 Cap Semi 100nF
rf
XBee ZigBee S2C surface mount
RX/TX
Header 2
R18 Res3 10k
R26 Res3 10k
R27 Res3 10k
C13 Cap Semi 100nF
IMPORTANT: SPI_MISO line is always driven! requires additional hardware (see page 32 of manual)
GPS_antenna
GPS_ant_dummy
GPS
GPS Bee Seeedstudio
L3 742861210
C17 Cap Semi 100nF
DISPLAY
Seeed Studio Display Shield
Note: probably remove the series resistors as we have a 3.3V - 3.3V connection
Title *
Size: A3
Number:*
Revision:*
Date: 07.06.2015
Time: 21:26:53
Sheet* of *
File: \\fileserv-data\tschur\My Documents\dory\dory-pcb\mainboard.SchDoc
Altium