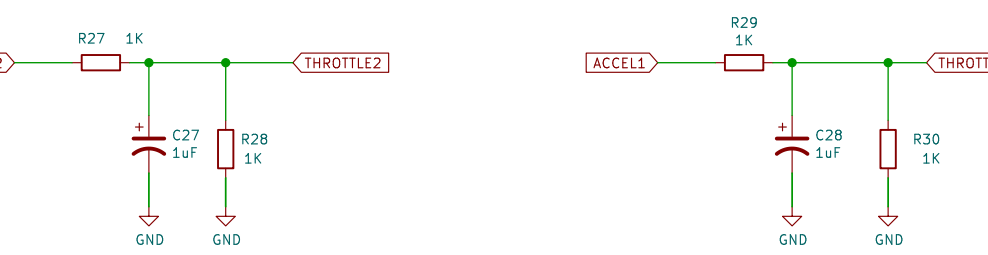
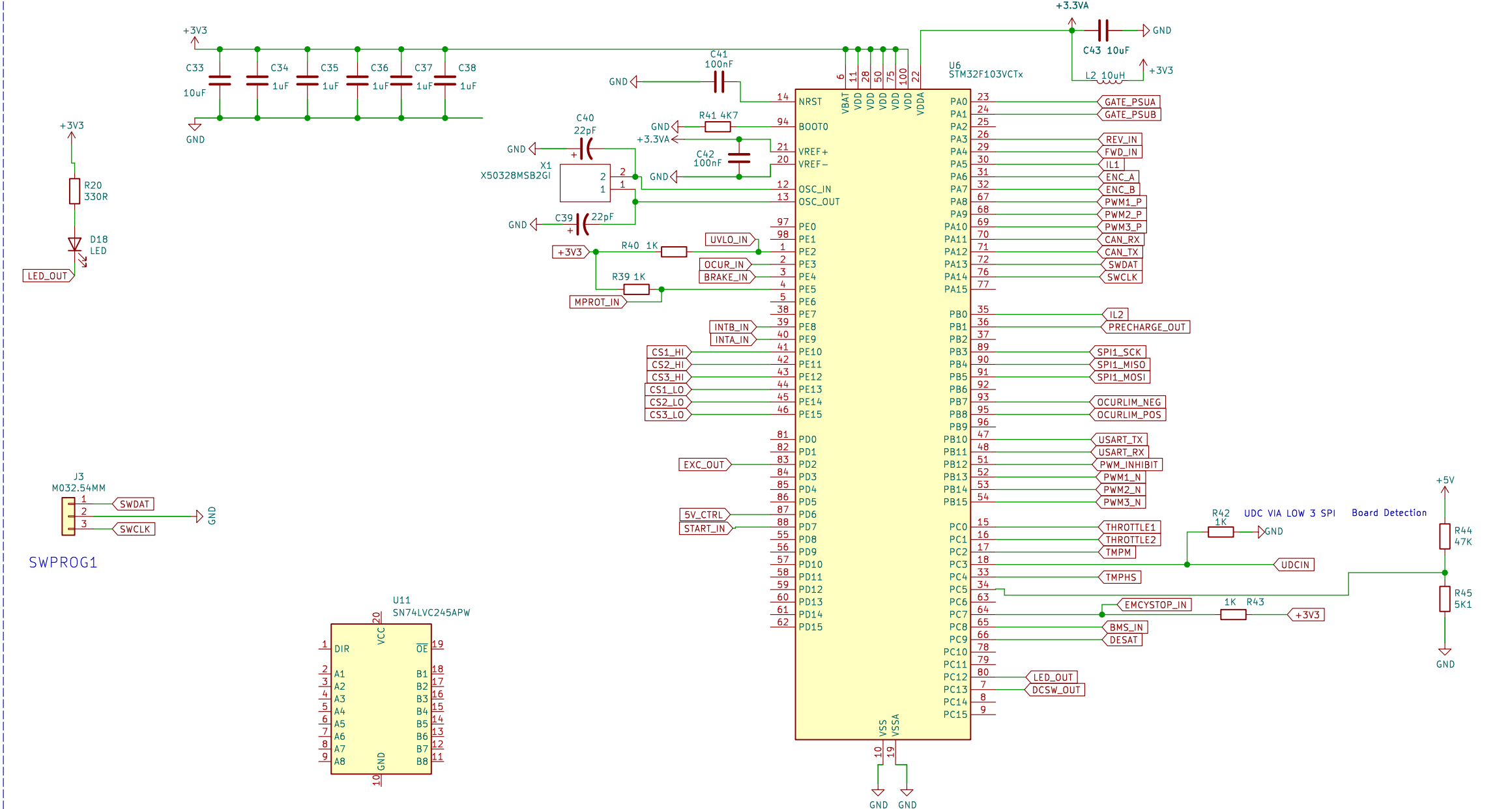
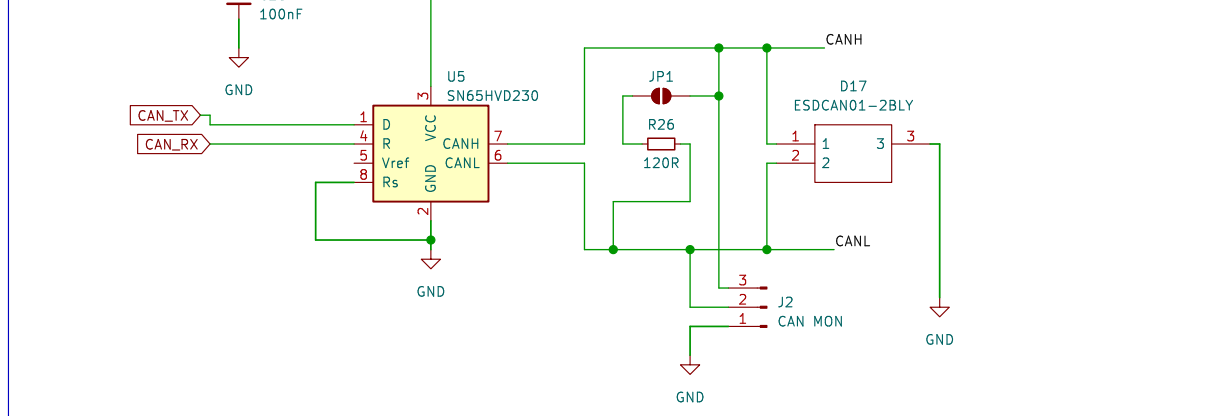
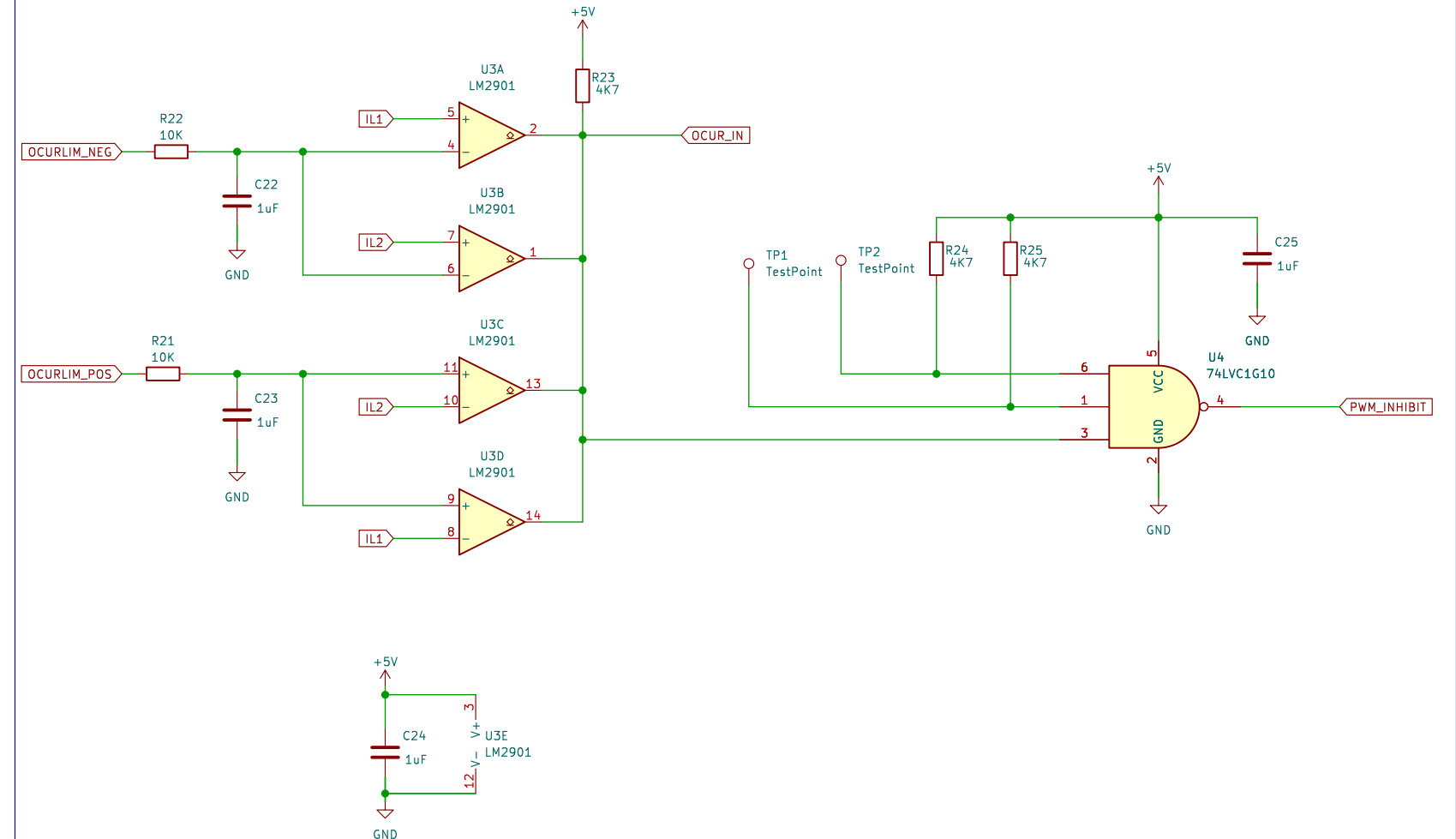
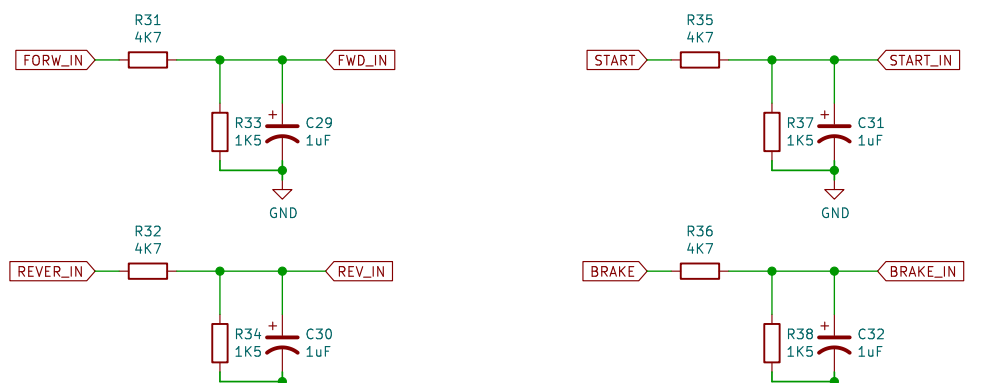
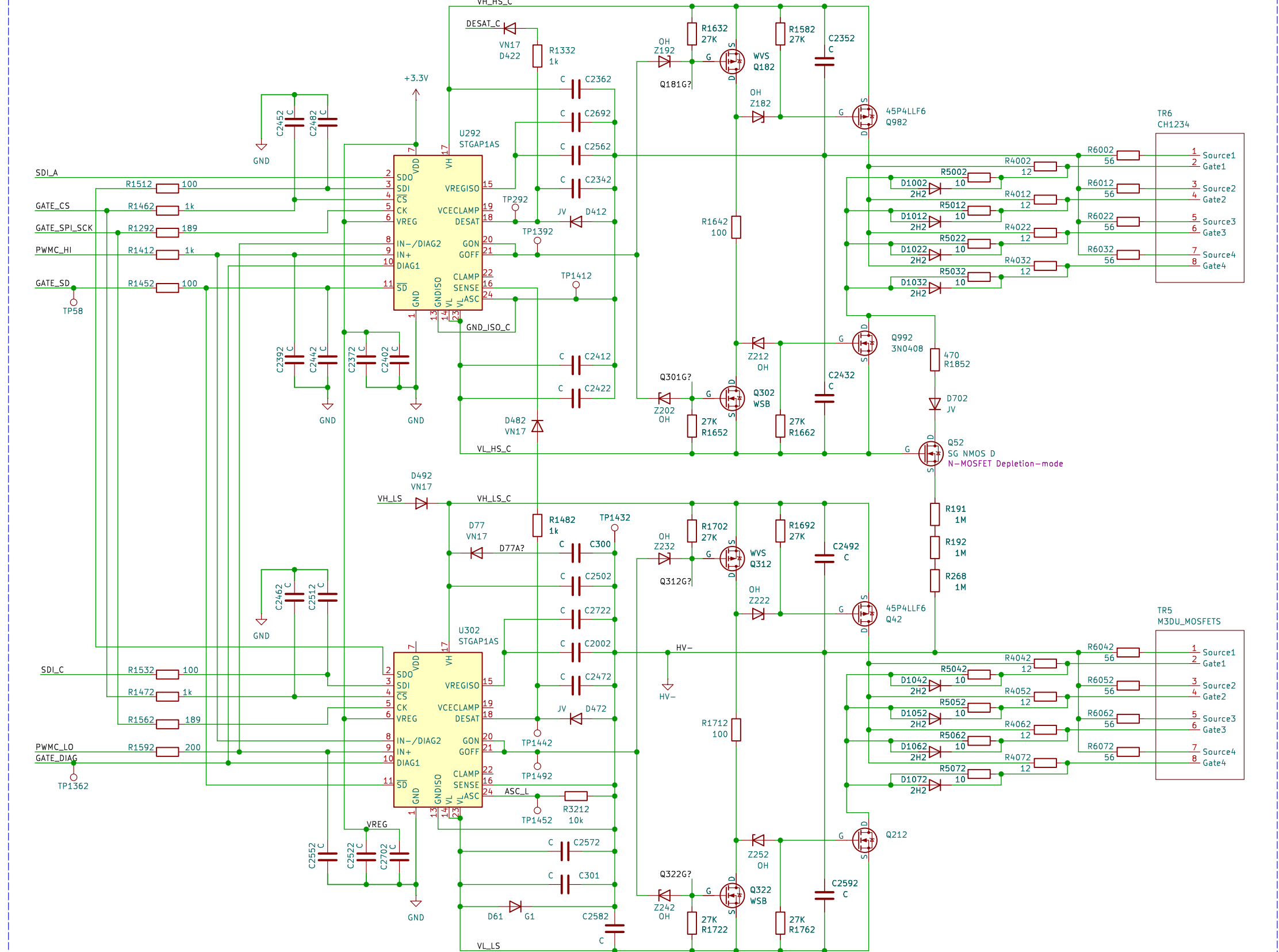


THROTTLE INPUTS

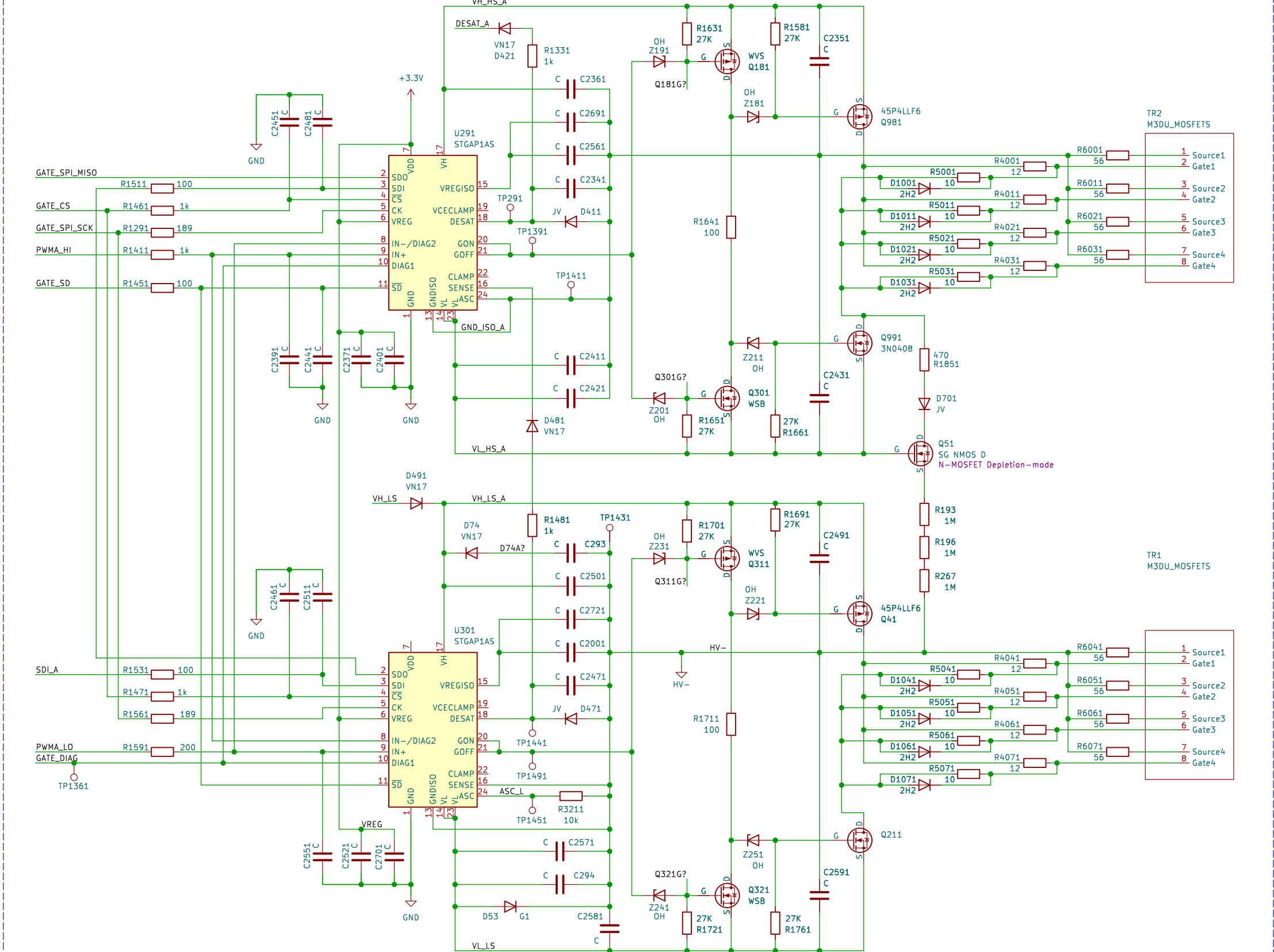
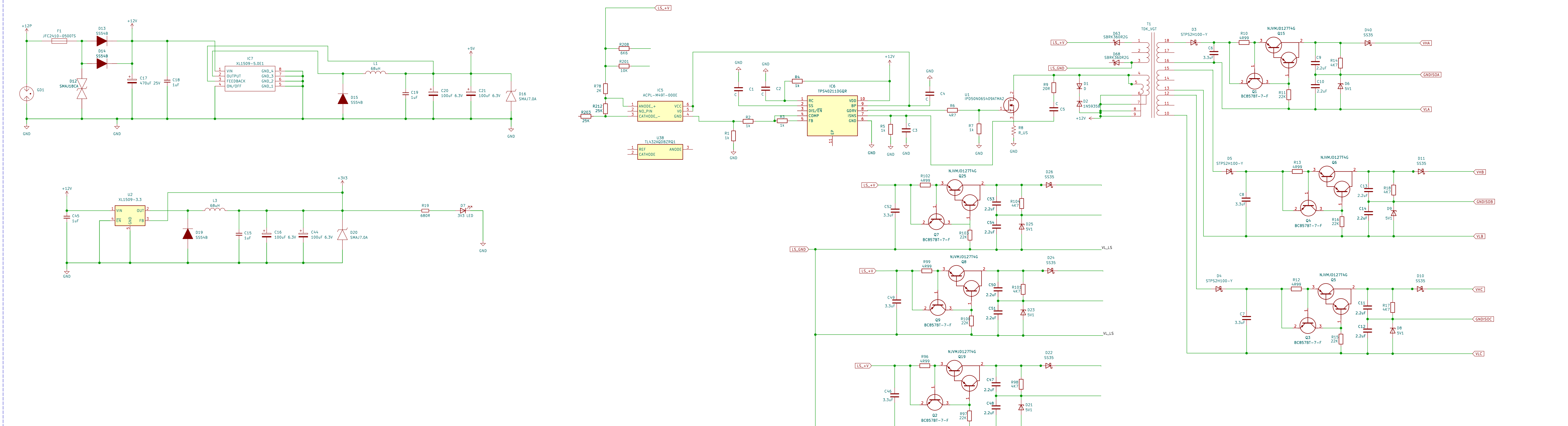
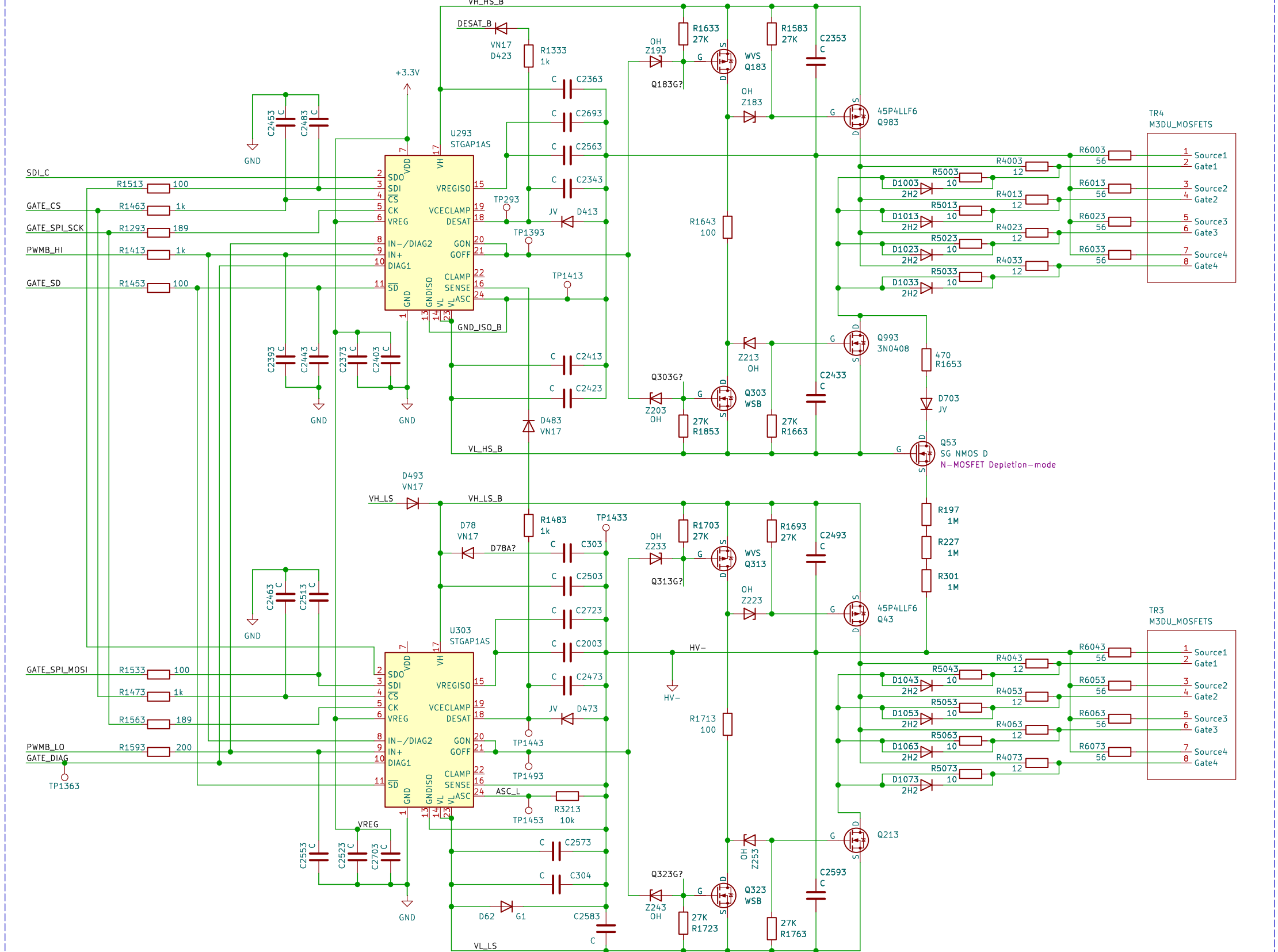


The figure contains two circuit diagrams for a 74138 decoder. In both, the A1 input is connected to a 10K resistor and a 5V supply. In the left diagram, A2 is connected to 5V and A3 to 0V. In the right diagram, A2 is connected to 0V and A3 to 5V.

[illegible]

Gate Driver, Phase A

The schematic diagram illustrates a 3-phase gate driver circuit for a 100V MOSFET. It consists of three identical gate driver stages for phases A, B, and C. Each stage includes a MOSFET (IRF540), a diode (1N4007), and a 100V MOSFET (IRF540). The MOSFETs are driven by a 10V logic signal. The diodes are connected in anti-parallel to the MOSFETs. The 100V MOSFETs are connected to a 100V supply. The diagram is labeled 'Gate Driver, Phase A'.

[illegible]

CURRENT SENSORS

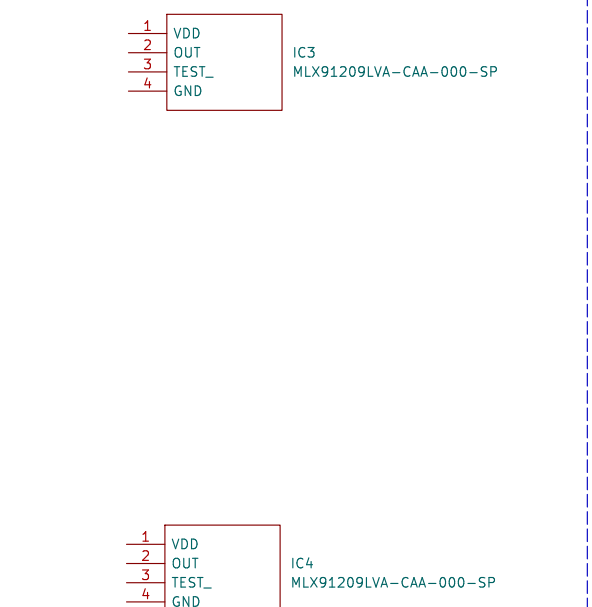


Diagram of a 12-pin connector with pin numbers 1-12 and labels for each pin:

- 1: GND
- 2: +5V
- 3: +5V
- 4: +5V
- 5: +5V
- 6: +5V
- 7: +5V
- 8: +5V
- 9: +5V
- 10: +5V
- 11: +5V
- 12: +5V

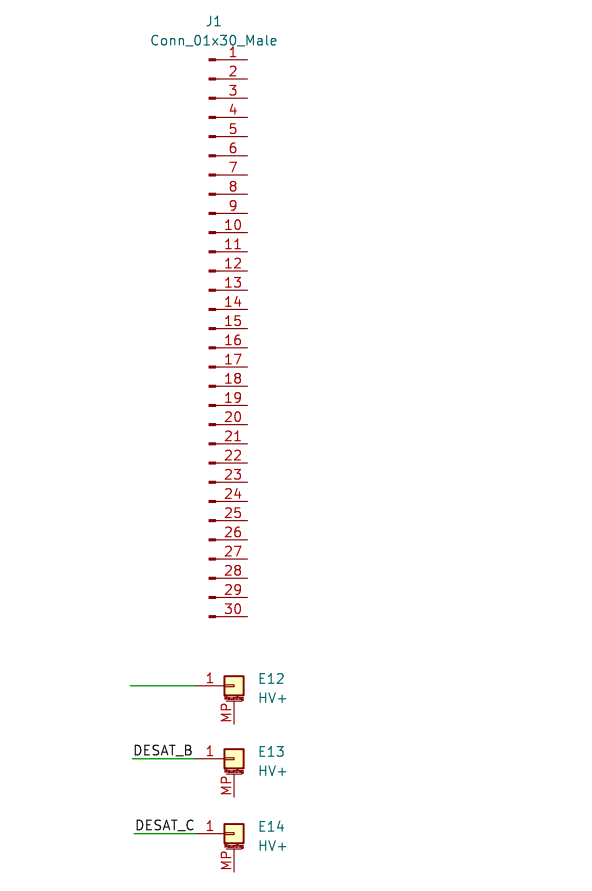
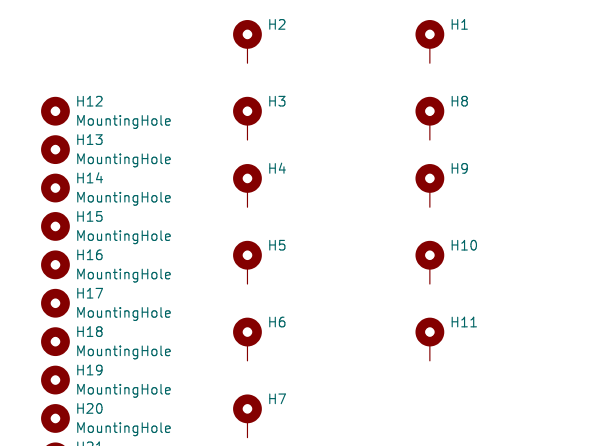


Diagram showing the mounting holes for the 12-pin D-sub connector on the rear panel. The diagram includes a 2x6 grid of holes with labels 1 through 12. Holes 1-6 are on the left, and holes 7-12 are on the right. Each hole is labeled with a number and a text label: 1: 12, 2: 12, 3: 12, 4: 12, 5: 12, 6: 12, 7: 12, 8: 12, 9: 12, 10: 12, 11: 12, 12: 12.



Legend:

- VLN₁ – Positive power supply for HV section, High Side, Phase A
- VLN₂ – Negative power supply for HV section, High Side, Phase A
- VLS₁ – Positive power supply for HV section, Low Side, Phase A
- VLS₂ – Negative power supply for HV section, Low Side, Phase A
- VH₁ – Positive power supply for HV section, High Side, Phase B
- VH₂ – Negative power supply for HV section, High Side, Phase B
- VLS₃ – Positive power supply for HV section, Low Side, Phase C
- VLS₄ – Negative power supply for HV section, Low Side, Phase C
- VH₃ – Positive power supply for HV section, High Side, Phase C
- VH₄ – Negative power supply for HV section, High Side, Phase C
- VLS₅ – Positive power supply for HV section, Low Side, Phase C
- VLS₆ – Negative power supply for HV section, Low Side, Phase C
- VH₅ – Positive power supply for HV section, Low Side, Phase C
- VH₆ – Negative power supply for HV section, Low Side, Phase C
- PMW_{IN} – Gate command input, Phase A, Positive
- PMW_{IN} – Gate command input, Phase A, Negative
- PMW_{IN} – Gate command input, Phase B, Positive
- PMW_{IN} – Gate command input, Phase B, Negative
- PMW_{IN} – Gate command input, Phase C, Positive
- PMW_{IN} – Gate command input, Phase C, Negative