CS4223 Multi-core Architectures

From ILP to TLP

National University of Singapore
2023 – 2024 School Year, Semester 1 (August 2023 – December 2023)
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Office hours by request

Overview

This Multi-core Architectures class is an undergraduate-level class that focuses on foundational topics that include both advanced single-core and multi-core systems. The goal of this class is to guide students on these fundamental topics, build up knowledge in these topics with project-based assignments, and to expose research-paper based source materials to begin a journey into architecture research. This course will take you through a number of topics, from modern CPU architectures to accelerators, and will ask you to work with tools to build up a level of expertise for new topics and software.

Goals

- Understanding foundational topics in advanced single-core and multi-core parallelism
- Implement key designs and initiate the research process
    - Understand simulation, sampling, and investigation of system parameters
    - Go in-depth with coherence with a high-level simulator implementation
- Develop computer architecture paper reading skills and better understand research papers
    - Review source material and provide an analysis of the paper structure
    - Be able to summarize key research results, better understand yourself as a paper reader, and to actively choose your reading type depending on the purpose

Class Meetings

Mondays, 14:00-16:00, In person (COM4-SR3132) and potentially online via Zoom

Class Topics

- ILP exploitation via superscalar and VLIW, caches
- DLP exploitation via vector processors
- TLP exploitation via multi-core
    - Cache Coherence
    - Memory Consistency
    - Synchronization
- Power/Energy issues
- GPUs and AI Accelerators

Grading Criteria

- Project / Assignments (50%)
  - Project 1 – ILP Identification (20%)
  - Project 2 – Build-your-own Simulator: Cache Coherence (30%)
- In-class Tests (45%)
  - A number of short (<=1 hour) in-class tests to reinforce learned knowledge. All tests can contain material since the beginning of class.
- Participation and preparation for class (5%)
  - Participation (5%)

Important deadlines and test dates (subject to updates)

- Programming Assignments
  - Assignment 1 due by end-of-day on Friday, September 8th
  - Assignment 2 due by end-of-day on Friday, November 17th
- Tests
  - Test 1 – ILP on August 28th
  - Test 2 – Caches on September 4th
  - Test 3 – Coherence on October 2nd
  - Test 4 – TLP/Synch on October 9th
  - Test 5 – Consistency on October 30st
  - Test 6 – DLP on November 6th
- Tutorials
  - Tutorial 1 – ILP on August 29th
  - Tutorial 2 – Caches on September 5th
  - Tutorial 3 – Coherence on September 19th
  - Tutorial 4 – Synchronization on October 3rd
  - Tutorial 5 – Consistency on October 10th
  - Tutorial 6 – DLP on October 17th

						August
Wk	Topic	Mon	Tues	Wed	Thurs	Fri
1	Introduction	14	15	16	17	18
2	ILP 1	21	22	23	24	25
3	ILP 2	28	29 Tutorial: ILP	30	31	1
						September
4	Caches	4	5 Test: ILP Tutorial: Caches	6	7	8 Programming Assignment 1
5	TLP, Multicore	11	12 Test: Caches	13	14	15
6	Cache Coherence	18	19 Tutorial: Coh.	20	21	22

R	Reading	25 Week	26 Reading	27 Week	28 Reading	29 Week
						October
7	Synchronization	2	3 Tutorial: Synch.	4	5	6
8	Consistency	9	10 Test: Coherence Tutorial: Consist.	11	12	13
9	DLP	16	17 Test: TLP/Synch.	18	19	20
10	DLP 2	23	24 Tutorial: DLP	25	26	27
11	TBD	30	31	1	2	3
						November
12	GPUs	6	7 Test: Consist.	8	9	10 NUS Holiday
13	ML/DNN Accelerators	13 Holiday	14 Test: DLP	15	16	17 Programming Assignment 2