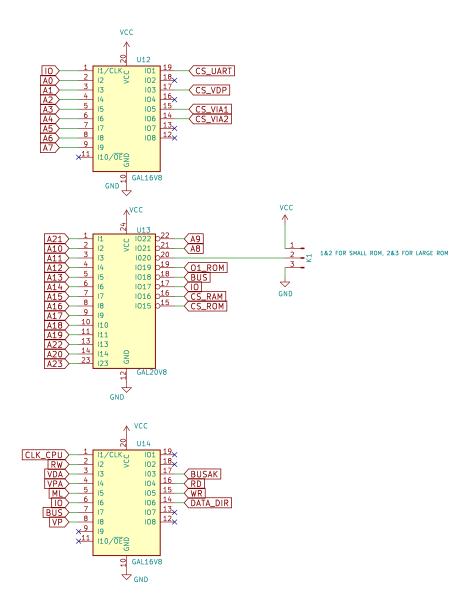
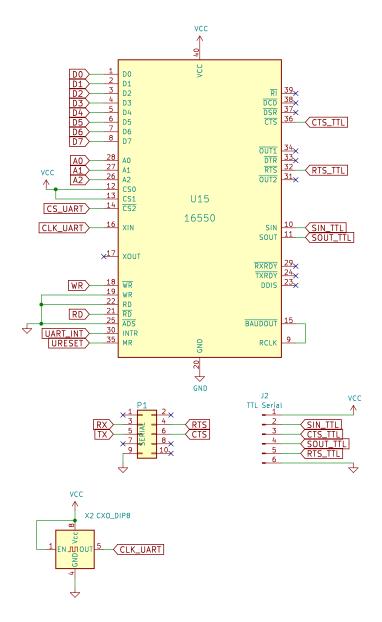


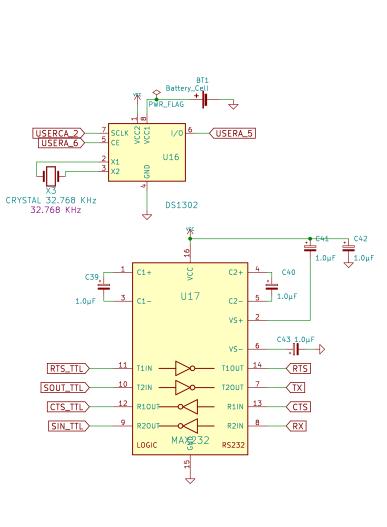
C47 IS OPTIONAL.
MOST MONITORS ARE
CAPACITIVELY COUPLED
ALREADY.

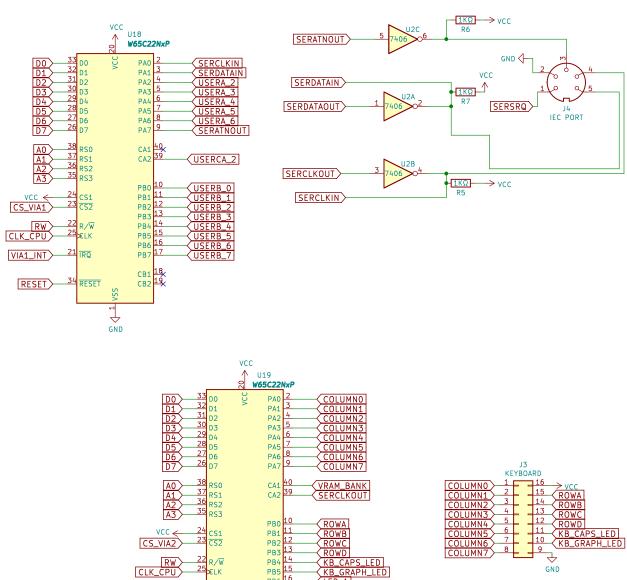
CS\_VDP>



## SERIAL 10

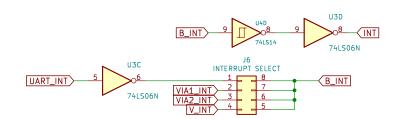


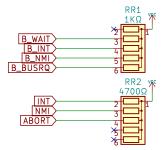




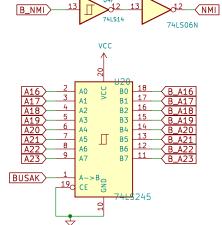
VIA2\_INT>

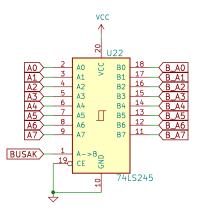
RESET

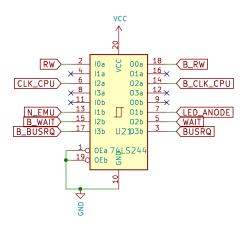


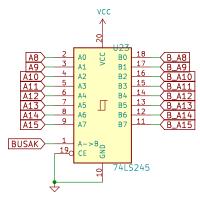


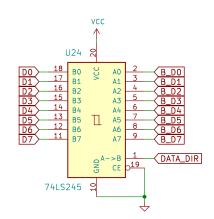
## BUS BUFFERING

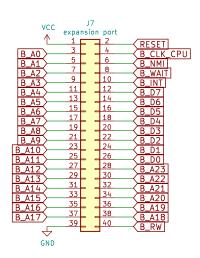


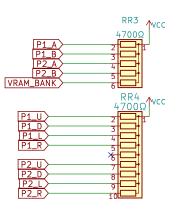


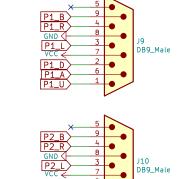




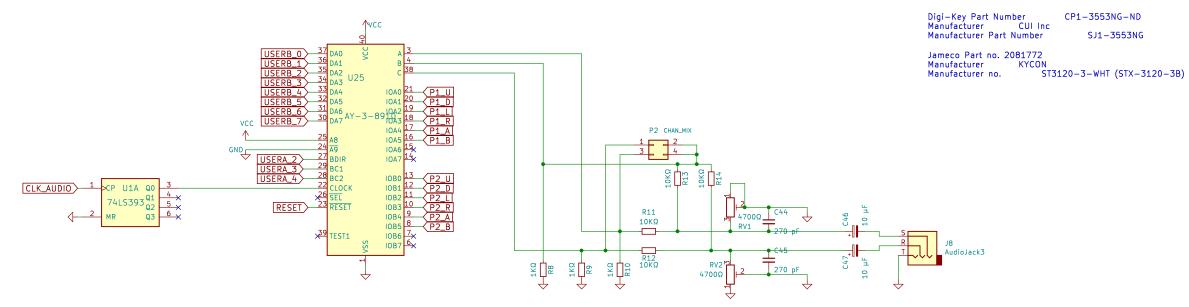


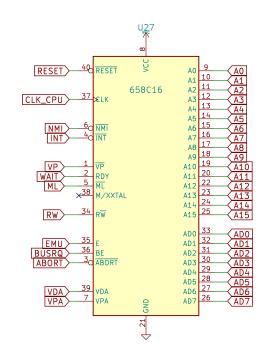


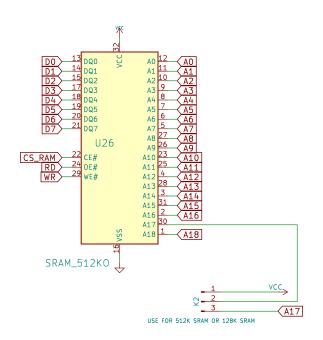




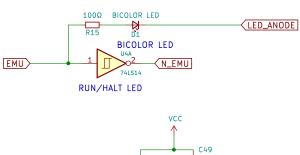
CP1-3553NG-ND SJ1-3553NG

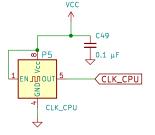


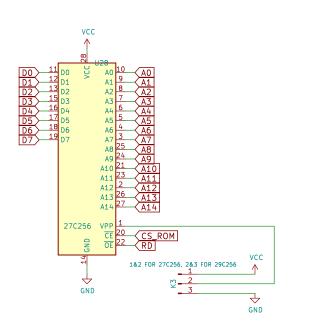


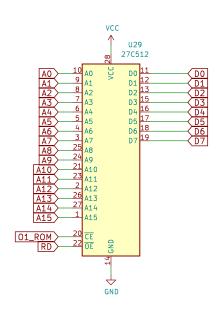


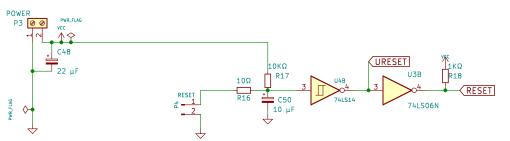
## CPU, CLOCK, AND MEMORY











RESET SWITCH / POWER ON RESET / POWER SWITCH

