## Makefile-bt.txt

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# File: Makefile
# Copyright (C) 2007-2010, Marvell International Ltd.
               $(CROSS_COMPILE)gcc
CC=
               $ (CROSS_COMPILE) 1d
LD=
BACKUP=
               /root/backup
YMD=
                date +%Y%m%d%H%M
# Configuration Options
# Debug Option
# DEBUG LEVEL n/1/2:
# n: NO DEBUG
\sharp 1: PRINTM(MSG,...), PRINTM(FATAL,...), PRINTM(WARN,...) and PRINTM(INFO,...)
# 2: All PRINTM()
CONFIG_DEBUG=1
# PXA3XX DMA alignment
CONFIG_PXA3XX_DMA_ALIGN=y
# SDIO suspend/resume
CONFIG_SDIO_SUSPEND_RESUME=y
# Select Platform Tools
MODEXT = ko
# KERNELDIR point to the installed kernel directory
 for PXA3XX BSP.
# KERNELDIR can be set on the command line
 \begin{tabular}{ll} \# \ make \ KERNELDIR = /usr/src/arm/\langle arch-bsp-path \rangle \\ \end{tabular} 
# Alternatively KERNELDIR can be set in the environment.
# Default value for KERNELDIR is set below.
KERNELDIR ?= /usr/src/arm/linux-2.6.29-pxa920
\# CROSS_COMPILE specify the prefix used for all executables used \# during compilation. Only gcc and related bin-utils executables
# CROSS_COMPILE can be set on the command line
# make CROSS COMPILE=</usr/local/arm/4.1.1/bin/>arm-linux-
# Alternatively CROSS_COMPILE can be set in the environment.
\# Default value for CROSS_COMPILE is set below.
CROSS_COMPILE ?= /usr/local/arm-marvell-linux-gnueabi/bin/arm-marvell-linux-gnueabi-
\# INSTALLDIR specify the path to install the kernel module after \# succesful compilation.
# INSTALLDIR can be set on the command line
 make INSTALLDIR=/tftpboot/<rootfs>
# Alternatively INSTALLDIR can be set in the environment.
# Default value for INSTALL is set below.
INSTALLDIR ?= /tftpboot/pxa3xx/root
# ARCH specifies the architecture of the target processor, this kernel
# module will run.
\# ARCH can be set on the command line \# make ARCH=<\!\!\mathrm{arm}/\mathrm{i}386\!\!>
# Alternatively ARCH can be set in the environment
# Default values of ARCH for specific platform are set below.
ARCH ?= arm
EXTRA CFLAGS += -I$ (KERNELDIR) / include
EXTRA\_CFLAGS += -I\$(PWD)/bt
BINDIR = ../bin_sd8787_bt
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# Compiler Flags
EXTRA_CFLAGS += -DFPNUM=' "57"
ifeq ($(CONFIG_DEBUG), 1)
        EXTRA_CFLAGS += -DDEBUG_LEVEL1
endif
ifeq ($(CONFIG_DEBUG), 2)
        EXTRA_CFLAGS += -DDEBUG_LEVEL1
EXTRA_CFLAGS += -DDEBUG_LEVEL2
                -dbg
endif
ifeq ($(CONFIG_PXA3XX_DMA_ALIGN), y)
    EXTRA_GFLAGS += -DPXA3XX_DMA_ALIGN
endif
ifeq ($(CONFIG_SDIO_SUSPEND_RESUME),y)
    EXTRA_CFLAGS += -DSDIO_SUSPEND_RESUME
endif
# Make Targets
ifneq ($(KERNELRELEASE),)
BTOBJS = bt/bt_main.o bt/bt_sdiommc.o bt/bt_proc.o
obj-m := bt8xxx.o
bt8xxx-objs := $(BT0BJS)
# Otherwise we were called directly from the command line; invoke the kernel build system.
else
default:
        $(MAKE) -C $(KERNELDIR) M=$(PWD) ARCH=$(ARCH) CROSS_COMPILE=$(CROSS_COMPILE) modules
endif
CC LD EXTRA_CFLAGS KERNELDIR
export
echo:
build:
                echo default
        cp -f bt8xxx. $(MODEXT) $(BINDIR)/bt8787$(DBG).$(MODEXT)
        cp -f README $(BINDIR)
clean:
       -find . -name "*.o" -exec rm {} \;
-find . -name "*.ko" -exec rm {} \;
-find . -name ".*.cmd" -exec rm {} \;
-find . -name "*.mod.c" -exec rm {} \;
-find . -name "*.symvers" -exec rm {} \;
-find . -name "modules.order" -exec rm {} \;
        -rm -rf .tmp_versions
install: default
distclean:
       -find . -name "*.o" -exec rm {} \;
-find . -name "*.orig" -exec rm {} \;
-find . -name "*.swp" -exec rm {} \;
-find . -name "*.*" -exec rm {} \;
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Makefile-bt.txt
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Makefile-b
-find . -name "*\" -exec rm {}\;
-find . -name "*\d" -exec rm {}\;
-find . -name "*\d" -exec rm {}\;
-find . -name "*\a" -exec rm {}\;
-find . -name "tags" -exec rm {}\;
-find . -name ".\*" -exec rm -rf 2> /dev/null \;
-find . -name "\*\ko" -exec rm {}\;
-find . -name "\*\kod\c" -exec rm {}\;
-rm -rf .tmp_versions
# End of file;
```