Makefile..txt

 \sharp kbuild trick to avoid linker error. Can be omitted if a module is built. ob j- := dummy. o

List of programs to build
hostprogs-y := spidev_test spidev_fdx

Tell kbuild to always build the programs
always := \$(hostprogs-y)

 $\label{eq:hostcflags_spidev_test.o} \begin{array}{lll} \mbox{HOSTCFLAGS_spidev_test.o} & += -\mbox{I\$ (objtree)/usr/include} \\ \mbox{HOSTCFLAGS_spidev_fdx.o} & += -\mbox{I\$ (objtree)/usr/include} \\ \end{array}$