Schematic for @787 Module (WiFi/BT/FM RXD 1.For power supply 3.3V and 1.8V, the max current consumption is 300mA 2.Must use external sleep clock 32.768KHz(AVDD1.8V) during power save mode 3. The voltage level of WIFI_IO(1.8V/2.6V/3.3V) is depended on host controller IO level. C1 NL/C/1.5pF/50V/0402 WIFI_3V3 WIFI IO C4 C/104K/6.3VX5R/02 WIFI_1V8 WIFI_1V8 3V3_FM_PWR 3V3 BT_PCM_DIN BT_PCM_CLK TP1 BT_PCM_DOUT PCM_DIN PCM_CLK PCM_MCLK WIFI 1V8 C5 C/104K/6.3VX5R/0 C7 C6 C/105K/10V/X5R/0402 C/105K/10V/X5R/0402 VIO 1V8_RF 1V8_LDO 1V8 1V8_BT 1V8_FM_PWR 1V8_FM PCM_OUT PCM_SYNC WIFI 1V8 C8 RF OUT R2 R/100K/0201/5% RE PORT R3 R/0/0201/5% C9 SLEEP_CLK SLEEP CLK 32K WiFi_PDn WIFI_RST_N HOST_WAK_DEV WLAN_LED DEV_WAK_HOST RESETn SLEEPn WIFI IO REF_CLK_IN E8 × SD_D0 SD_D1 SD_D2 SD_D3 SD_CLK SD_CMD SD_D0 SD_D1 SD_D2 SD_D3 SD_CLK SD_CMD R/100K/0201/5% 1V2_IN 1V2_OUT R/0/0201/5% TMS TDO TDI TCK U/8787 module_J20H04 WIFI_IO Option R11 R/100K/0201/5% Reserve ANT matching circuit ANT C11 C/10pF/+-0.25pF/50V/0402 MM84302600B C12 C/10pF/+-0.25pF/50V/0402 PQ. 3 GND C GNDGND GND L3 (L4 NL/2.2nH/250mA/0402NL/2.2nH/250mA/0402 If SDIO host internal pull-high, no load R8/R9/RIU/R11/R12