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programlisting>
 * frontend.h
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 * Foundation, Inc., 59 Temple Place - Suite 330, Boston, MA 02111-1307, USA.
 */
#ifndef DVBFRONTEND H
#define DVBFRONTEND H
#include <linux/types.h&gt;
typedef enum fe type {
        FE QPSK,
        FE QAM,
        FE OFDM,
        FE ATSC
} fe_type_t;
typedef enum fe_caps {
                                         = 0,
        FE_IS_STUPID
        FE_CAN_INVERSION_AUTO
                                         = 0x1,
        FE_CAN_FEC_1_2
                                         = 0x2,
        FE_CAN_FEC_2_3
                                         = 0x4.
        FE CAN FEC 3 4
                                         = 0x8,
        FE CAN FEC 4 5
                                         = 0x10.
        FE CAN FEC 5 6
                                         = 0x20.
        FE CAN FEC 6 7
                                         = 0x40,
        FE CAN FEC 7 8
                                         = 0x80,
        FE_CAN_FEC_8_9
FE_CAN_FEC_AUTO
                                         = 0x100.
                                         = 0x200,
                                         = 0x400,
        FE_CAN_QPSK
        FE CAN QAM 16
                                         = 0x800,
        FE CAN QAM 32
                                         = 0x1000.
        FE CAN QAM 64
                                         = 0x2000,
                                         = 0x4000,
        FE CAN QAM 128
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        FE CAN QAM 256
                                          = 0x8000.
                                          = 0x10000,
        FE CAN QAM AUTO
        FE CAN TRANSMISSION MODE AUTO
                                          = 0x20000,
        FE CAN BANDWIDTH AUTO
                                          = 0x40000,
        FE CAN GUARD INTERVAL AUTO
                                          = 0x80000,
        FE CAN HIERARCHY AUTO
                                          = 0x100000,
        FE_CAN_8VSB
FE_CAN_16VSB
                                          = 0x200000.
                                          = 0x400000,
        FE HAS EXTENDED CAPS
                                          = 0x800000,
                                                         /* We need more bitspace
for newer APIs, indicate this. */
FE_CAN_2G_MODULATION
                                          = 0x10000000, /* frontend supports "2nd
generation modulation" (DVB-S2) */
        FE NEEDS BENDING
                                          = 0x20000000, /* not supported anymore,
don't use (frontend requires frequency bending) */
        FE CAN RECOVER
                                          = 0x40000000, /* frontend can recover
from a cable unplug automatically */
        FE CAN MUTE TS
                                          = 0x80000000 /* frontend can stop
spurious TS data output */
} fe caps t;
struct dvb_frontend_info {
                   name[128];
        char
        fe type_t
                    type;
          u32
                    frequency min;
          u32
                    frequency max;
                    frequency stepsize;
          u32
                    frequency tolerance;
          u32
          u32
                    symbol_rate_min;
         u32
                    symbol_rate_max;
                                                 /* ppm */
         u32
                    symbol rate tolerance;
                                                  /* DEPRECATED */
          u32
                    notifier delay;
        fe_caps_t
                   caps;
};
/**
   Check out the DiSEqC bus spec available on http://www.eutelsat.org/ for
   the meaning of this struct...
struct dvb_diseqc_master_cmd {
        __u8 msg [6];
                       /* { framing, address, command, data [3] } */
                         /* valid values are 3...6 */
        u8 msg len;
};
struct dvb_diseqc_slave_reply {
        \overline{u}8 \text{ msg } [4];
                             { framing, data [3] } */
                         /*
                         /* valid values are 0...4, 0 means no msg */
          u8 msg len;
        int timeout:
                         /* return from ioctl after timeout ms with */
                            errorcode when no message was received */
};
typedef enum fe_sec_voltage {
        SEC_VOLTAGE 13,
        SEC_VOLTAGE_18,
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SEC VOLTAGE OFF
} fe_sec_voltage_t;
typedef enum fe sec tone mode {
         SEC_TONE_OFF
} fe sec tone mode t;
typedef enum fe_sec_mini_cmd {
         SEC MINI A,
         SEC MINI B
} fe sec mini cmd t;
typedef enum fe status {
         FE_HAS_SIGNAL
                                         /* found something above the noise level */
                            = 0x01,
         FE HAS CARRIER
                            = 0x02,
                                         /* found a DVB signal
         FE_HAS_VITERBI
FE_HAS_SYNC
                            = 0x04,
                                         /* FEC is stable */
                                        /* found sync bytes */
/* everything's working... */
/* no lock within the last ~2 seconds */
                            = 0x08,
         FE_HAS_LOCK
                            = 0x10,
         FE_TIMEDOUT
                            = 0x20,
         FE REINIT
                            = 0x40
                                         /* frontend was reinitialized,
} fe status t;
                                         /* application is recommended to reset */
                                         /* DiSEqC, tone and parameters */
typedef enum fe_spectral_inversion {
          INVERSION_OFF,
         INVERSION_ON,
         INVERSION AUTO
} fe spectral inversion t;
typedef enum fe_code_rate {
    FEC_NONE = 0,
    FEC_1_2,
         FEC_2_3,
         FEC_3_4,
         FEC_4_5,
         FEC_4_5,
FEC_5_6,
FEC_6_7,
FEC_7_8,
FEC_8_9,
         FEC_AUTO,
         FEC 3 5,
         FEC 9 10,
} fe_code_rate_t;
typedef enum fe_modulation {
         QPSK,
         QAM_16,
         QAM_32,
         QAM 64,
         QAM 128,
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QAM_256,
         QAM_AUTO,
         VSB 8,
         VSB 16,
         PSK<sup>8</sup>,
         APSK_16,
APSK_32,
         DQPSK,
} fe modulation t;
typedef enum fe_transmit_mode {
         TRANSMISSION_MODE_2K,
         TRANSMISSION_MODE_8K, TRANSMISSION_MODE_AUTO,
         TRANSMISSION MODE 4K
} fe transmit mode t;
typedef enum fe bandwidth {
         BANDWIDTH_8_MHZ,
         BANDWIDTH_7_MHZ,
BANDWIDTH_6_MHZ,
         BANDWIDTH AUTO
} fe bandwidth t;
typedef enum fe_guard_interval {
         GUARD_INTERVAL_1_32,
GUARD_INTERVAL_1_16,
GUARD_INTERVAL_1_8,
         GUARD_INTERVAL_1_4,
         GUARD_INTERVAL_AUTO
} fe guard interval t;
typedef enum fe_hierarchy {
         HIERARCHY_NONE,
         HIERARCHY_1,
         HIERARCHY 2,
         HIERARCHY 4,
         HIERARCHY_AUTO
} fe_hierarchy_t;
struct dvb_qpsk_parameters {
                            symbol rate; /* symbol rate in Symbols per second */
         fe code_rate_t fec_inner;
                                             /* forward error correction (see above) */
}:
struct dvb_qam_parameters {
          _u32
                            symbol_rate; /* symbol rate in Symbols per second */
         fe_code_rate_t fec_inner; /* forward error correction (see above) */
fe_modulation_t modulation; /* modulation type (see above) */
};
struct dvb vsb parameters {
         fe modulation t modulation; /* modulation type (see above) */
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};
struct dvb ofdm parameters
         fe bandwidth t
                               bandwidth:
         fe_code_rate_t
                                code rate HP;
                                               /* high priority stream code rate */
         fe_code_rate_t
                               code_rate_LP; /* low priority stream code rate */
constellation; /* modulation type (see above) */
         fe modulation t
         fe transmit mode t
                               transmission mode;
         fe guard interval t guard interval;
                               hierarchy information;
         fe hierarchy t
};
struct dvb frontend parameters {
                                 /* (absolute) frequency in Hz for QAM/OFDM/ATSC */
         u32 frequency;
                                 /* intermediate frequency in kHz for QPSK */
         fe spectral inversion t inversion;
         union {
                  struct dvb qpsk parameters qpsk;
                  struct dvb gam parameters gam;
                  struct dvb ofdm parameters ofdm;
                  struct dvb_vsb_parameters vsb;
         } u;
};
struct dvb frontend event {
         fe status t status;
         struct dvb frontend parameters parameters;
};
/* S2API Commands */
#define DTV UNDEFINED
                                    0
#define DTV_TUNE
                                    1
#define DTV_CLEAR
#define DTV_FREQUENCY
                                    2
                                    3
#define DTV_MODULATION
                                    4
                                    5
#define DTV BANDWIDTH HZ
                                    6
7
#define DTV INVERSION
#define DTV_DISEQC_MASTER
#define DTV_SYMBOL_RATE
#define DTV_INNER_FEC
#define DTV_VOLTAGE
#define DTV_TONE
                                    8
                                    9
                                    10
                                    11
#define DTV PILOT
                                    12
#define DTV ROLLOFF
                                    13
#define DTV DISEQC SLAVE REPLY
                                   14
/* Basic enumeration set for querying unlimited capabilities */
#define DTV_FE_CAPABILITY_COUNT 15
#define DTV_FE_CAPABILITY
                                    16
#define DTV_DELIVERY_SYSTEM
                                    17
/* ISDB-T and ISDB-Tsb */
#define DTV ISDBT PARTIAL RECEPTION
#define DTV ISDBT SOUND BROADCASTING
                                             19
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#define DTV ISDBT SB SUBCHANNEL ID
                                                20
#define DTV ISDBT SB SEGMENT IDX
                                                21
#define DTV ISDBT SB SEGMENT COUNT
                                                22
#define DTV_ISDBT_LAYERA_FEC
#define DTV_ISDBT_LAYERA_MODULATION
#define DTV_ISDBT_LAYERA_SEGMENT_COUNT
                                                          23
                                                          24
                                                          25
#define DTV ISDBT LAYERA TIME INTERLEAVING
                                                          26
#define DTV ISDBT LAYERB FEC
                                                          27
#define DTV_ISDBT_LAYERB_MODULATION
#define DTV_ISDBT_LAYERB_SEGMENT_COUNT
#define DTV_ISDBT_LAYERB_TIME_INTERLEAVING
                                                          28
                                                          29
                                                          30
#define DTV ISDBT LAYERC FEC
                                                          31
#define DTV_ISDBT_LAYERC_MODULATION
                                                          32
#define DTV ISDBT LAYERC SEGMENT COUNT
                                                          33
#define DTV ISDBT LAYERC TIME INTERLEAVING
                                                          34
#define DTV API VERSION
                                      35
#define DTV_CODE_RATE_HP
                                      36
#define DTV CODE RATE LP
                                      37
#define DTV GUARD INTERVAL
                                      38
#define DTV TRANSMISSION MODE
                                      39
#define DTV HIERARCHY
                                      40
#define DTV ISDBT LAYER ENABLED 41
#define DTV ISDBS TS ID
                                      42
#define DTV_MAX_COMMAND
                                                          DTV ISDBS TS ID
typedef enum fe pilot {
         PILOT_ON,
         PILOT_OFF,
         PILOT AUTO,
} fe pilot t;
typedef enum fe_rolloff {
         ROLLOFF_35, /* Implied value in DVB-S, default for DVB-S2 */
         ROLLOFF_20,
         ROLLOFF_25,
         ROLLOFF AUTO,
} fe rolloff t;
typedef enum fe delivery system {
         SYS UNDEFINED,
         SYS_DVBC_ANNEX_AC, SYS_DVBC_ANNEX_B,
         SYS_DVBT,
         SYS DSS,
         SYS DVBS,
         SYS DVBS2,
         SYS DVBH,
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        SYS_ISDBT,
        SYS_ISDBS,
        SYS ISDBC,
        SYS ATSC,
        SYS ATSCMH,
        SYS_DMBTH,
SYS_CMMB,
        SYS DAB,
} fe delivery system t;
struct dtv_cmds_h {
                                /* A display name for debugging purposes */
        char
                *name:
                                /* A unique ID */
        u32
                cmd;
        /* Flags */
        u32
                                /* Either a set or get property */
                set:1;
        __u32
                                /* Does this property use the buffer? */
                buffer:1;
                                /* Align */
        u32
                reserved:30;
};
struct dtv_property {
        __u32 cmd;
        __u32 reserved[3];
        union {
                  u32 data;
                struct {
                        _u8 data[32];
                        __u32 len;
                         u32 reserved1[3];
                        void *reserved2;
                } buffer;
        } u;
        int result;
} attribute ((packed));
/* num of properties cannot exceed DTV_IOCTL_MAX_MSGS per ioctl */
#define DTV IOCTL MAX MSGS 64
struct dtv_properties {
        u32 num;
        struct dtv_property *props;
#define <link linkend="FE GET PROPERTY">FE SET PROPERTY</link>
_IOW('o', 82, struct dtv_properties)
#define <link linkend="FE GET PROPERTY">FE GET PROPERTY</link>
_IOR('o', 83, struct dtv_properties)
/**
 * When set, this flag will disable any zigzagging or other "normal" tuning
 * behaviour. Additionally, there will be no automatic monitoring of the lock
 * status, and hence no frontend events will be generated. If a frontend device
 * is closed, this flag will be automatically turned off when the device is
 * reopened read-write.
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#define FE TUNE MODE ONESHOT 0x01
#define <link linkend="FE GET INFO">FE GET INFO</link>
                                                                     IOR ('o',
61, struct dvb frontend info)
#define <link linkend="FE_DISEQC_RESET_OVERLOAD">FE DISEQC RESET OVERLOAD</link>
  _IO('o', 62)
#define <link
linkend="FE DISEQC SEND MASTER CMD">FE DISEQC SEND MASTER CMD</link> IOW('o',
63, struct dvb_diseqc_master_cmd)
#define <link
linkend="FE DISEQC RECV SLAVE REPLY">FE DISEQC RECV SLAVE REPLY</link> IOR('o',
64, struct dvb diseqc slave reply)
#define <link linkend="FE DISEQC SEND BURST">FE DISEQC SEND BURST</link>
IO('o', 65) /* fe sec mini cmd t */
#define <link linkend="FE SET TONE">FE SET TONE</link>
                                                                     IO('o',
_IO('o', 67) /* fe_sec_voltage_t */
#define <link
linkend="FE ENABLE HIGH LNB VOLTAGE">FE ENABLE HIGH LNB VOLTAGE</link> IO('o',
    /* int */
#define <link linkend="FE READ STATUS">FE READ STATUS</link>
_IOR('o', 69, fe_status_t)
#define <link linkend="FE_READ_BER">FE_READ_BER</link>
                                                                     IOR('o',
   u32)
#define <link linkend="FE READ SIGNAL STRENGTH">FE READ SIGNAL STRENGTH</link>
 IOR ('o', 71, u16)
#define <link linkend="FE READ SNR">FE READ SNR</link>
                                                                     IOR('o',
72, u16)
#define link
linkend="FE_READ_UNCORRECTED_BLOCKS">FE_READ_UNCORRECTED_BLOCKS</link> _IOR('o',
73, u32)
#define <link linkend="FE SET FRONTEND">FE SET FRONTEND</link>
 IOW('o', 76, struct dvb_frontend_parameters)
#define <link linkend="FE GET FRONTEND">FE GET FRONTEND</link>
_IOR('o', 77, struct dvb_frontend_parameters)
#define <link
linkend="FE_SET_FRONTEND_TUNE_MODE">FE_SET_FRONTEND_TUNE_MODE</link> _IO('o',
81) /* unsigned int */
#define <link linkend="FE GET EVENT">FE GET EVENT</link>
                                                                      IOR('o',
78, struct dvb frontend event)
#define <link
linkend="FE_DISHNETWORK_SEND_LEGACY_CMD">FE_DISHNETWORK_SEND_LEGACY_CMD</link>
_IO('o', 80) /* unsigned int */
#endif /* DVBFRONTEND H */
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