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# File: Makefile
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CC=
                $ (CROSS_COMPILE) gcc
LD=
               $ (CROSS_COMPILE) 1d
               /root/backup
BACKUP=
                date +%Y%m%d%H%M
YMD=
# Configuration Options
# Debug Option
# DEBUG LEVEL n/1/2:
# 1: Only PRINTM(MMSG,...), PRINTM(MFATAL,...), ...
# 2: All PRINTM()
CONFIG_DEBUG=1
# Proc debug file
CONFIG_PROC_DEBUG=y
# ENABLE STA mode support
CONFIG_STA_SUPPORT=y
# Enable AP mode support CONFIG_UAP_SUPPORT=y
# Re-association in driver
CONFIG REASSOCIATION=v
# Manufacturing firmware support
CONFIG_MFG_CMD_SUPPORT=y
# Big-endian platform
CONFIG_BIG_ENDIAN=n
# Enable SDIO multi-port Tx aggregation
CONFIG_SDIO_MULTI_PORT_TX_AGGR=y
# Enable SDIO multi-port Rx aggregation
CONFIG_SDIO_MULTI_PORT_RX_AGGR=y
# SDIO suspend/resume
CONFIG_SDIO_SUSPEND_RESUME=y
# Use static link for app build
export CONFIG_STATIC_LINK=n
# Select Platform Tools
MODEXT = ko
EXTRA CFLAGS += -I$ (PWD) /mlan
EXTRA_CFLAGS += -DLINUX
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# KERNELDIR point to the installed kernel directory.
# KERNELDIR can be set on the command line,
# make KERNELDIR=/usr/src/arm/<arch-bsp-path>
 Alternatively KERNELDIR can be set in the environment.
# Default value for KERNELDIR is set below.
KERNELDIR ?= /usr/src/arm/linux-2.6.29-pxa920
\# CROSS_COMPILE specify the prefix used for all executables used
 during compilation. Only gcc and related bin-utils executables
# CROSS COMPILE can be set on the command line
# make CROSS_COMPILE=</usr/local/arm/4.1.1/bin/>arm-linux-
# Alternatively CROSS_COMPILE can be set in the environment.
# Default value for CROSS_COMPILE is set below.
CROSS COMPILE 2= /usr/local/arm-warvell-linux-grueabi/bin/arm-warvell-linux-grueabi-
\# INSTALLDIR specify the path to install the kernel module after
  successful compilation.
# INSTALLDIR can be set on the command line
 make INSTALLDIR=/tftpboot/<rootfs>
# Alternatively INSTALLDIR can be set in the environment.
# Default value for INSTALL is set below. INSTALLDIR ?= /tftpboot/pxa9xx/root
# ARCH specifies the architecture of the target processor, this kernel
# module will run.
# ARCH can be set on the command line
# make ARCH=<arm/i386>
# Alternatively ARCH can be set in the environment
# Default values of ARCH for specific platform are set below.
ARCH ?= arm
LD += -S
BINDIR = .../bin_sd8787
# Compiler Flags
EXTRA CFLAGS += -I$ (KERNELDIR) / include
       EXTRA_CFLAGS += -DFPNUM=' "57"'
ifeq ($(CONFIG DEBUG), 1)
        EXTRA_CFLAGS += -DDEBUG_LEVEL1
endif
ifeq ($(CONFIG_DEBUG), 2)
        EXTRA_CFLAGS += -DDEBUG_LEVEL1
        EXTRA_CFLAGS += -DDEBUG_LEVEL2
        DBG=
               -dbg
endif
export CONFIG_PROC_DEBUG
endif
EXTRA_CFLAGS += -DREASSOCIATION
endif
endif
ifeq ($(CONFIG_UAP_SUPPORT), y)
        EXTRA_CFLAGS += -DUAP_SUPPORT
endif
ifeq ($(CONFIG_MFG_CMD_SUPPORT),y)
        EXTRA_CFLAGS += -DMFG_CMD_SUPPORT
endif
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ifeq ($(CONFIG_BIG_ENDIAN), y)
        EXTRA_CFLAGS += -DBIG_ENDIAN_SUPPORT
endif
endif
\label{eq:config_sdio_multi_port_rx_aggr} \texttt{($(CONFIG\_SDIO\_MULTI\_PORT\_RX\_AGGR),y)}
        EXTRA_CFLAGS += -DSDIO_MULTI_PORT_RX_AGGR
endif
ifeq ($(CONFIG_SDIO_SUSPEND_RESUME),y)
        EXTRA_CFLAGS += -DSDIO_SUSPEND_RESUME
endif
______
# Make Targets
ifneg ($(KERNELRELEASE),)
MOALOBJS =
                mlinux/moal_main.o \
                mlinux/moal_ioctl.o \
                mlinux/moal_shim.o
MLANOBJS =
                mlan/mlan_shim.o mlan/mlan_init.o \
                mlan/mlan_txrx.o \
                mlan/mlan_cmdevt.o mlan/mlan_misc.o \
                mlan/mlan_module.o
MLANOBJS += mlan/mlan_wmm.o
MLANOBJS += mlan/mlan_sdio.o
MLANOBJS += mlan/mlan_11n_aggr.o
MLANOBJS += mlan/mlan_11n_rxreorder.o

MLANOBJS += mlan/mlan_11n.o

ifeq ($(CONFIG_STA_SUPPORT),y)

MLANOBJS += mlan/mlan_11d.o
MLANOBJS += mlan/mlan_11h.o
MLANOBJS += mlan/mlan_meas.o
MLANOBJS += mlan/mlan_cfp.o
                        mlan/mlan_scan.o \
                        mlan/mlan_sta_ioctl.o \
                        mlan/mlan_sta_rx.o \
                        mlan/mlan_sta_tx.o \
mlan/mlan_sta_event.o \
                        mlan/mlan_sta_cmd.o \
                        mlan/mlan_sta_cmdresp.o \
                        mlan/mlan_join.o
MOALOBJS += mlinux/moal priv.o
            mlinux/moal_wext.o
endif
ifeq ($(CONFIG_UAP_SUPPORT),y)
MLANOBJS += mlan/mlan_uap_ioctl.o
MLANOBJS += mlan/mlan_uap_cmdevent.o
MLANOBJS += mlan/mlan_uap_txrx.o
MOALOBJS += mlinux/moal_uap.o
MOALOBJS += mlinux/moal_uap_priv.o
MOALOBJS += mlinux/moal_uap_wext.o
ifdef CONFIG_PROC_FS
MOALOBJS += mlinux/moal_proc.o
ifeq ($(CONFIG_PROC_DEBUG), y)
MOALOBJS += mlinux/moal_debug.o
endif
endif
obj-m := mlan.o
mlan-objs := $(MLANOBJS)
MOALOBJS += mlinux/moal_sdio_mmc.o
obj-m += sd8xxx.o
sd8xxx-objs := $(MOALOBJS)
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# Otherwise we were called directly from the command line; invoke the kernel build system.
else
default:
         $(MAKE) -C $(KERNELDIR) M=$(PWD) ARCH=$(ARCH) CROSS_COMPILE=$(CROSS_COMPILE) modules
endif
CC LD EXTRA_CFLAGS KERNELDIR
export
ifeq ($(CONFIG_STA_SUPPORT),y)
ifeq ($(CONFIG_UAP_SUPPORT),y)
.PHONY: mapp/mlanconfig mapp/mlan2040coex mapp/mlanevent mapp/uaputl clean distclean
. PHONY: mapp/mlanconfig mapp/mlanevent mapp/mlan2040coex clean distclean
endif
else
ifeq ($(CONFIG_UAP_SUPPORT),y)
.PHONY: mapp/mlanevent mapp/uaputl clean distclean
endif
endif
         @echo "Finished Making Marvell Wlan Linux Driver"
ifeq ($(CONFIG_STA_SUPPORT), y)
mapp/mlanconfig:
$(MAKE) -C $@
endif
ifeq ($(CONFIG_UAP_SUPPORT), y)
mapp/uaputl:
         $ (MAKE) -C $@
endif
mapp/mlanevent:
         $ (MAKE) -C $@
echo:
build:
                  echo default
         @if [ ! -d \$(BINDIR) ]; then \
                  mkdir $(BINDIR);
         cp -f mlan. $(MODEXT) $(BINDIR)/mlan$(DBG). $(MODEXT)
         cp -f sd8xxx. $(MODEXT) $(BINDIR)/sd8787$(DBG). $(MODEXT)
ifeq ($(CONFIG_STA_SUPPORT), y)
         cp -f README $(BINDIR)
         $(MAKE) -C mapp/mlanconfig $@ INSTALLDIR=$(BINDIR)
$(MAKE) -C mapp/mlan2040coex $@ INSTALLDIR=$(BINDIR)
endif
ifeq ($(CONFIG_UAP_SUPPORT), y)
         cp -f README_UAP $ (BINDIR)
         $ (MAKE) -C mapp/uaputl $@ INSTALLDIR=$ (BINDIR)
endif
         $(MAKE) -C mapp/mlanevent $@ INSTALLDIR=$(BINDIR)
clean:
         -find . -name "*.o" -exec rm {} \;
-find . -name "*.ko" -exec rm {} \;
-find . -name "*.ko" -exec rm {} \;
-find . -name "*.mod.c" -exec rm {} \;
-find . -name "Module.symvers" -exec rm {} \;
-find . -name "Module.markers" -exec rm {} \;
-find . -name "modules.order" -exec rm {} \;
          -rm -rf .tmp_versions
$(MAKE) -C mapp/mlan2040coex $@
endif
```

End of file