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Makefile..txt
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# File : mlanconfig/Makefile
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# Path to the top directory of the mlandriver distribution
PATH TO TOP = \dots/\dots
# Determine how we should copy things to the install directory
ABSPATH := $(filter /%, $(INSTALLDIR))
RELPATH := $(filter-out /%, $(INSTALLDIR))
INSTALLPATH := \$(ABSPATH)
ifeq ($(strip $(INSTALLPATH)),)
INSTALLPATH := $(PATH TO TOP) / $(RELPATH)
endif
# Override CFLAGS for application sources, remove __ kernel namespace defines
CFLAGS := $(filter-out -D %, $(EXTRA CFLAGS))
# List of application executables to create
libobjs:= mlanconfig. o mlanhostcmd. o mlanmisc. o
exectarget=mlanconfig
TARGETS := $(exectarget)
# Make target rules
# All rule compiles list of TARGETS using builtin program target from src rule
all:
$(exectarget): $(libobjs)
else
        $(CC) $(CFLAGS) $(libobjs) -o $(exectarget)
endif
# Update any needed TARGETS and then copy to the install path
build install: $(TARGETS)
        @cp -f $(exectarget) $(INSTALLPATH)
        @cp -rf config $(INSTALLPATH)
clean:
        @rm -f $(exectarget)
        @rm -f *.o
distclean: clean
@rm -f *~ core
        @rm -f tags
```