



1. Description

1.1. Project

Project Name	printalyzer-cube
Board Name	custom
Generated with:	STM32CubeMX 6.1.0
Date	12/07/2020

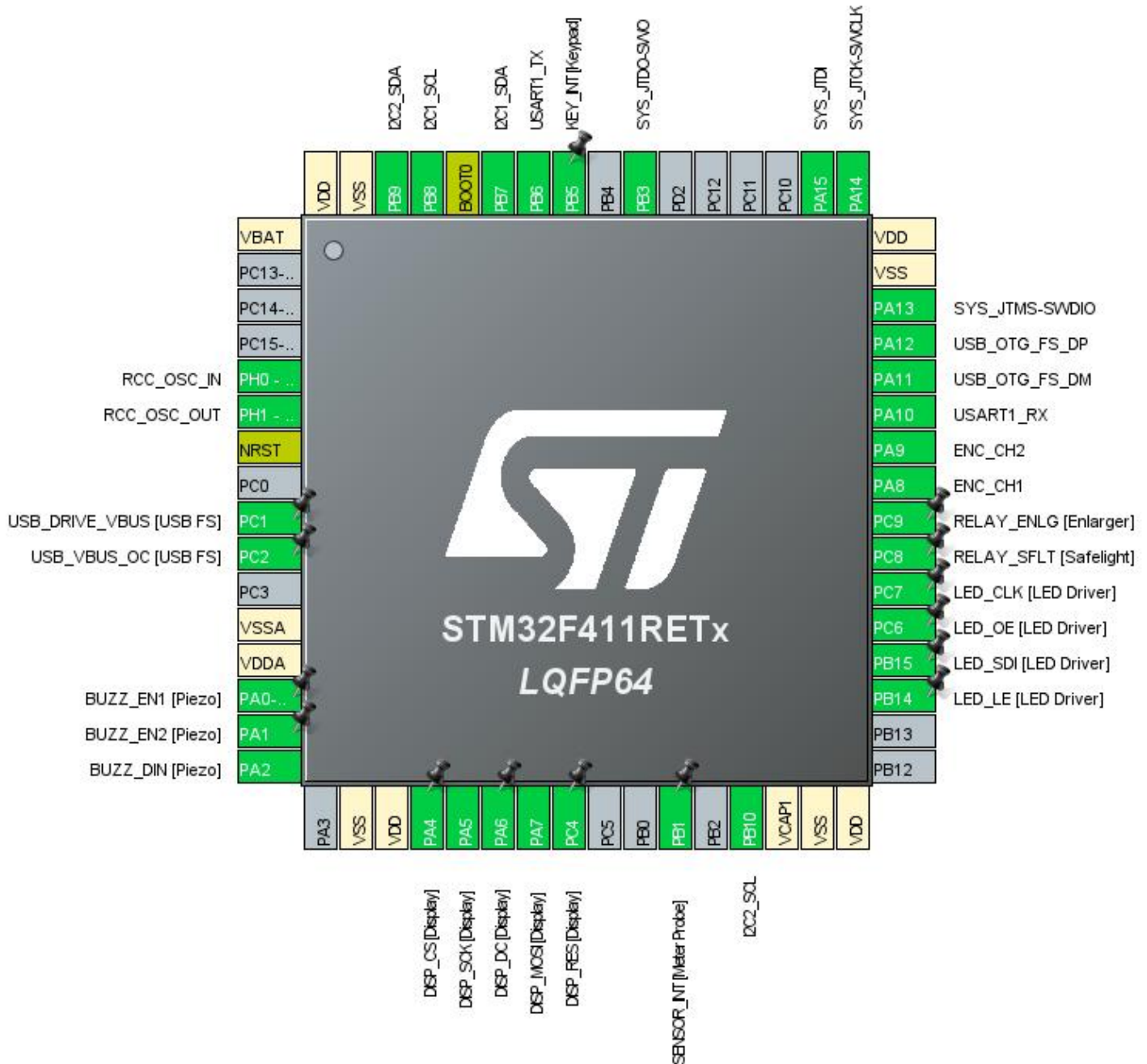
1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F411
MCU name	STM32F411RETx
MCU Package	LQFP64
MCU Pin number	64

1.3. Core(s) information

Core(s)	Arm Cortex-M4
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2. Pinout Configuration



3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
5	PH0 - OSC_IN	I/O	RCC_OSC_IN	
6	PH1 - OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
9	PC1 *	I/O	GPIO_Output	USB_DRIVE_VBUS [USB FS]
10	PC2	I/O	GPIO_EXTI2	USB_VBUS_OC [USB FS]
12	VSSA	Power		
13	VDDA	Power		
14	PA0-WKUP *	I/O	GPIO_Output	BUZZ_EN1 [Piezo]
15	PA1 *	I/O	GPIO_Output	BUZZ_EN2 [Piezo]
16	PA2	I/O	TIM9_CH1	BUZZ_DIN [Piezo]
18	VSS	Power		
19	VDD	Power		
20	PA4 *	I/O	GPIO_Output	DISP_CS [Display]
21	PA5	I/O	SPI1_SCK	DISP_SCK [Display]
22	PA6 *	I/O	GPIO_Output	DISP_DC [Display]
23	PA7	I/O	SPI1_MOSI	DISP_MOSI [Display]
24	PC4 *	I/O	GPIO_Output	DISP_RES [Display]
27	PB1	I/O	GPIO_EXTI1	SENSOR_INT [Meter Probe]
29	PB10	I/O	I2C2_SCL	
30	VCAP1	Power		
31	VSS	Power		
32	VDD	Power		
35	PB14 *	I/O	GPIO_Output	LED_LE [LED Driver]
36	PB15	I/O	SPI2_MOSI	LED_SDI [LED Driver]
37	PC6	I/O	TIM3_CH1	LED_OE [LED Driver]
38	PC7	I/O	SPI2_SCK	LED_CLK [LED Driver]
39	PC8 *	I/O	GPIO_Output	RELAY_SFLT [Safelight]
40	PC9 *	I/O	GPIO_Output	RELAY_ENLG [Enlarger]
41	PA8	I/O	TIM1_CH1	ENC_CH1
42	PA9	I/O	TIM1_CH2	ENC_CH2
43	PA10	I/O	USART1_RX	
44	PA11	I/O	USB_OTG_FS_DM	
45	PA12	I/O	USB_OTG_FS_DP	
46	PA13	I/O	SYS_JTMS-SWDIO	

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
47	VSS	Power		
48	VDD	Power		
49	PA14	I/O	SYS_JTCK-SWCLK	
50	PA15	I/O	SYS_JTDI	
55	PB3	I/O	SYS_JTDO-SWO	
57	PB5	I/O	GPIO_EXTI5	KEY_INT [Keypad]
58	PB6	I/O	USART1_TX	
59	PB7	I/O	I2C1_SDA	
60	BOOT0	Boot		
61	PB8	I/O	I2C1_SCL	
62	PB9	I/O	I2C2_SDA	
63	VSS	Power		
64	VDD	Power		

* The pin is affected with an I/O function



5. Software Project

5.1. Project Settings

Name	Value
Project Name	printalyzer-cube
Project Folder	/home/octo/devel/printalyzer-cube
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F4 V1.25.2
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	Yes
Enable Full Assert	No

5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	MX_GPIO_Init	GPIO
2	SystemClock_Config	RCC
3	MX_USART1_UART_Init	USART1
4	MX_FATFS_Init	FATFS
5	MX_USB_HOST_Init	USB_HOST
6	MX_I2C1_Init	I2C1
7	MX_I2C2_Init	I2C2
8	MX_SPI1_Init	SPI1
9	MX_TIM1_Init	TIM1
10	MX_TIM9_Init	TIM9
11	MX_SPI2_Init	SPI2

Rank	Function Name	Peripheral Instance Name
12	MX_TIM3_Init	TIM3

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32F4
Line	STM32F411
MCU	STM32F411RETx
Datasheet	DS10314_Rev6

6.2. Parameter Selection

Temperature	25
Vdd	1.7

6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

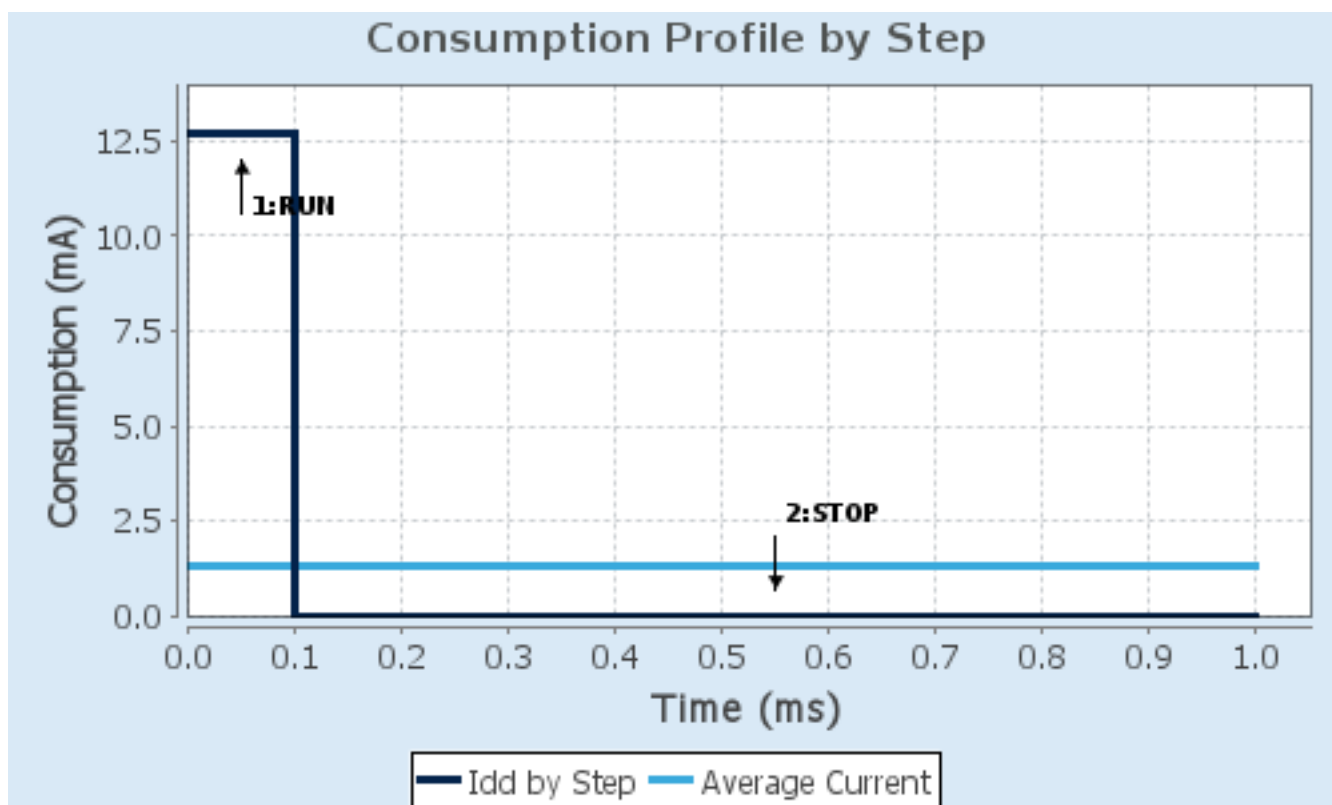
6.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	1.7	1.7
Voltage Source	Battery	Battery
Range	Scale1-High	No Scale
Fetch Type	SRAM	n/a
CPU Frequency	100 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator_LPLV Flash-PwrDwn
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	12.7 mA	9 μ A
Duration	0.1 ms	0.9 ms
DMIPS	125.0	0.0
Ta Max	103.99	105
Category	In DS Table	In DS Table

6.5. Results

Sequence Time	1 ms	Average Current	1.28 mA
Battery Life	3 months, 19 days, 6 hours	Average DMIPS	125.0 DMIPS

6.6. Chart



7. Peripherals and Middlewares Configuration

7.1. I2C1

I2C: I2C

7.1.1. Parameter Settings:

Master Features:

I2C Speed Mode	Fast Mode *
I2C Clock Speed (Hz)	400000
Fast Mode Duty Cycle	Duty cycle Tlow/Thigh = 2

Slave Features:

Clock No Stretch Mode	Disabled
Primary Address Length selection	7-bit
Dual Address Acknowledged	Disabled
Primary slave address	0
General Call address detection	Disabled

7.2. I2C2

I2C: I2C

7.2.1. Parameter Settings:

Master Features:

I2C Speed Mode	Standard Mode
I2C Clock Speed (Hz)	100000

Slave Features:

Clock No Stretch Mode	Disabled
Primary Address Length selection	7-bit
Dual Address Acknowledged	Disabled
Primary slave address	0
General Call address detection	Disabled

7.3. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

7.3.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
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Instruction Cache	Enabled
Prefetch Buffer	Enabled
Data Cache	Enabled
Flash Latency(WS)	3 WS (4 CPU cycle)

RCC Parameters:

HSI Calibration Value	16
TIM Prescaler Selection	Disabled
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

Power Parameters:

Power Regulator Voltage Scale	Power Regulator Voltage Scale 1
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7.4. SPI1

Mode: Transmit Only Master

7.4.1. Parameter Settings:

Basic Parameters:

Frame Format	Motorola
Data Size	8 Bits
First Bit	MSB First

Clock Parameters:

Prescaler (for Baud Rate)	4 *
Baud Rate	24.0 MBits/s *
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

Advanced Parameters:

CRC Calculation	Disabled
NSS Signal Type	Software

7.5. SPI2

Mode: Transmit Only Master

7.5.1. Parameter Settings:

Basic Parameters:

Frame Format	Motorola
Data Size	8 Bits
First Bit	MSB First

Clock Parameters:

Prescaler (for Baud Rate)	16 *
Baud Rate	3.0 MBits/s *
Clock Polarity (CPOL)	High *
Clock Phase (CPHA)	1 Edge

Advanced Parameters:

CRC Calculation	Disabled
NSS Signal Type	Software

7.6. SYS

Debug: JTAG (4 pins)

Timebase Source: TIM11

7.7. TIM1

Combined Channels: Encoder Mode

7.7.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	0
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	65535
Internal Clock Division (CKD)	No Division
Repetition Counter (RCR - 8 bits value)	0
auto-reload preload	Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

Encoder:

Encoder Mode	Encoder Mode TI1 and TI2 *
____ Parameters for Channel 1 ____	
Polarity	Rising Edge
IC Selection	Direct
Prescaler Division Ratio	No division
Input Filter	0x0F *
____ Parameters for Channel 2 ____	
Polarity	Rising Edge
IC Selection	Direct
Prescaler Division Ratio	No division

Input Filter

0x0F *

7.8. TIM3

Channel1: PWM Generation CH1

7.8.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	0
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	65535
Internal Clock Division (CKD)	No Division
auto-reload preload	Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

PWM Generation Channel 1:

Mode	PWM mode 1
Pulse (16 bits value)	32768 *
Output compare preload	Enable
Fast Mode	Disable
CH Polarity	High

7.9. TIM9

Channel1: PWM Generation CH1

7.9.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	0
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	65535
Internal Clock Division (CKD)	No Division
auto-reload preload	Disable

PWM Generation Channel 1:

Mode	PWM mode 1
Pulse (16 bits value)	32768 *
Output compare preload	Enable
Fast Mode	Disable

CH Polarity High

7.10. USART1

Mode: Asynchronous

7.10.1. Parameter Settings:

Basic Parameters:

Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples

7.11. USB_OTG_FS

Mode: Host_Only

7.11.1. Parameter Settings:

Speed	Host Full Speed 12MBit/s
Signal start of frame	Disabled

7.12. FATFS

mode: USB Disk

7.12.1. Set Defines:

Version:

FATFS version	R0.12c
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Function Parameters:

FS_READONLY (Read-only mode)	Disabled
FS_MINIMIZE (Minimization level)	Disabled
USE_STRFUNC (String functions)	Enabled with LF -> CRLF conversion
USE_FIND (Find functions)	Disabled
USE_MKFS (Make filesystem function)	Enabled
USE_FASTSEEK (Fast seek function)	Enabled
USE_EXPAND (Use f_expand function)	Disabled

USE_CHMOD (Change attributes function)	Disabled
USE_LABEL (Volume label functions)	Disabled
USE_FORWARD (Forward function)	Disabled

Locale and Namespace Parameters:

CODE_PAGE (Code page on target)	Latin 1
USE_LFN (Use Long Filename)	Disabled
MAX_LFN (Max Long Filename)	255
LFN_UNICODE (Enable Unicode)	ANSI/OEM
STRF_ENCODE (Character encoding)	UTF-8
FS_RPATH (Relative Path)	Disabled

Physical Drive Parameters:

VOLUMES (Logical drives)	1
MAX_SS (Maximum Sector Size)	512
MIN_SS (Minimum Sector Size)	512
MULTI_PARTITION (Volume partitions feature)	Disabled
USE_TRIM (Erase feature)	Disabled
FS_NOFSINFO (Force full FAT scan)	0

System Parameters:

FS_TINY (Tiny mode)	Disabled
FS_EXFAT (Support of exFAT file system)	Disabled
FS_NORTC (Timestamp feature)	Dynamic timestamp
FS_REENTRANT (Re-Entrancy)	Enabled
FS_TIMEOUT (Timeout ticks)	1000
USE_MUTEX	Disabled
SYNC_t (O/S sync object)	osSemaphoreId_t
FS_LOCK (Number of files opened simultaneously)	2

7.12.2. Advanced Settings:

USBH:

USBH instance	USB Host MSC FS
Use dma template	Disabled

7.13. FREERTOS

Interface: CMSIS_V2

7.13.1. Config parameters:

API:

FreeRTOS API	CMSIS v2
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Versions:

FreeRTOS version	10.2.1
CMSIS-RTOS version	2.00

MPU/FPU:

ENABLE_MPU	Disabled
ENABLE_FPU	Enabled *

Kernel settings:

USE_PREEMPTION	Enabled
CPU_CLOCK_HZ	SystemCoreClock
TICK_RATE_HZ	1000
MAX_PRIORITIES	56
MINIMAL_STACK_SIZE	128
MAX_TASK_NAME_LEN	16
USE_16_BIT_TICKS	Disabled
IDLE_SHOULD_YIELD	Enabled
USE_MUTEXES	Enabled
USE_RECURSIVE_MUTEXES	Enabled
USE_COUNTING_SEMAPHORES	Enabled
QUEUE_REGISTRY_SIZE	8
USE_APPLICATION_TASK_TAG	Disabled
ENABLE_BACKWARD_COMPATIBILITY	Enabled
USE_PORT_OPTIMISED_TASK_SELECTION	Disabled
USE_TICKLESS_IDLE	Disabled
USE_TASK_NOTIFICATIONS	Enabled
RECORD_STACK_HIGH_ADDRESS	Disabled

Memory management settings:

Memory Allocation	Dynamic / Static
TOTAL_HEAP_SIZE	32768 *
Memory Management scheme	heap_4

Hook function related definitions:

USE_IDLE_HOOK	Disabled
USE_TICK_HOOK	Disabled
USE_MALLOC_FAILED_HOOK	Disabled
USE_DAEMON_TASK_STARTUP_HOOK	Disabled
CHECK_FOR_STACK_OVERFLOW	Disabled

Run time and task stats gathering related definitions:

GENERATE_RUN_TIME_STATS	Disabled
USE_TRACE_FACILITY	Enabled
USE_STATS_FORMATTING_FUNCTIONS	Disabled

Co-routine related definitions:

USE_CO_ROUTINES	Disabled
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MAX_CO_ROUTINE_PRIORITIES 2

Software timer definitions:

USE_TIMERS Enabled
TIMER_TASK_PRIORITY 2
TIMER_QUEUE_LENGTH 10
TIMER_TASK_STACK_DEPTH 256

Interrupt nesting behaviour configuration:

LIBRARY_LOWEST_INTERRUPT_PRIORITY 15
LIBRARY_MAX_SYSCALL_INTERRUPT_PRIORITY 5

Added with 10.2.1 support:

MESSAGE_BUFFER_LENGTH_TYPE size_t
USE_POSIX_ERRNO Disabled

7.13.2. Include parameters:

Include definitions:

vTaskPrioritySet Enabled
uxTaskPriorityGet Enabled
vTaskDelete Enabled
vTaskCleanUpResources Disabled
vTaskSuspend Enabled
vTaskDelayUntil Enabled
vTaskDelay Enabled
xTaskGetSchedulerState Enabled
xTaskResumeFromISR Enabled
xQueueGetMutexHolder Enabled
xSemaphoreGetMutexHolder Disabled
pcTaskGetTaskName Disabled
uxTaskGetStackHighWaterMark Enabled
xTaskGetCurrentTaskHandle Disabled
eTaskGetState Enabled
xEventGroupSetBitFromISR Disabled
xTimerPendFunctionCall Enabled
xTaskAbortDelay Disabled
xTaskGetHandle Disabled
uxTaskGetStackHighWaterMark2 Disabled

7.13.3. Advanced settings:

Newlib settings (see parameter description first):

USE_NEWLIB_REENTRANT Disabled

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
I2C1	PB7	I2C1_SDA	Alternate Function Open Drain	Pull-up	Very High *	
	PB8	I2C1_SCL	Alternate Function Open Drain	Pull-up	Very High *	
I2C2	PB10	I2C2_SCL	Alternate Function Open Drain	Pull-up	Very High *	
	PB9	I2C2_SDA	Alternate Function Open Drain	Pull-up	Very High *	
RCC	PH0 - OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PH1 - OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	DISP_SCK [Display]
	PA7	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	DISP_MOSI [Display]
SPI2	PB15	SPI2_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	LED_SDI [LED Driver]
	PC7	SPI2_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	LED_CLK [LED Driver]
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
	PA15	SYS_JTDI	n/a	n/a	n/a	
	PB3	SYS_JTDO-SWO	n/a	n/a	n/a	
TIM1	PA8	TIM1_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	ENC_CH1
	PA9	TIM1_CH2	Alternate Function Push Pull	No pull-up and no pull-down	Low	ENC_CH2
TIM3	PC6	TIM3_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	LED_OE [LED Driver]
TIM9	PA2	TIM9_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	BUZZ_DIN [Piezo]
USART1	PA10	USART1_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PB6	USART1_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
USB_OTG_FS	PA11	USB_OTG_FS_DM	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PA12	USB_OTG_FS_DP	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
GPIO	PC1	GPIO_Output	Output Open Drain *	No pull-up and no pull-down	Low	USB_DRIVE_VBUS [USB FS]
	PC2	GPIO_EXTI2	External Interrupt Mode with Falling edge trigger detection	No pull-up and no pull-down	n/a	USB_VBUS_OC [USB FS]
	PA0-WKUP	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	BUZZ_EN1 [Piezo]
	PA1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	BUZZ_EN2 [Piezo]
	PA4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DISP_CS [Display]
	PA6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DISP_DC [Display]
	PC4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DISP_RES [Display]
	PB1	GPIO_EXTI1	External Interrupt Mode with Falling edge trigger detection	No pull-up and no pull-down	n/a	SENSOR_INT [Meter Probe]
	PB14	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED_LE [LED Driver]
	PC8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	RELAY_SFLT [Safelight]
	PC9	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	RELAY_ENLG [Enlarger]
	PB5	GPIO_EXTI5	External Interrupt Mode with Falling edge trigger detection	No pull-up and no pull-down	n/a	KEY_INT [Keypad]

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	15	0
System tick timer	true	15	0
EXTI line1 interrupt	true	5	0
EXTI line2 interrupt	true	5	0
EXTI line[9:5] interrupts	true	5	0
TIM1 trigger and commutation interrupts and TIM11 global interrupt	true	0	0
TIM1 capture compare interrupt	true	5	0
USB On The Go FS global interrupt	true	6	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
TIM1 break interrupt and TIM9 global interrupt	unused		
TIM1 update interrupt and TIM10 global interrupt	unused		
TIM3 global interrupt	unused		
I2C1 event interrupt	unused		
I2C1 error interrupt	unused		
I2C2 event interrupt	unused		
I2C2 error interrupt	unused		
SPI1 global interrupt	unused		
SPI2 global interrupt	unused		
USART1 global interrupt	unused		
FPU global interrupt	unused		

8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	false	false
Debug monitor	false	true	false
Pendable request for system service	false	false	false
System tick timer	false	false	true
EXTI line1 interrupt	false	true	true
EXTI line2 interrupt	false	true	true
EXTI line[9:5] interrupts	false	true	true
TIM1 trigger and commutation interrupts and TIM11 global interrupt	false	true	true
TIM1 capture compare interrupt	false	true	true
USB On The Go FS global interrupt	false	true	true

* User modified value

9. System Views

9.1. Category view

9.1.1. Current

Middleware					
<div>FATFS ✓FREERTOS ✓USB_HOST ✓</div>					
System Core	Analog	Timers	Connectivity	Multimedia	Computing
DMA		TIM1 ✓	I2C1 ✓		
GPIO ✓		TIM3 ✓	I2C2 ✓		
NVIC ✓		TIM9 ✓	SPI1 ✓		
RCC ✓			SPI2 ✓		
SYS ✓			USART1 ✓		
			USB_FS ✓		

10. Docs & Resources

Type	Link
Datasheet	http://www.st.com/resource/en/datasheet/DM00115249.pdf
Reference manual	http://www.st.com/resource/en/reference_manual/DM00119316.pdf
Programming manual	http://www.st.com/resource/en/programming_manual/DM00046982.pdf
Errata sheet	http://www.st.com/resource/en/errata_sheet/DM00137034.pdf
Application note	http://www.st.com/resource/en/application_note/CD00167594.pdf
Application note	http://www.st.com/resource/en/application_note/CD00211314.pdf
Application note	http://www.st.com/resource/en/application_note/CD00249778.pdf
Application note	http://www.st.com/resource/en/application_note/CD00259245.pdf
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Application note	http://www.st.com/resource/en/application_note/DM00046011.pdf
Application note	http://www.st.com/resource/en/application_note/DM00072315.pdf
Application note	http://www.st.com/resource/en/application_note/DM00073742.pdf
Application note	http://www.st.com/resource/en/application_note/DM00073853.pdf
Application note	http://www.st.com/resource/en/application_note/DM00080497.pdf
Application note	http://www.st.com/resource/en/application_note/DM00081379.pdf
Application note	http://www.st.com/resource/en/application_note/DM00115714.pdf
Application note	http://www.st.com/resource/en/application_note/DM00129215.pdf
Application note	http://www.st.com/resource/en/application_note/DM00156364.pdf
Application note	http://www.st.com/resource/en/application_note/DM00160482.pdf

Application note http://www.st.com/resource/en/application_note/DM00144612.pdf
Application note http://www.st.com/resource/en/application_note/DM00213525.pdf
Application note http://www.st.com/resource/en/application_note/DM00220769.pdf
Application note http://www.st.com/resource/en/application_note/DM00257177.pdf
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Application note http://www.st.com/resource/en/application_note/DM00226326.pdf
Application note http://www.st.com/resource/en/application_note/DM00236305.pdf
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Application note http://www.st.com/resource/en/application_note/DM00296349.pdf
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Application note http://www.st.com/resource/en/application_note/DM00327191.pdf
Application note http://www.st.com/resource/en/application_note/DM00354244.pdf
Application note http://www.st.com/resource/en/application_note/DM00315319.pdf
Application note http://www.st.com/resource/en/application_note/DM00380469.pdf
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