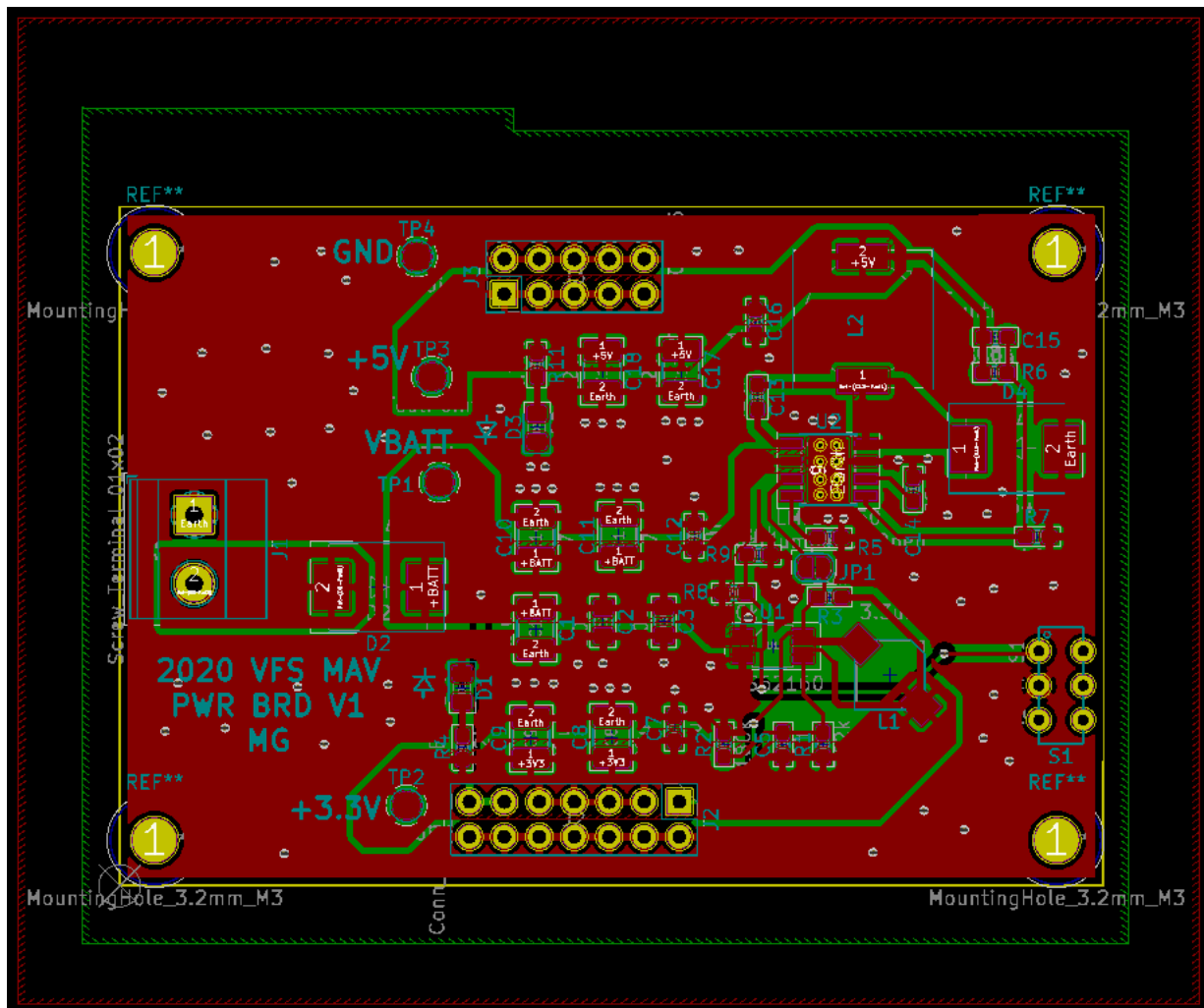
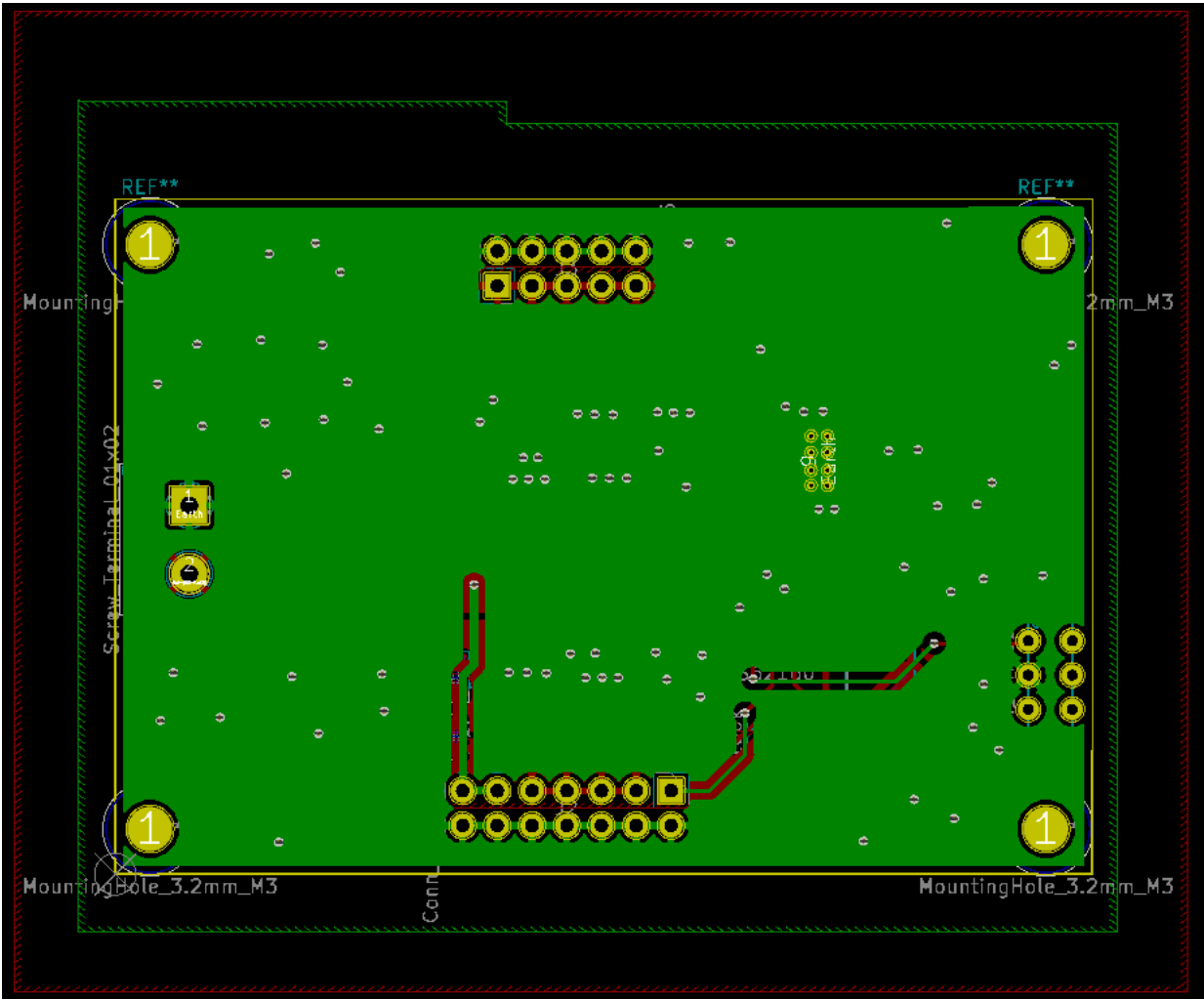


Front Layer with Silkscreen



Back layer



2020 VFS MAV
PWR BRD V1
MG

PCB layout details:

- Connectors:** J1 (Screw Terminal 01x02), J2 (CONN_02X05).
- Test Points:** TP1, TP2, TP3, TP4.
- Resistors:** R1 (12k), R2 (100k), R3 (3.3k), R4 (12k), R5 (3.3k), R6 (300k), R7 (10k), R8 (100k), R9 (100k), R10 (100k).
- Capacitors:** C1 (22uF), C2 (10uF), C3 (10uF), C4 (10uF), C5 (100k), C6 (100k), C7 (100k), C8 (22uF), C9 (100k), C10 (100k), C11 (100k), C12 (100k), C13 (100k), C14 (100k), C15 (300k), C16 (100k).
- Integrated Circuits:** U1 (TPS62160), U2 (LMR34050), U3 (CDBC540-G), U4 (CDBC540-G), U5 (SPDT_MHSS1104).
- Other Components:** D1 (LED), D2 (LED), D3 (LED), D4 (LED), L1 (Inductor), L2 (Inductor).