

Operating System Security and Memory Protection

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Operating Systems

Important security goals:

- *separation and access control to resources*
- first line of defence against unwanted behaviour
- fundamental controller of all system resources
- compromising it means loss of control to resources

Important functions:

- access control to resources
- identification, authentication and credential management
- information flow and synchronisation
- audit and integrity protection

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General boot sequence:

1. primitive functions and device drivers
2. process controllers
3. file and memory management
4. user interface

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Protection from malware:

- must be running before an attack takes place
- but is often an add-on so it is subject to delayed initialisation

If a malware manages to exploit an OS vulnerability

- run undetected and as privileged user (e.g rootkit)

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Different hardware:

- personal computers and mainframes
- dedicated devices
- automobiles and avionics
- smartphones, tablets, web appliances
- network appliances

Adequate fit to the:

- complexity of the device
- degree of control it must exercise
- amount of interaction to support (humans, devices)

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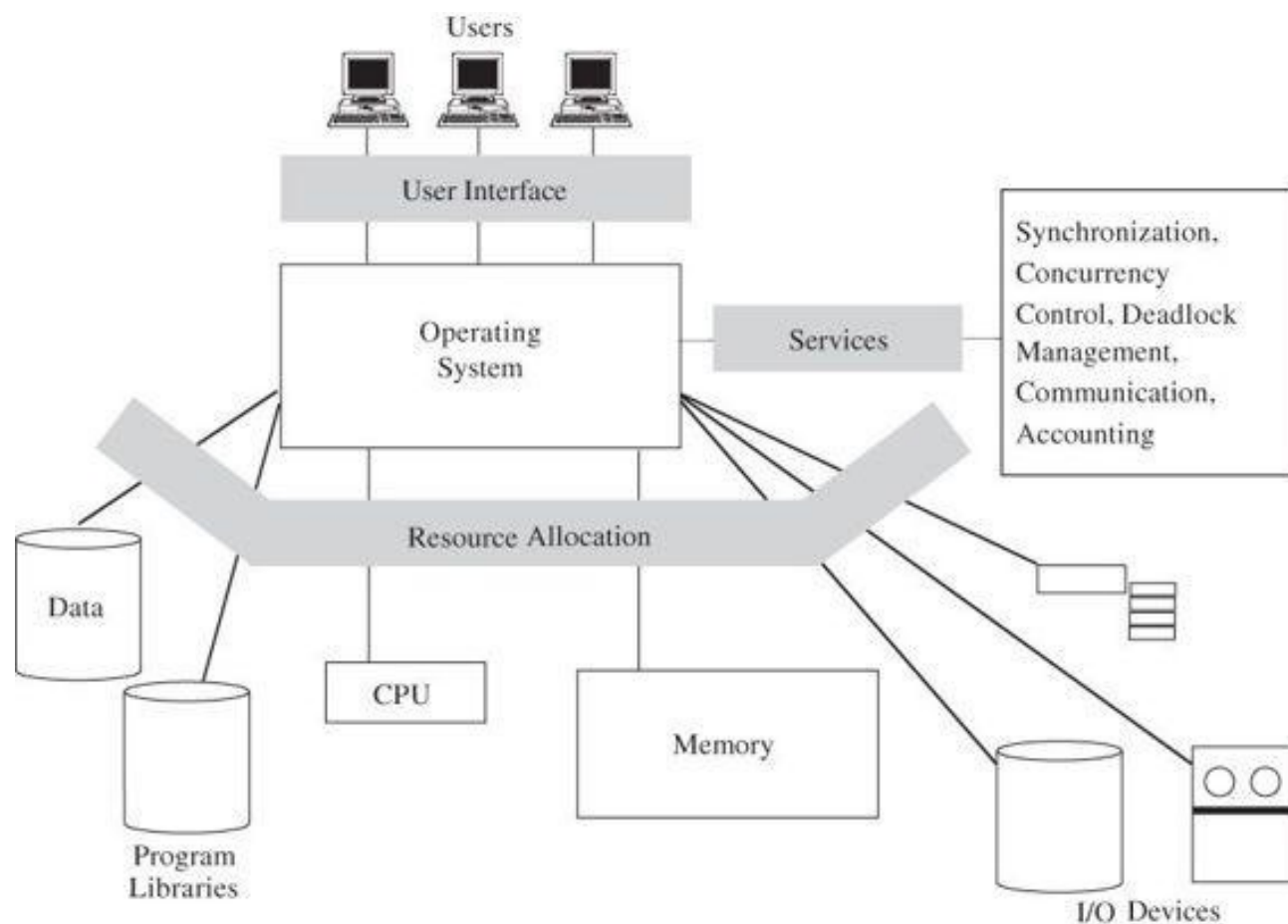
Characteristics:

- single- vs. multi-user
- single- vs. multi-program (cooperative scheduling or on I/O)
- single- vs. multi-task (preemptive scheduling)
- single- vs. multi-threaded

From the security standpoint:

- interested in the OS's control of resources
- which users are allowed which access to which objects

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Primitive OS functions:

- aka kernel functions
- base for enforcing security and other higher-level OS functions

Some primitive OS functions:

- enforced sharing
- interprocess communication and synchronisation
- protection of critical OS data
- guaranteed fair service
- interface to hardware
- user authentication
- memory protection
- file and I/O access control
- allocation and access control to general objects

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Resource Protection

Protected Objects:

- memory
- sharable I/O devices (e.g disk)
- serially reusable I/O devices (e.g printer)
- sharable programs and libraries
- networks
- sharable data

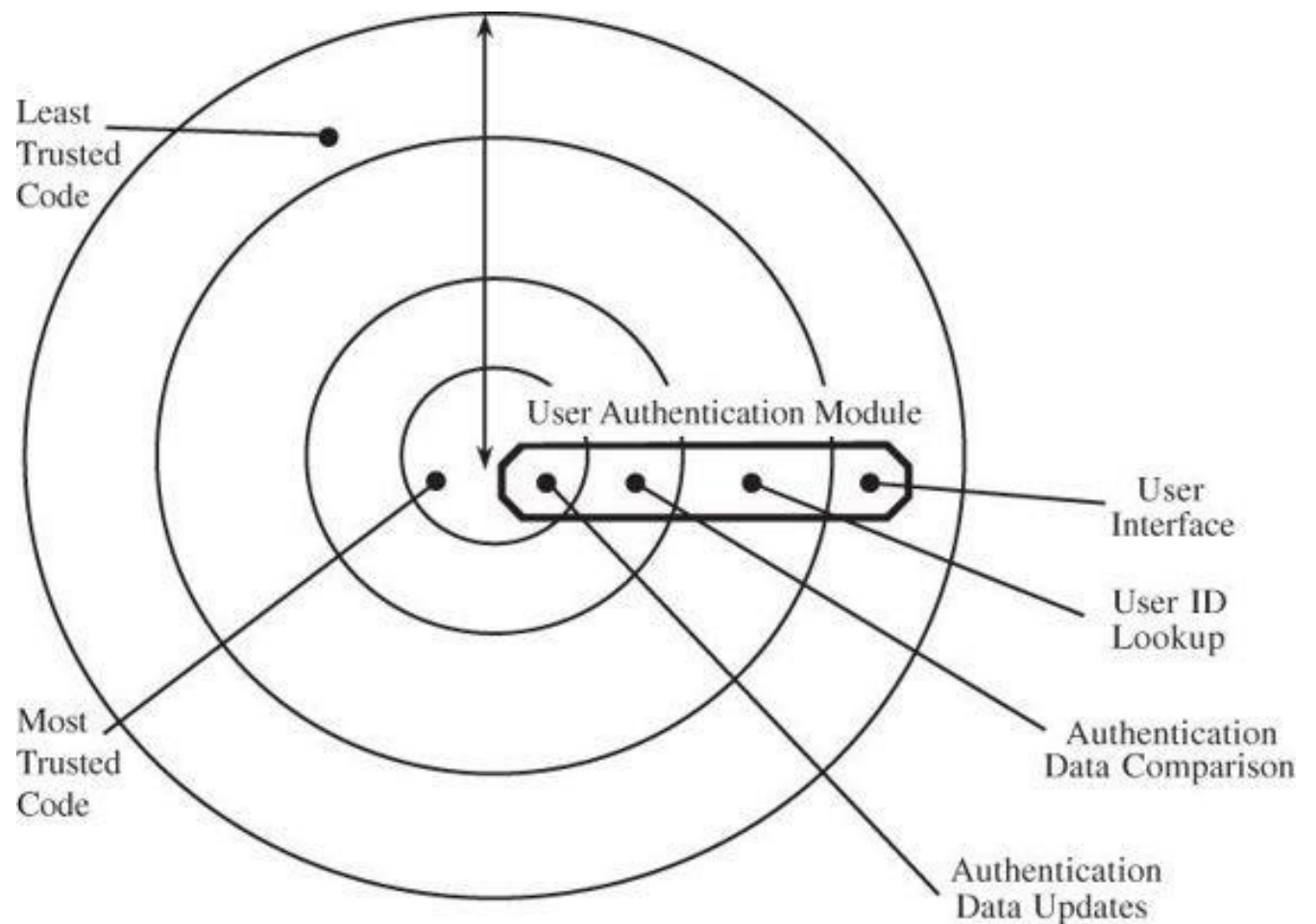
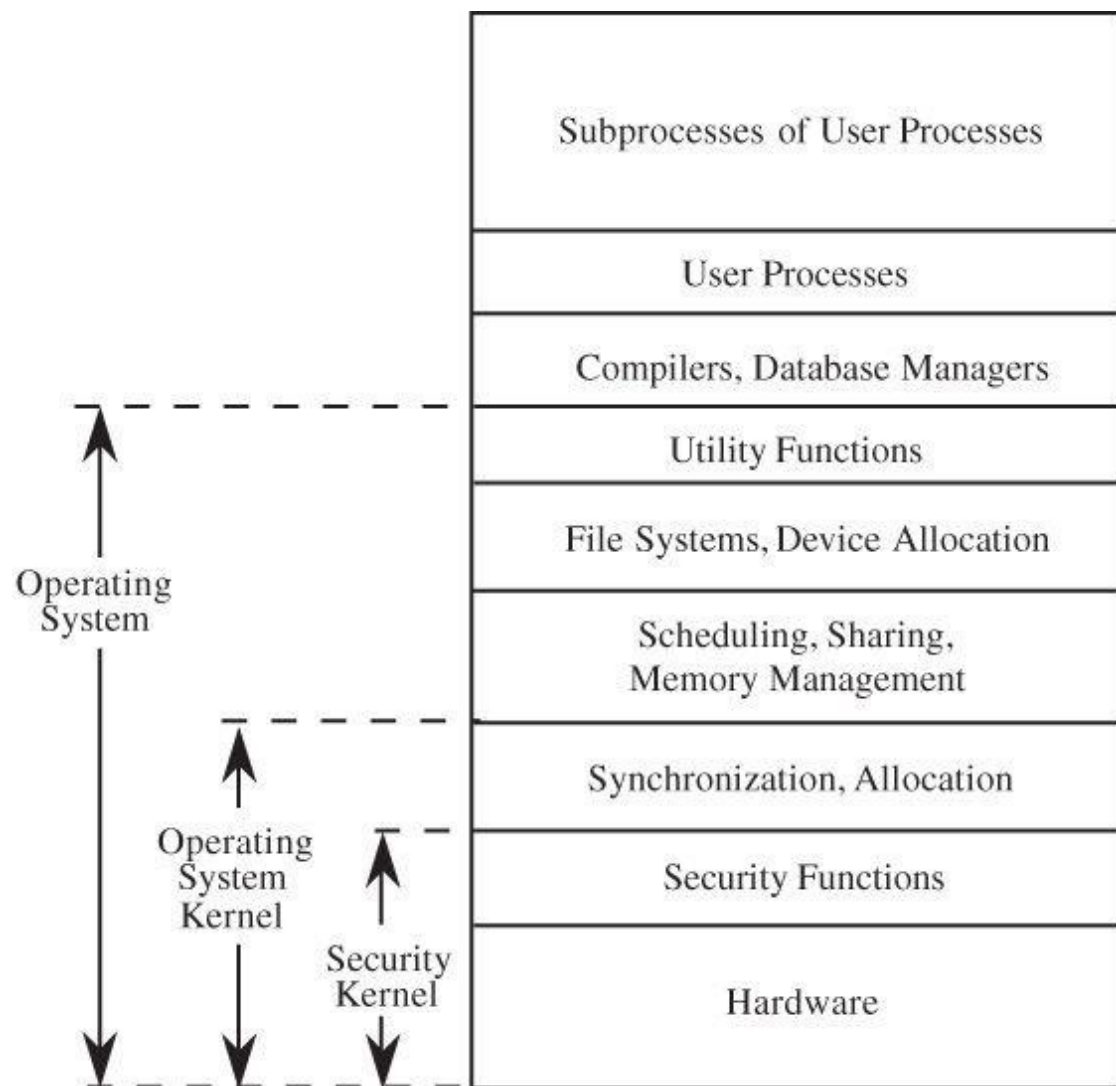
But:

- how to protect these objects?
- how to tackle the need for sharing some of these objects between users?
- and at which resource granularity?

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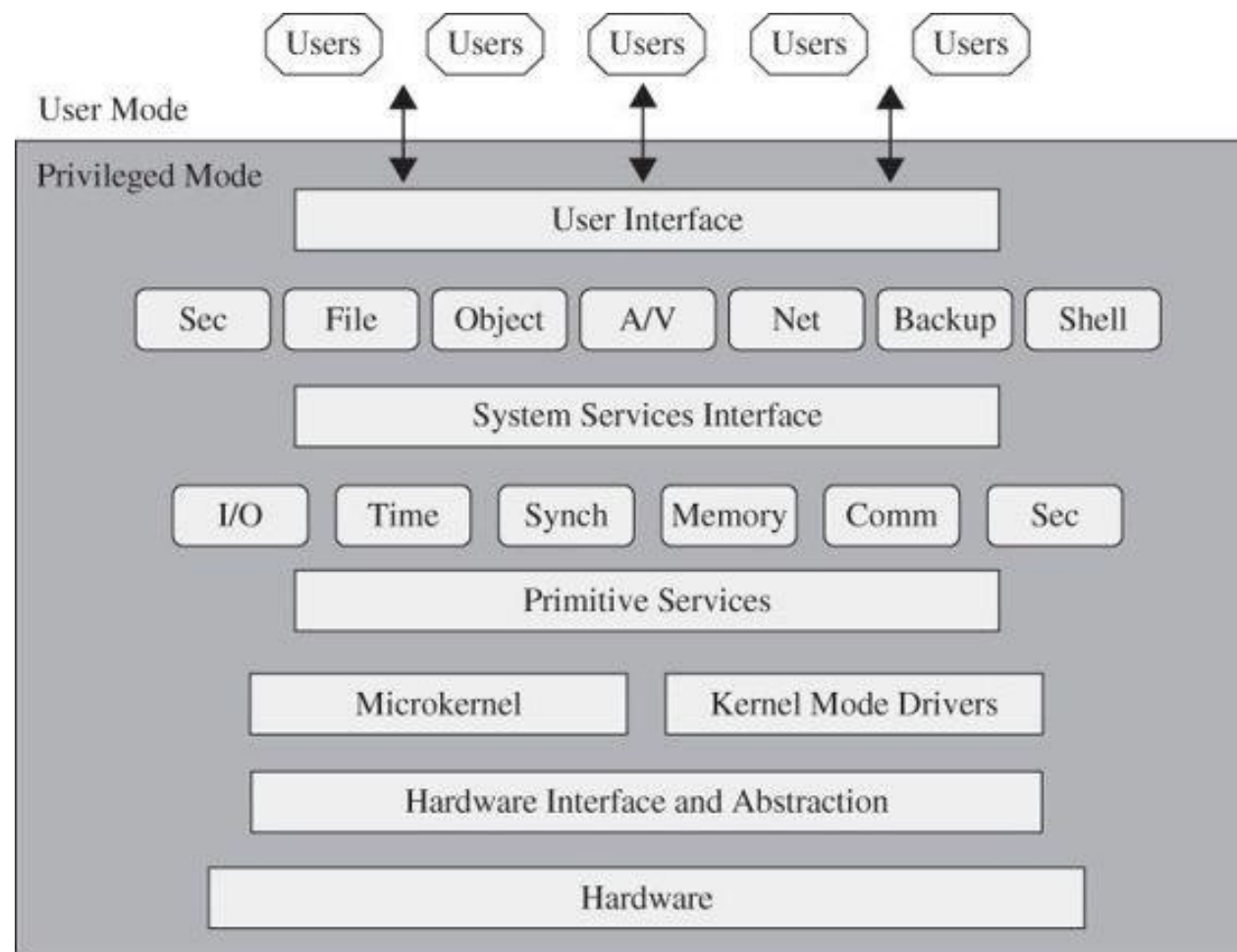
As a Layered System

Functions span across layers



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As a Modular System



Security and safety challenges:

- can come from different sources
- not all trustworthy
- and must integrate successfully

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Design for Self-Protection

An OS must be designed for self-protection

Against compromise from:

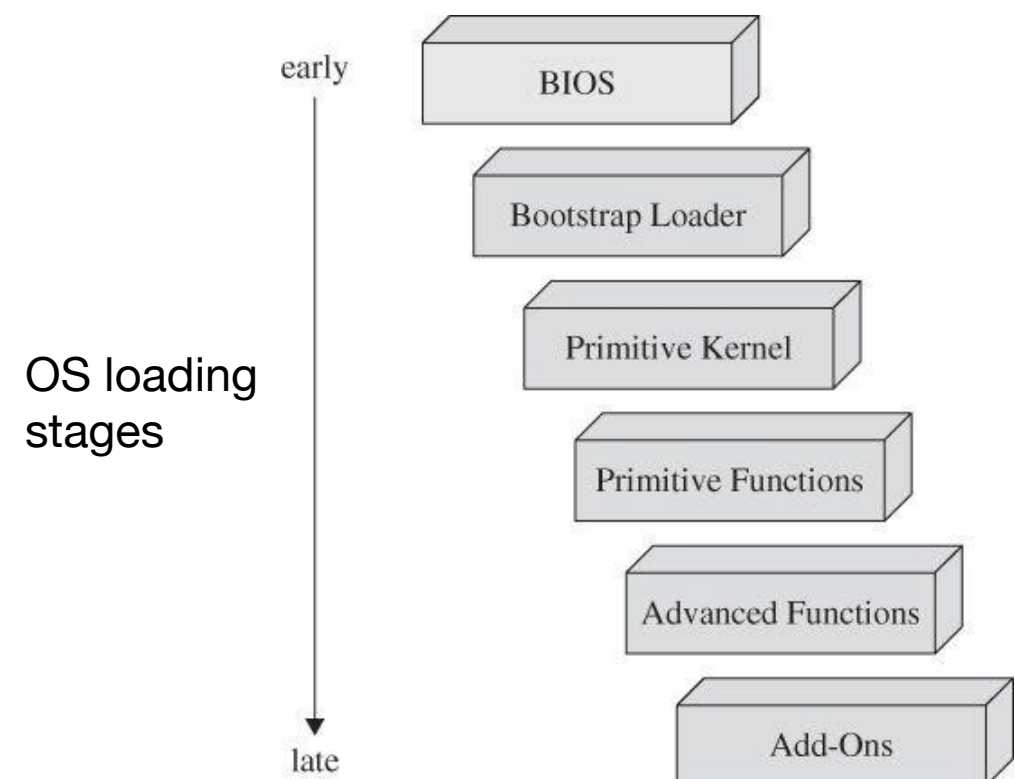
- malicious user programs
- incorporated modules

Challenges:

- timing, coordination, hand-off
- changing requirements and functionality
- compatibility, consistency

Limited knowledge:

- who can it trust
- for what capabilities



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Tools to Implement Security

Access control paradigm:

- Reference monitor: a subject is permitted to access an object in a particular mode, and only such authorised accesses are allowed
- Access control techniques: access control list (ACL), privilege list, and capabilities
- OS needs to implement both the underlying tables supporting access control and the mechanism that checks for acceptable uses

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Tools to Implement Security

Audit:

- log: who, what, when, how
- tool for reacting after a security breach (not preventing it)
- what information was compromised, by whom and when
- help in preventing future incidents
- adequate level of logging
- useful only if can be analysed (info overload)

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Tools to Implement Security

Virtualisation:

- availability of one set of resources by using a different set
- only the set of resources the user is entitled to access
- e.g. virtual machine, sandbox, honeypot
- useful for flexible resource allocation

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Tools to Implement Security

Hypervisor

- virtual machine monitor
- implements a virtual machine
 - receives all user access requests
 - passes along those that apply to real resources the user is allowed to access
 - redirects other requests to the virtualised resources
- separation and overlap of resources
- multiple operating systems and multiple hardware

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Tools to Implement Security

Sandbox

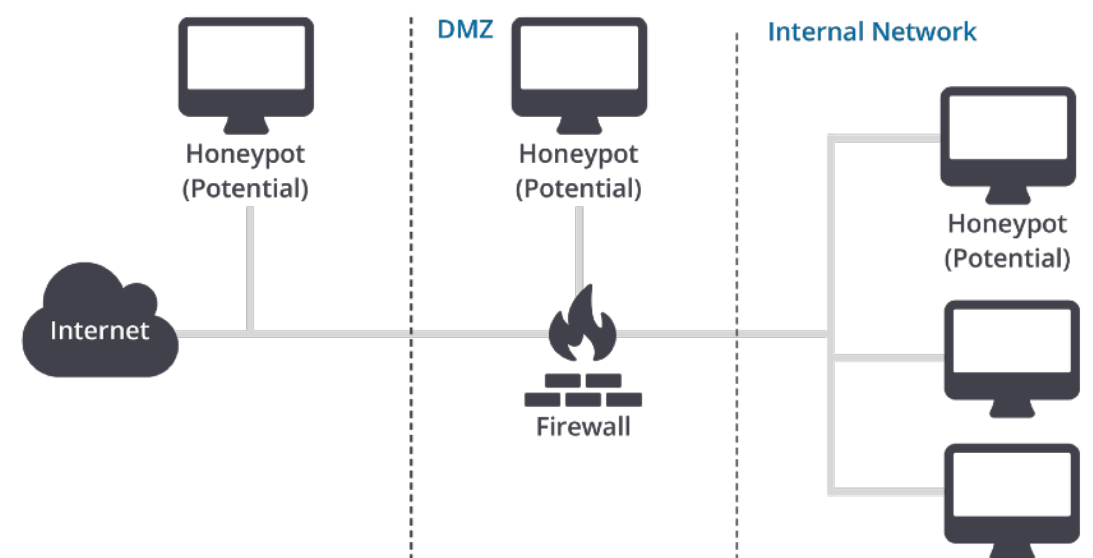
- protected execution environment
- isolated from each other
- often virtualised resources
- e.g. Docker containers
 - each container has its own software, libraries and configurations
 - communicate with each other through well-defined channels
 - all containers run by a single OS kernel
 - more lightweight than virtual machines

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Tools to Implement Security

Honeyport

- vm or application based
 - fake, monitored, network-accessible environment intended to lure an attacker
 - detect and/or deflect an attack
 - study attack techniques and objectives
 - shows limited (safe) set of resources for the attacker
 - may suggest it is running well-known vulnerable services
- types: pure, low and high-interaction



Example deployment of honeypots in a network infrastructure

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Tools to Implement Security

Pure honeypots:

- full-fledged production systems
- attacker is monitored using a bug tap installed on the honeypot's link to the network
- useful but stealthiness can be ensured by a more controlled mechanism

Low-interaction honeypots:

- simulate only the services frequently requested by attackers.
- relatively few resources, short response time, less code to implement, reduced complexity of the virtual system's security

High-interaction honeypots:

- imitate the activities of the production systems hosting a variety of services
- an attacker may be allowed a lot of services to waste their time (e.g. HoneyNet)
- difficult to detect, but expensive to maintain

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Tools to Implement Security

Separation:

- keep one user's objects separate from other users

Types of Separation:

- physical separation: different processes use different physical objects according to security requirements
- temporal separation: processes executed at different times according to security requirements
- logical separation: processes executed as if no other processes and objects exist outside each permitted domain
- cryptographic separation: process data and computation are concealed and meaningless to other processes

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Tools to Implement Security

Remarks on separation:

- types of separation can be used together
- increasing order of complexity and decreasing order of security: physical, temporal, logic
- too inefficient: physical, temporal
- efficiency favours shifting the burden of protection to the OS

Sharing:

- separation is only half the answer
- need for sharing of objects between users

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Tools to Implement Security

Different kinds of separation and sharing:

- Do not protect. Appropriate when sensitive procedures are run at separate times.
- Isolate. Processes running concurrently are unaware of the presence of each other.
- Share all or nothing. Owner declares an object to be public (available to all) or private (available to the owner).
- Share but limited access. OS as guard between users and objects, ensuring that only authorised access occurs.
- Limit use of an object. Not only limits access to objects but also the use made of that object after it has been accessed (e.g. view but not copy, or copy but not print).
- Remark: increasing order of complexity to implement and of granularity

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Tools to Implement Security

Hardware protection of memory

- separation and sharing
- sharing of parts of memory between processes and users
- coexistence of the operating system and user processes
- mechanisms can be hard to implement
- but mostly can be reduced to hardware
- sharing can be efficient and resistant to tampering
- e.g.: fence, base/bounds, tagged, segmented, paginated

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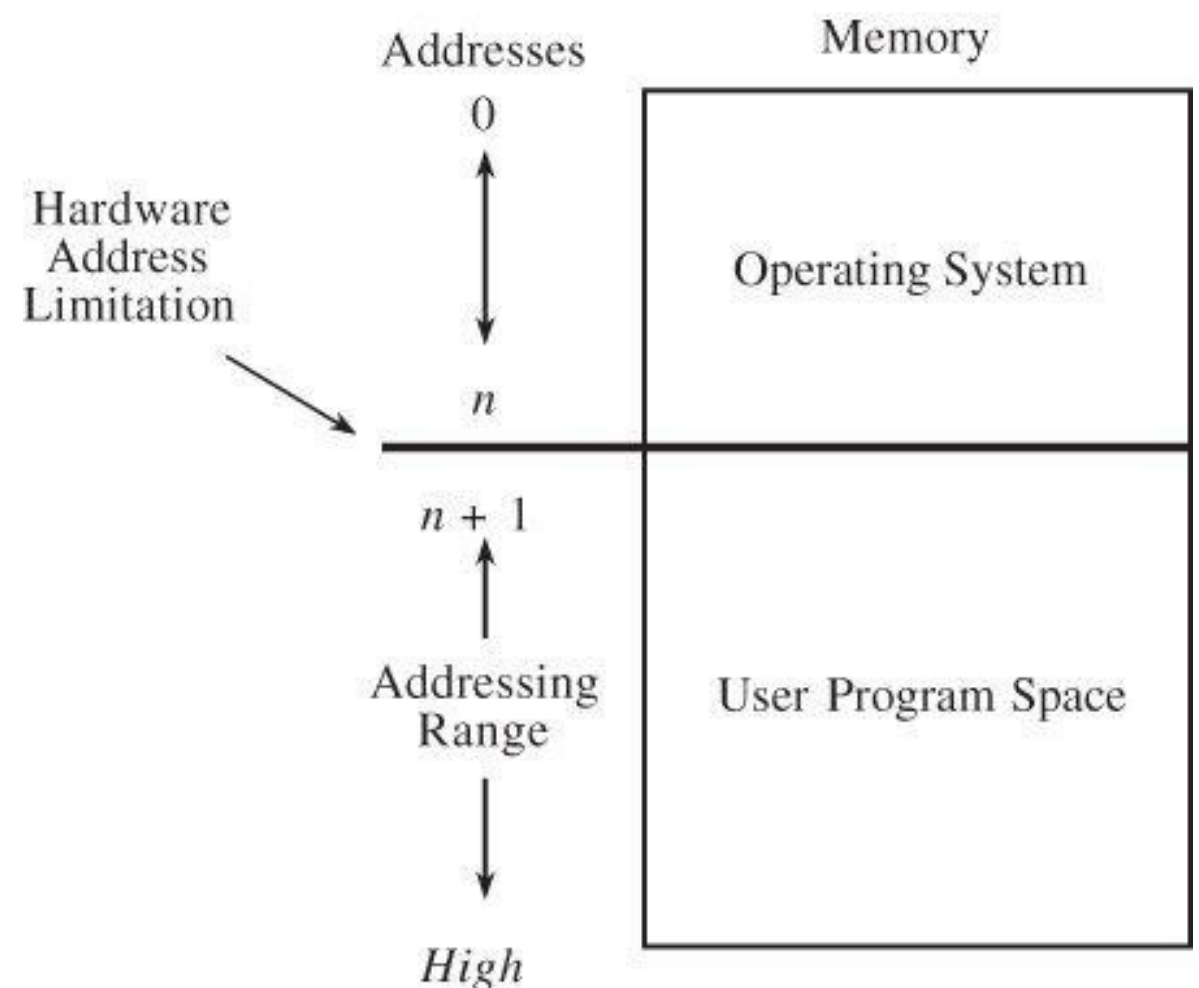
Memory Protection: Fence

Simplest form of memory protection:

- introduced in single-user operating systems
- a method to confine users to one side of a boundary

Fence as predefined memory address:

- a predefined amount of space reserved for OS
- very restrictive

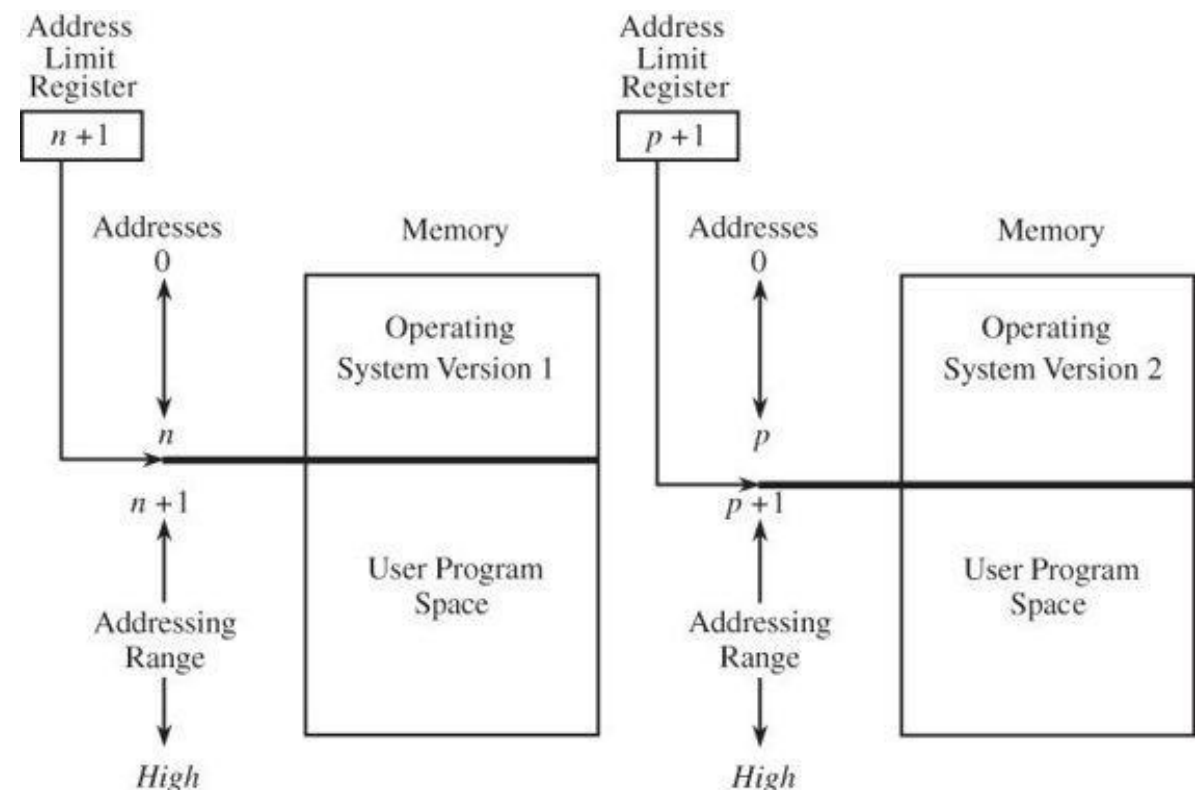


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Memory Protection: Fence

Fence set in hardware register:

- amount of space for the OS can be changed
- access to memory addresses is automatically compared to fence register
- if greater than the access was granted, if lower it was denied



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Memory Protection: Fence

Protects in one direction only:

- the operating system can be protected from the users
- user programs cannot be protected from each other
- user programs cannot protect areas of the program:
 - e.g. define areas as non-writable or non-executable

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Memory Protection: Base/Bounds Registers

Base/bounds registers:

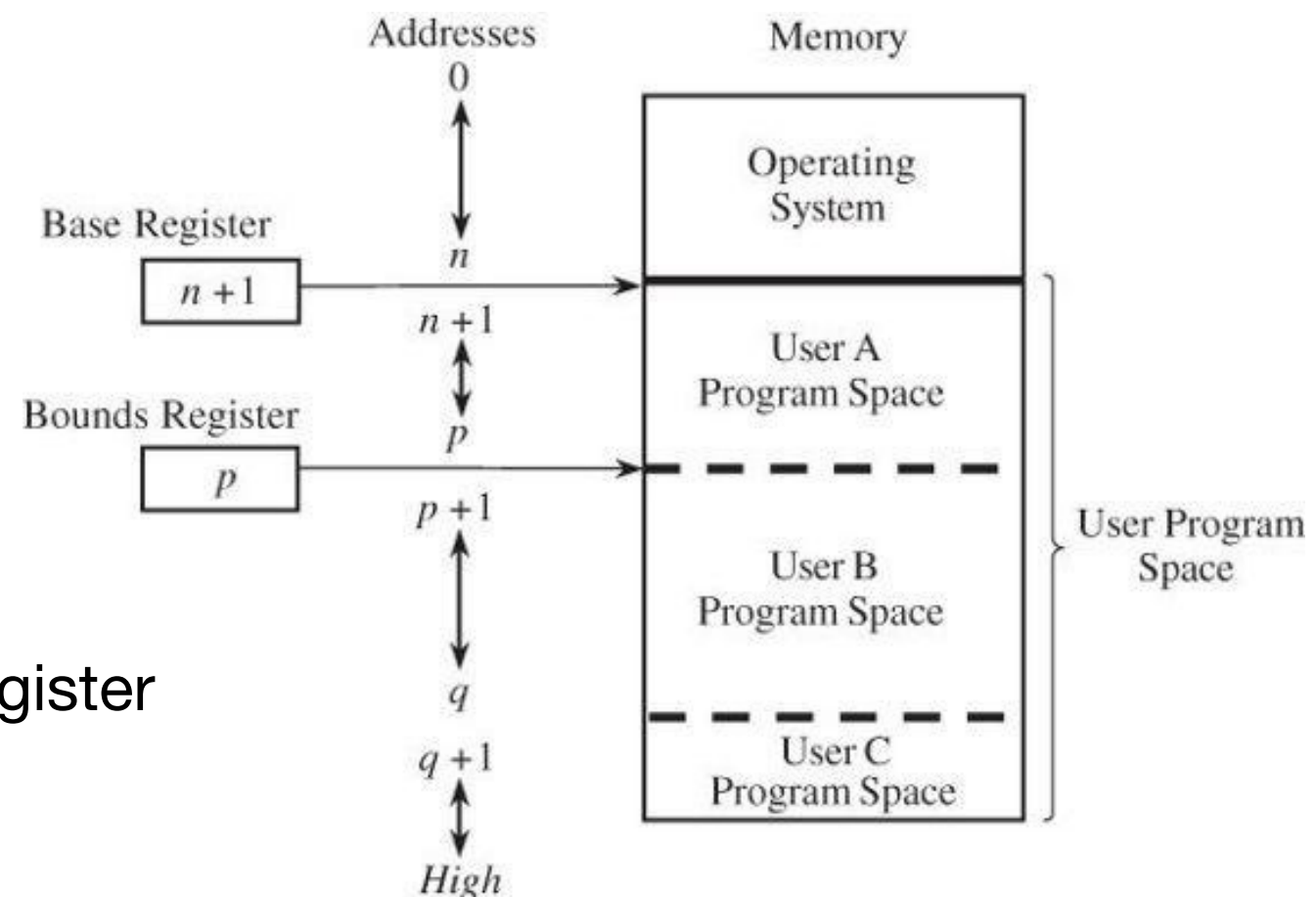
- surrounds a program address space
- specific to each user program
- updated on context switching

User program addresses:

- always added to a base register
- always checked against a bounds register

Protection as well as relocation:

- important for multiprogramming systems



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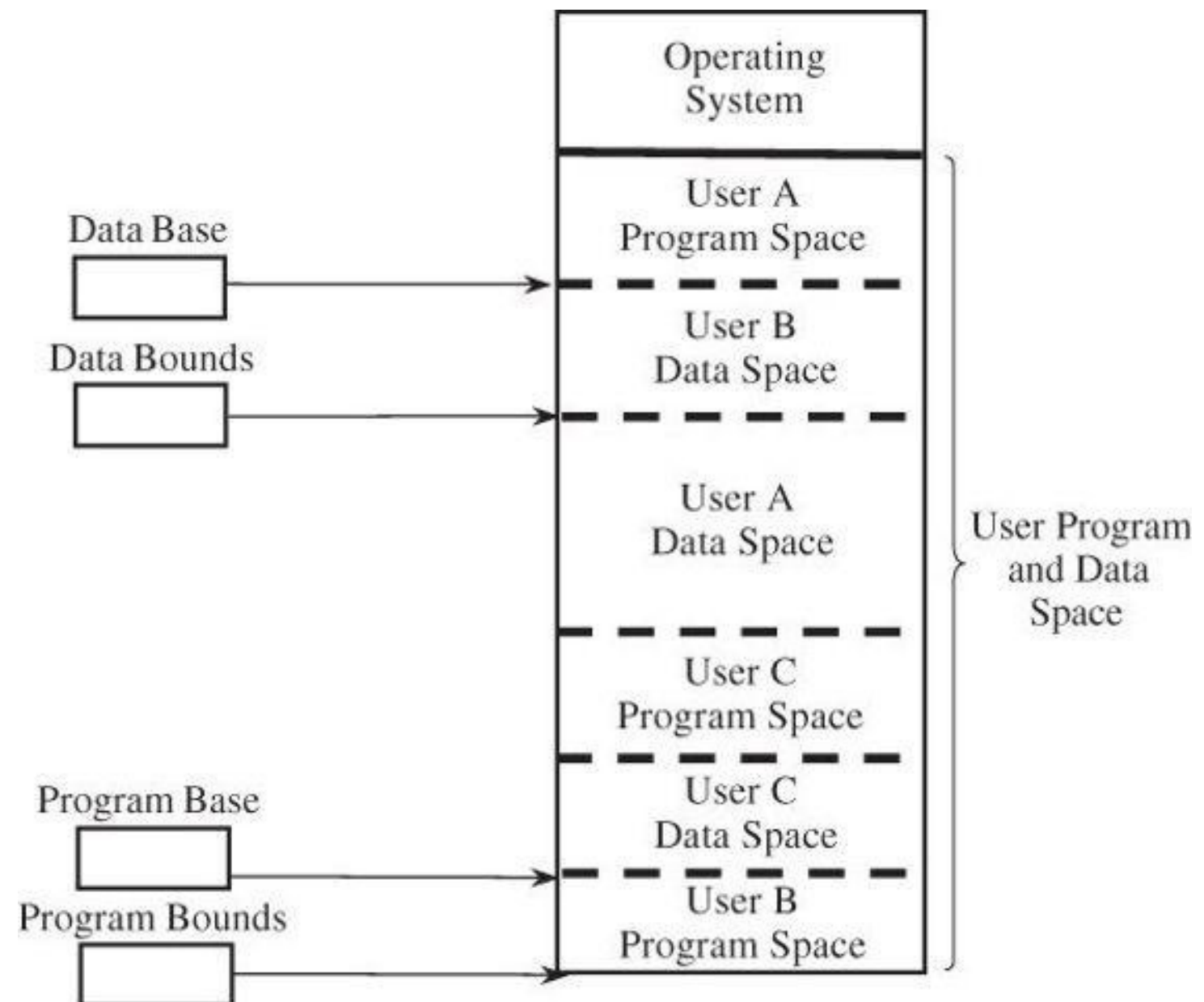
Memory Protection: Base/Bound Registers

Base/bounds registers can be used to protect specific memory areas of user programs:

- e.g. code and data

Could be extended to support more than two areas

- but practical limit is two register pairs only

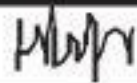
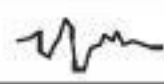
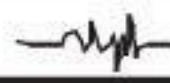
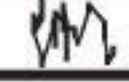
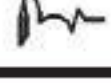
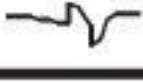


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Memory Protection: Tagged Architecture

Tagged architecture:

- every word has extra bits identifying the access rights to that word (or range of words)
- extra bits are set only by privileged instructions (OS)
- these bits are checked on each access to the word
- for practical reasons the number of bits was always small
- not popular due to compatibility challenges

Tag	Memory Word
R	0001
RW	0137
R	0099
X	
X	
X	
X	
X	
X	
R	4091
RW	0002

Code: R = Read-only RW = Read/Write
X = Execute-only

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Memory Protection: Virtual Memory

Virtual memory:

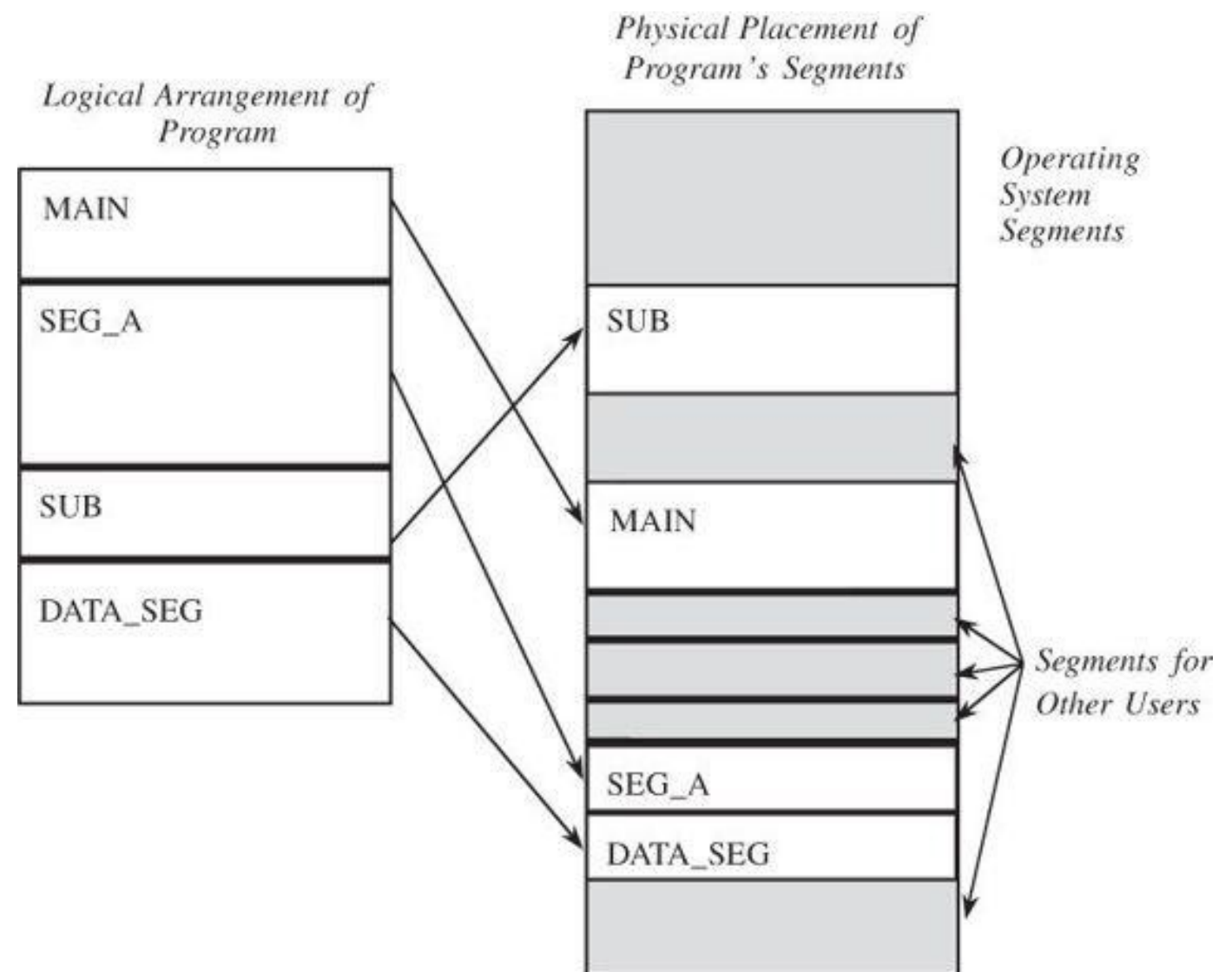
- hardware support generally (e.g. MMU)
- used in most general purpose OSes
- advantages in addressing and protection
- e.g. segmentation, paging, paging with segmentation

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Memory Protection: Segmentation

Segmentation:

- program divided into logical units (different sizes)
- addressing: name of segment plus offset
- similar to an “unbound” number of base/bounds pairs
- process: OS maintains a table of accessible segment names
- map: segment names and real addresses (and size)

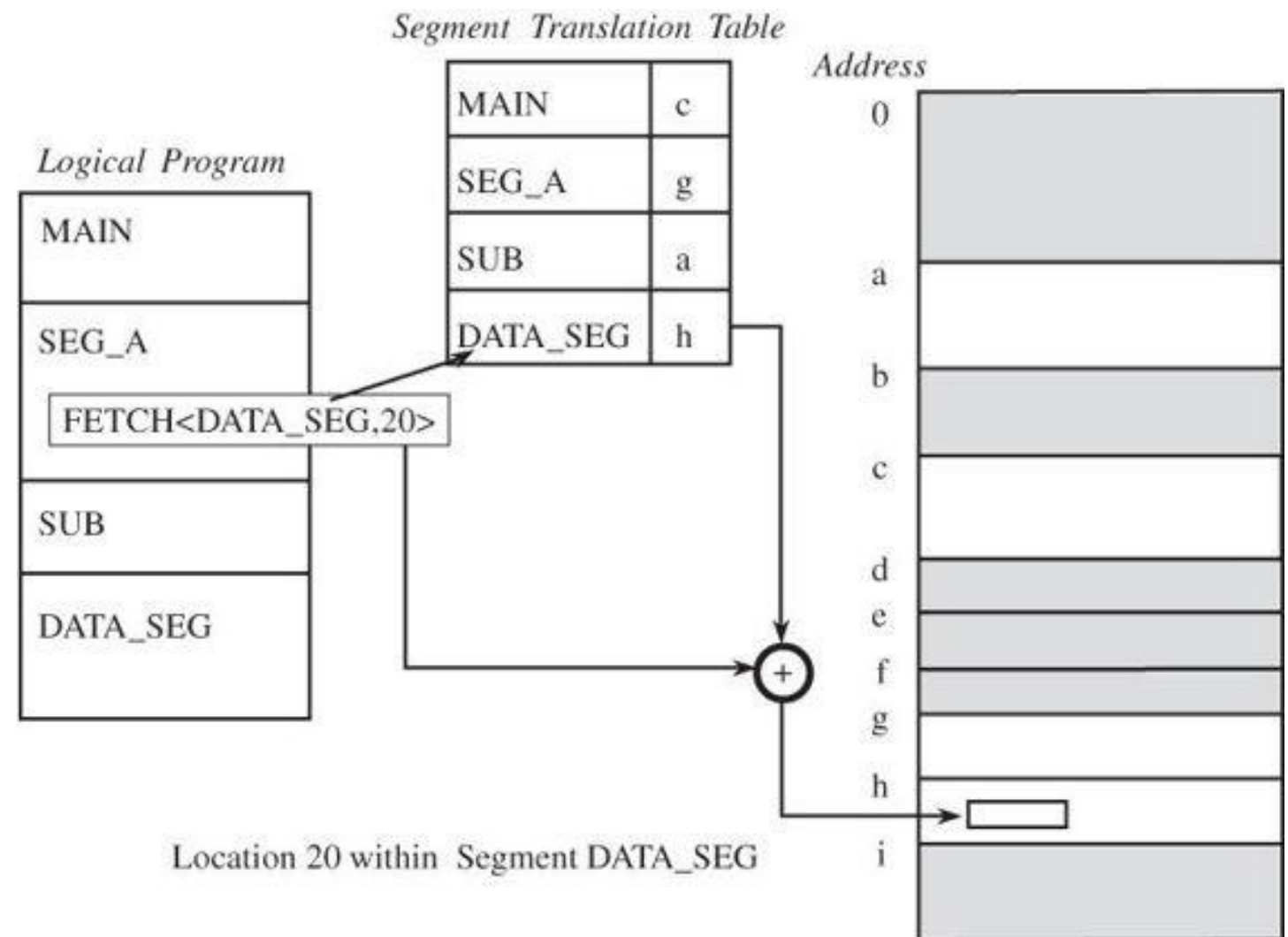


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Memory Protection: Segmentation

Segmentation:

- advantages: relocation, sharing, swap, protection (on each access, MMU), fast address translation, no internal fragmentation
- disadvantages: inconvenience (e.g. address must identify a segment), external fragmentation

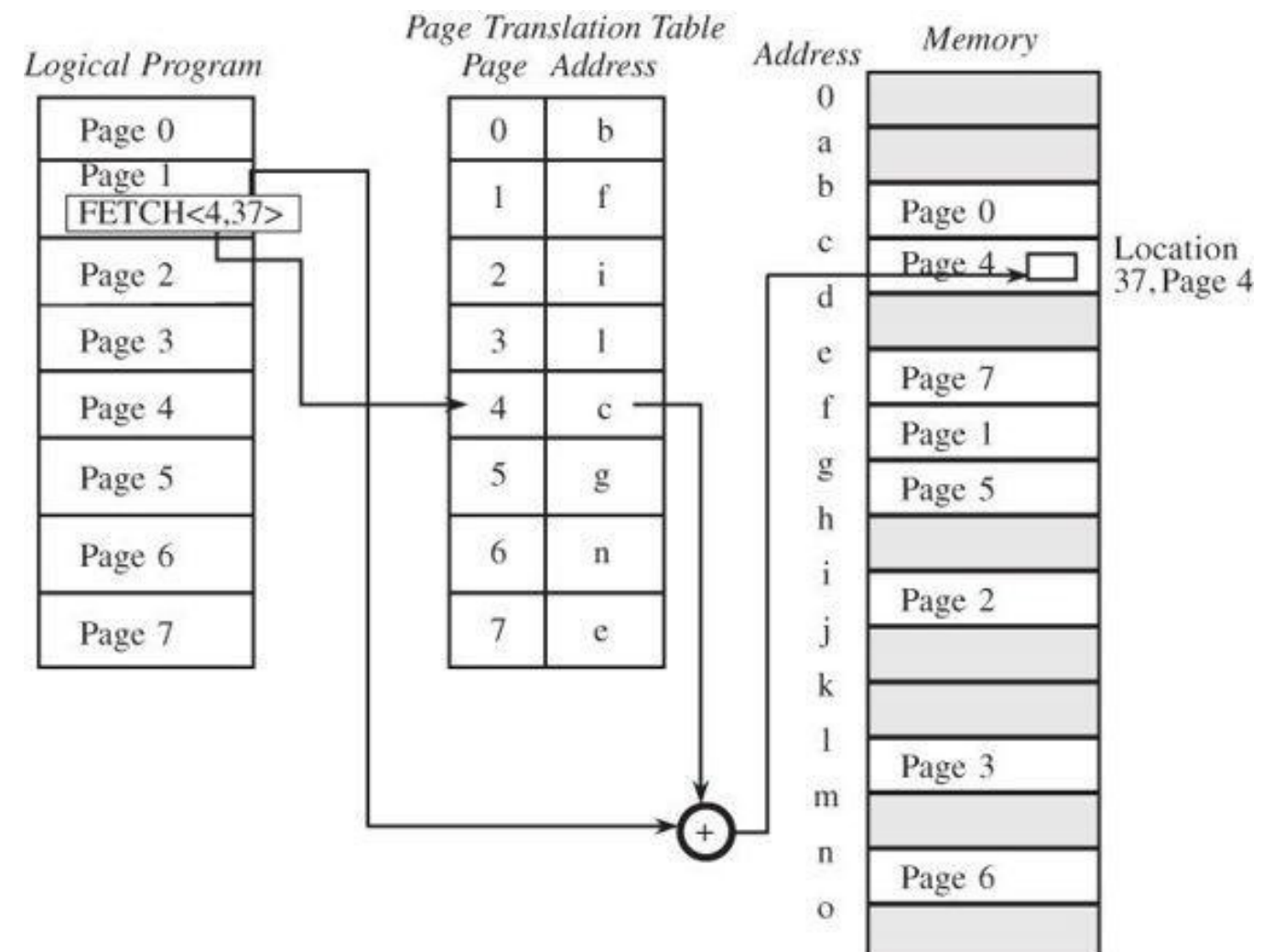


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Memory Protection: Paging

Paging:

- program divided into equal-sized units (frame, e.g. 4KB)
- addressing: page frame (number) plus offset
- process: OS maintains a table of accessible page frames
- map: page frames and real addresses
- very low internal and no external fragmentation
- advantage: no internal or external fragmentation
- disadvantage: more complex, slower than segmentation, no logical unity



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Memory Protection: Paged Segmentation

Paged Segmentation:

- process: set of logical segments
- segment: set of fixed size pages
- advantages: flexible page sizes, simplified memory allocation, additional level of protection

