

Realization	Processor			
	$x_{1,1}$	$x_{1,2}$	\dots	$x_{1,p}$
	$x_{2,1}$	$x_{2,2}$	\dots	$x_{2,p}$
	$x_{3,1}$	$x_{3,2}$	\dots	$x_{3,p}$
	$x_{4,1}$	$x_{4,2}$	\dots	$x_{4,p}$
	$x_{5,1}$	$x_{5,2}$	\dots	$x_{5,p}$
	$x_{6,1}$	$x_{6,2}$	\dots	$x_{6,p}$
	$x_{7,1}$	$x_{7,2}$	\dots	$x_{7,p}$
	$x_{8,1}$	$x_{8,2}$	\dots	$x_{8,p}$
	\vdots	\vdots	\ddots	\vdots
	$x_{N-3,1}$	$x_{N-3,2}$	\dots	$x_{N-3,p}$
	$x_{N-2,1}$	$x_{N-2,2}$	\dots	$x_{N-2,p}$
	$x_{N-1,1}$	$x_{N-1,2}$	\dots	$x_{N-1,p}$
	$x_{N,1}$	$x_{N,2}$	\dots	$x_{N,p}$