

Interface description

Pin. No.	Name	Description	Note for our implementation
1	DAT3	MMC: Chip select	Chip IO Pin, <code>mmc_cs</code> , D7
2	CMD	Command	Chip IO Pin, <code>mmc_mosi</code> , D8, (MOSI – master out, slave in)
3	GND	Ground	Board ground
4	VDD	Voltage	Supply Voltage (+3.3 V)
5	CLK	Clock	Chip IO Pin, <code>mmc_clock</code> , D10
6	GND	Ground	Board ground
7	DAT0	Data	Chip IO, <code>mmc_miso</code> , B4
8	DAT1	Data	Pull up (Voltage via resistor)
9	DAT2	Data	Pull up (Voltage via resistor)

