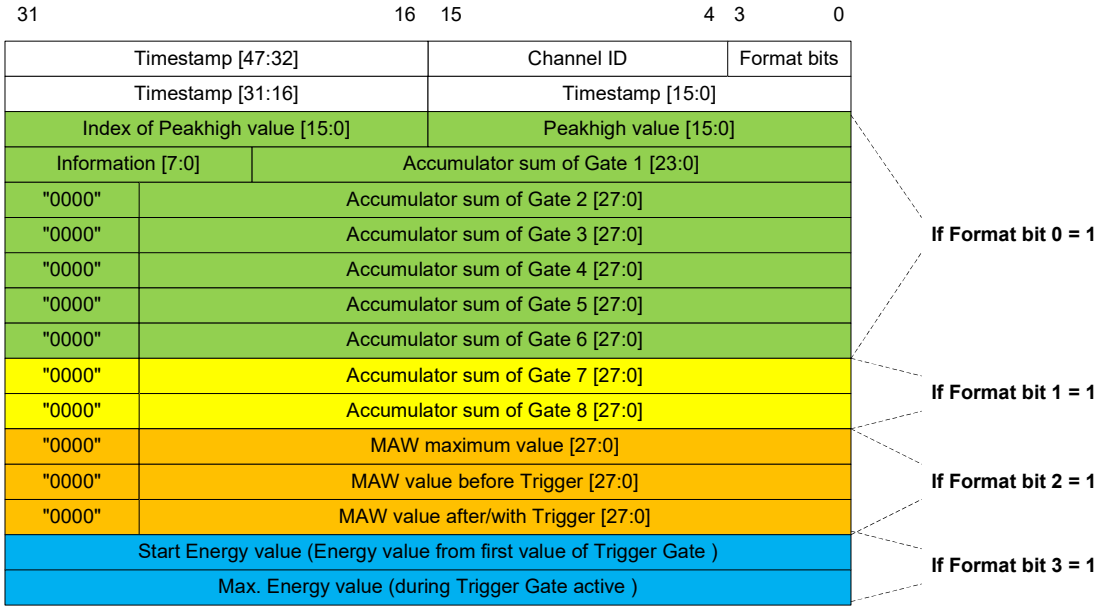

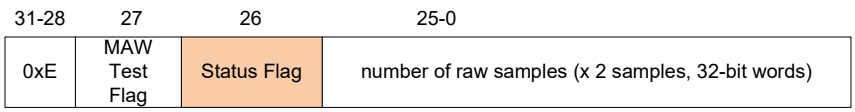

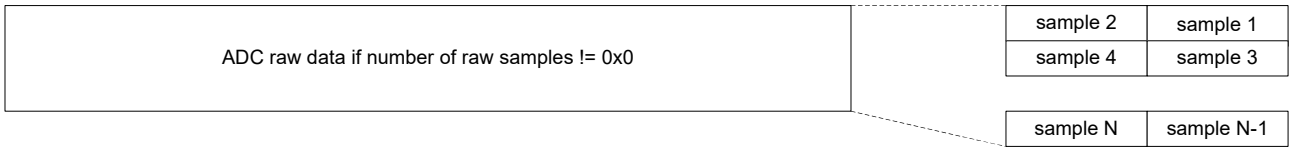

MAW Test data

Information [7:0]

- bit 7: Overflow flag
- bit 6: Underflow flag
- bit 5: RePileup flag
- bit 4: Pileup flag
- bit 5-0: reserved

Status Flag
until ADC FPGA Versions V0125-0009/V0250-0009

Status Flag = RePileup or Pileup Flag

Status Flag
from ADC FPGA Versions V0125-000A/V0250-000A

Internal Trigger enabled	External Trigger enabled	Pileup Trigger enabled	Status Flag
1	x	x	RePileup or Pileup
0	1	x	Internal Trigger
0	0	1	1