

Criar um novo projeto, para ser executado no kit EasyFPGA, no Quartus[®] Prime Lite 20.1

1 - Na tela inicial, ir em File / New Project Wizard

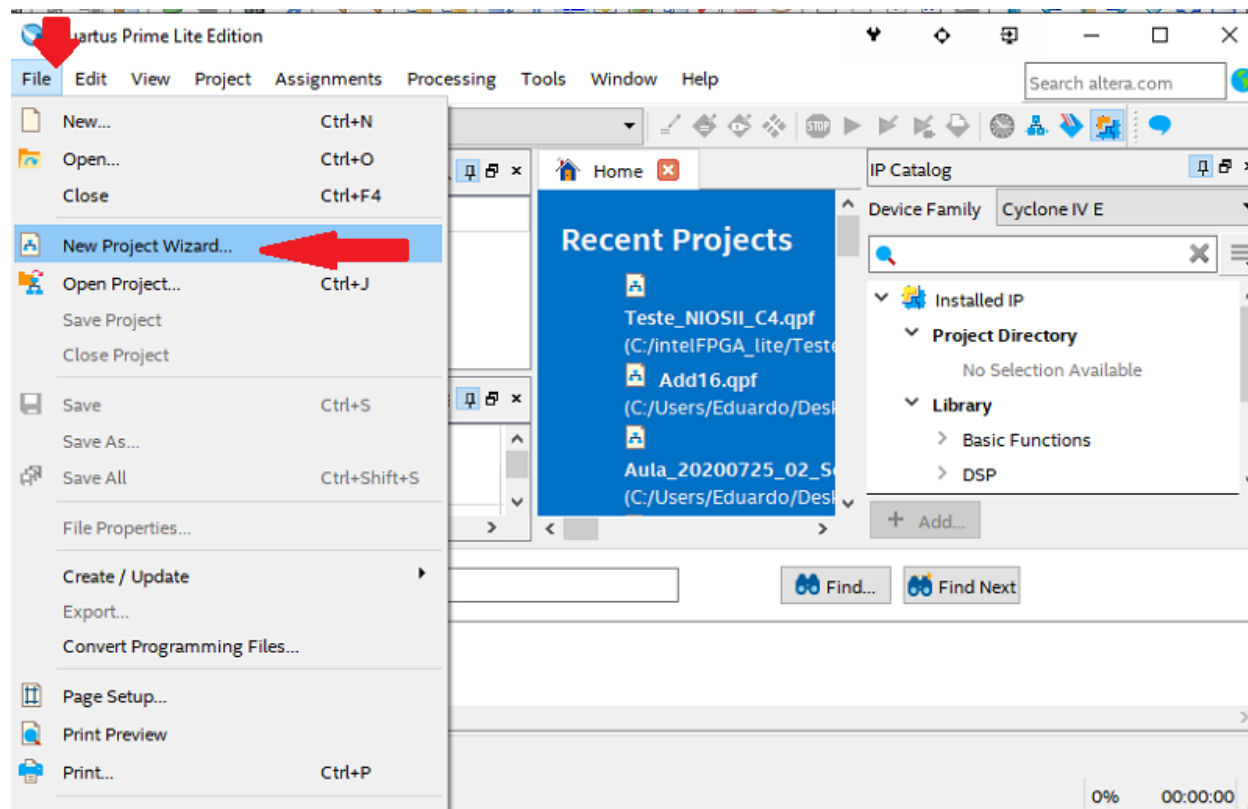


Figure 1: 1

2 - Na Janela que irá abrir, clicar no botão “Next >”

3 - Escolher o diretório do projeto e o nome do projeto e clicar em “Next >”

4 - Escolher a opção “Empty Project” e clicar em “Next >”

5 - Na tela seguinte, clicar novamente em “Next >”

6 - Selecionar a família “Cyclone IV” e depois o CI (*device*) EP4CE6E22C8 e, por fim, clicar no botão “Finish”

7 - Na tela do projeto, criar o arquivo HDL onde será escrito o projeto

7.1 - Se o projetousar a linguagem Verilog, selecionar a opção “Verilog HDL File”

7.2 - Se o projetousar a linguagem VHDL, selecionar a opção “Verilog HDL File”

7.3 - O Nome do novo arquivo deverá ser exatamente o mesmo do projeto.

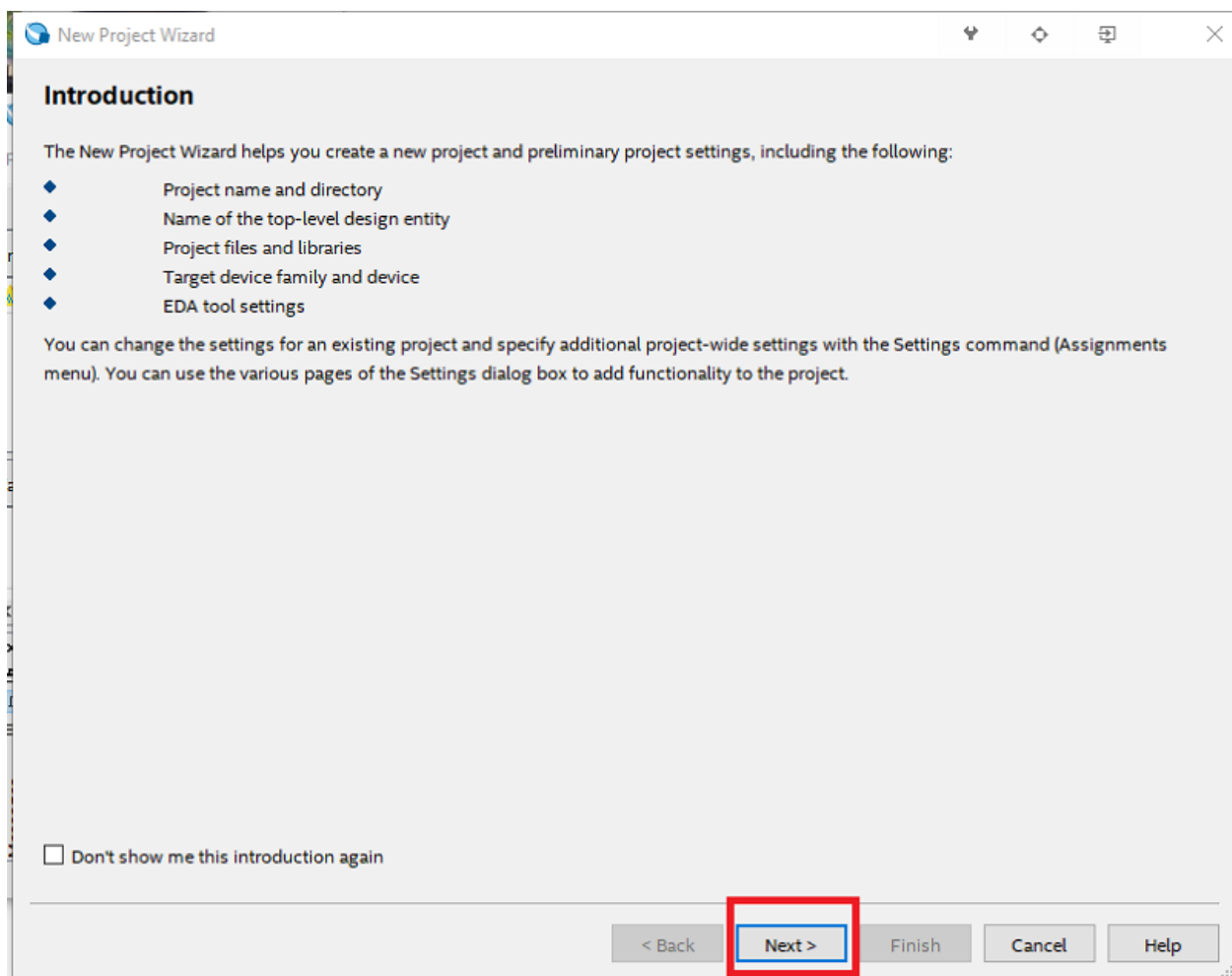


Figure 2: 2

New Project Wizard

Directory, Name, Top-Level Entity

What is the working directory for this project?

C:\intelFPGA_lite\Nome_Projeto

What is the name of this project?

Nome_Projeto

What is the name of the top-level design entity for this project? This name is case sensitive and must exactly match the entity name in the design file.

Nome_Projeto

Use Existing Project Settings...

< Back Next > Finish Cancel Help

Figure 3: 3

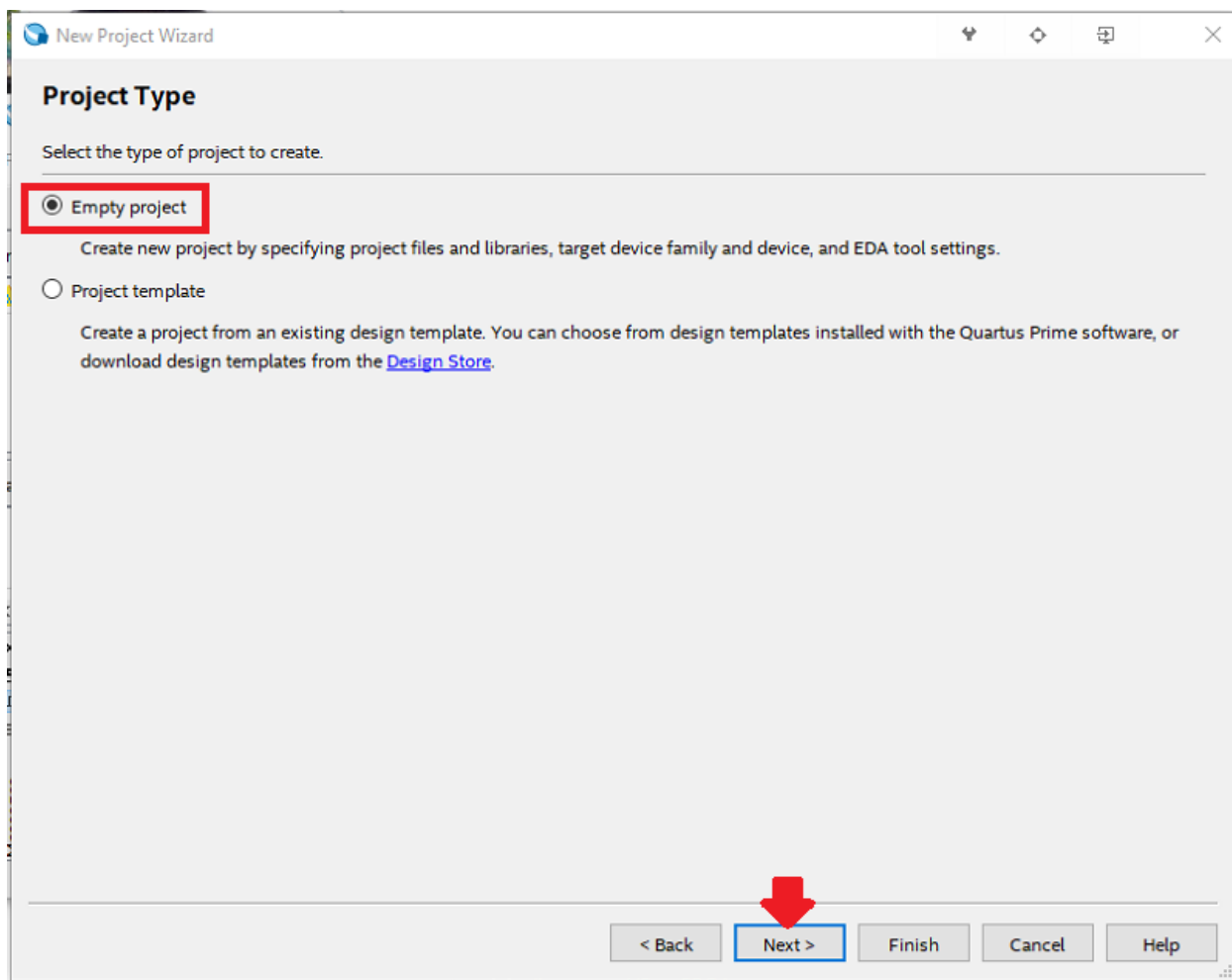


Figure 4: 4

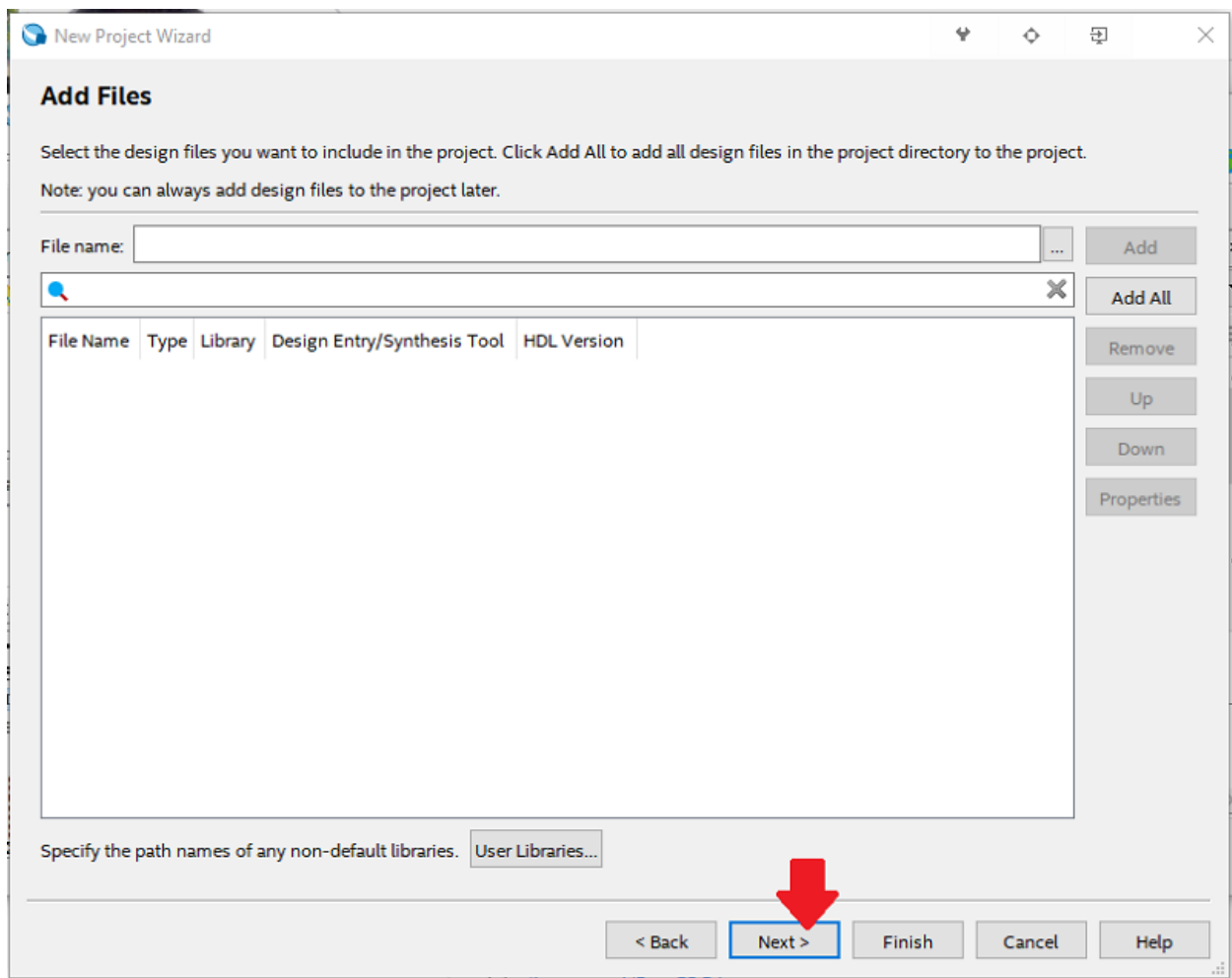


Figure 5: 5

New Project Wizard

Family, Device & Board Settings

Device Board

Select the family and device you want to target for compilation.
You can install additional device support with the Install Devices command on the Tools menu.

To determine the version of the Quartus Prime software in which your target device is supported, refer to the [Device Support List](#) webpage.

Device family

Family: Cyclone IV E

Device: All

Target device

☐ Auto device selected by the Fitter

☒ Specific device selected in 'Available devices' list

☐ Other: n/a

Show in 'Available devices' list

Package: Any

Pin count: Any

Core speed grade: Any

Name filter: ep4ce6E22C8

☒ Show advanced devices

Available devices:

Name	Core Voltage	LEs	Total I/Os	GPIOs	Memory Bits	Embedded multiplier 9-bit element
EP4CE6E22C8	1.2V	6272	92	92	276480	30
EP4CE6E22C8L	1.0V	6272	92	92	276480	30

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< Back Next > Finish Cancel Help

Figure 6: 6

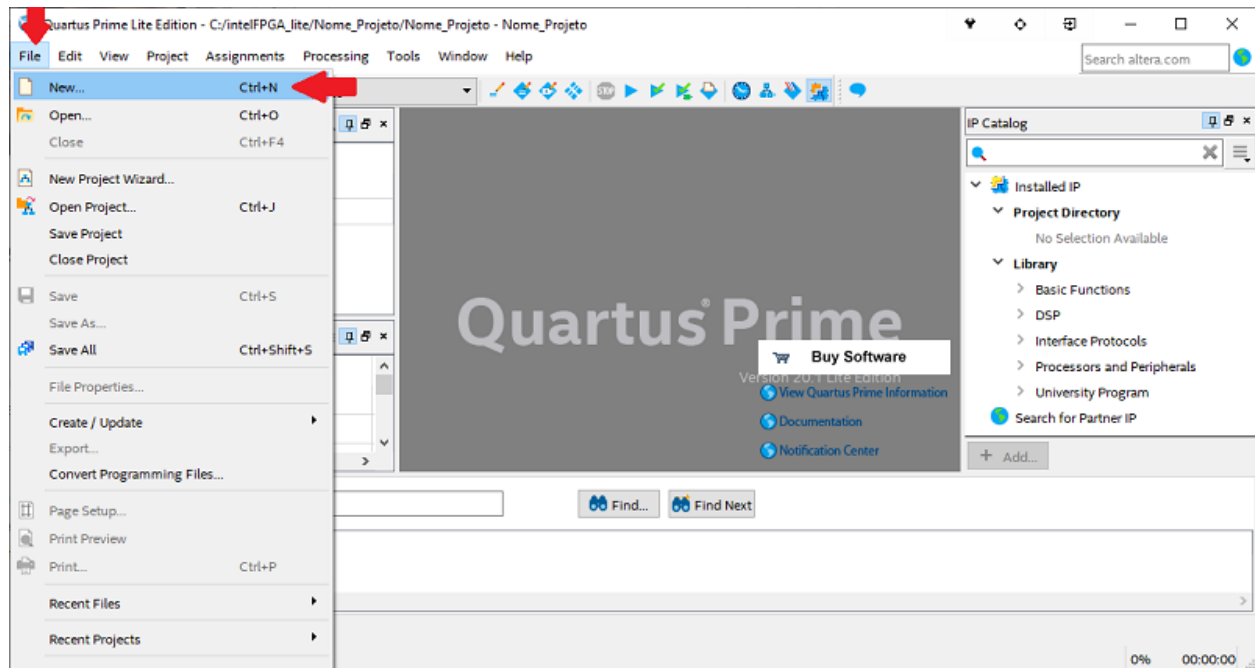


Figure 7: 7

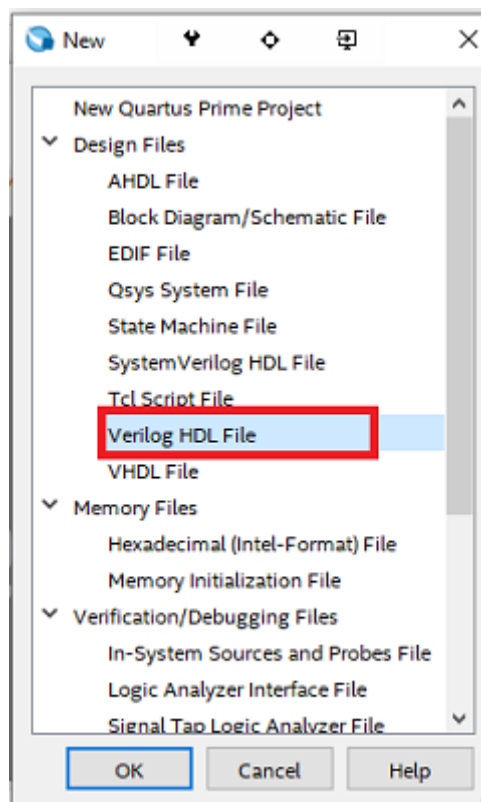


Figure 8: Novo arquivo - Verilog

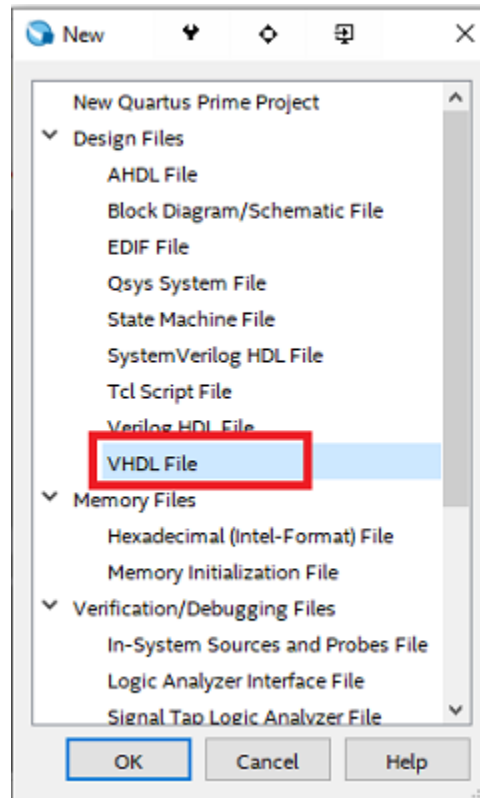


Figure 9: Novo arquivo - VHDL

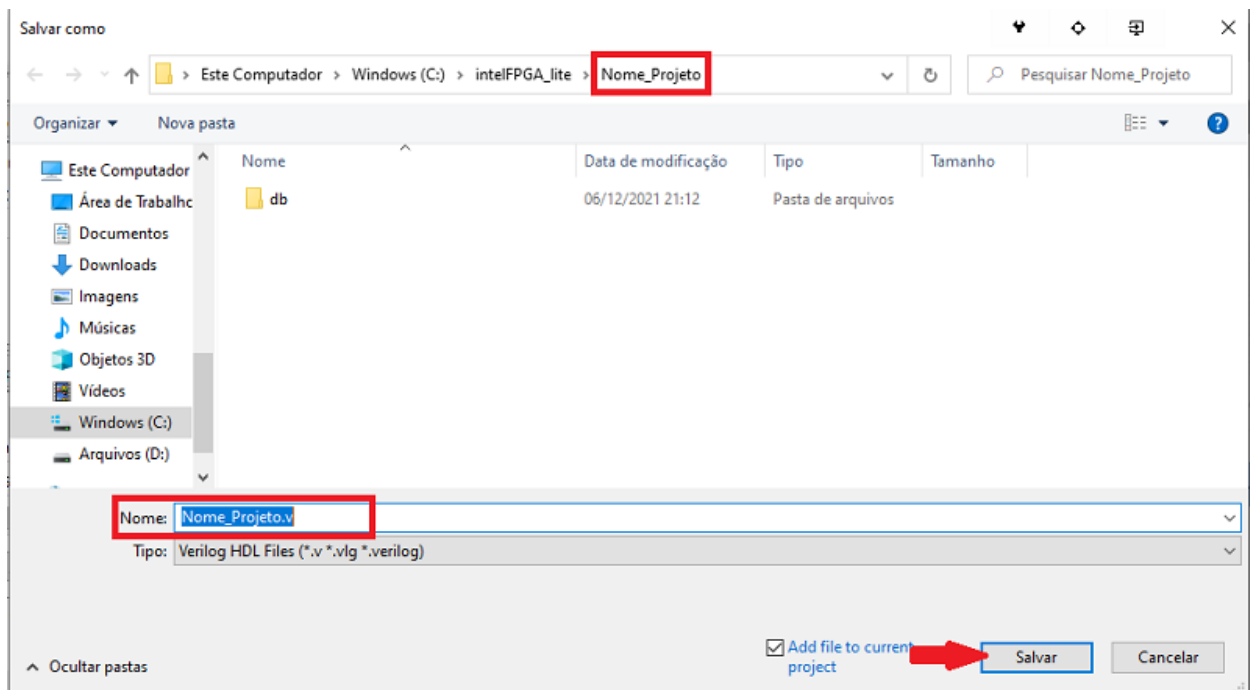


Figure 10: Salvando o arquivo com o código em HDL