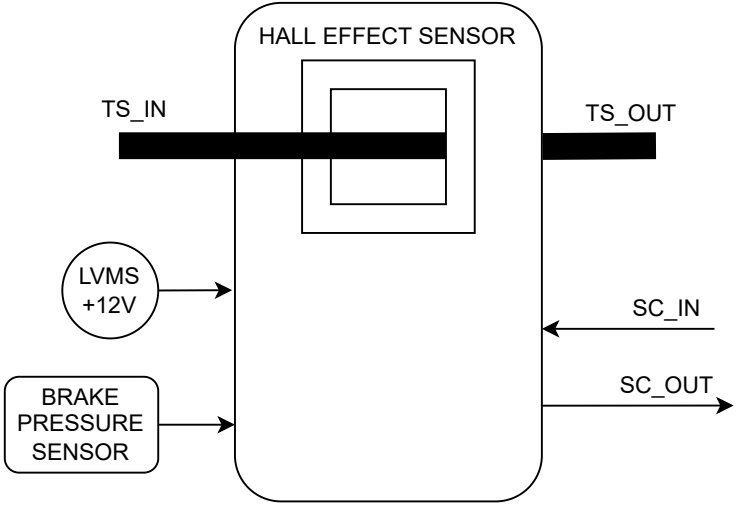
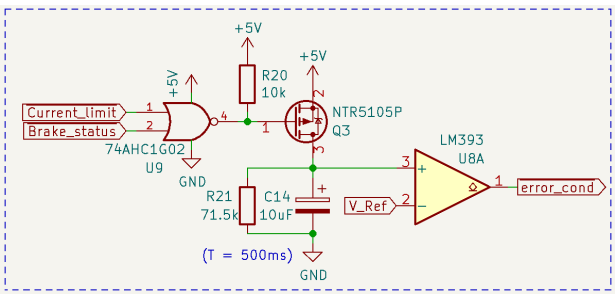


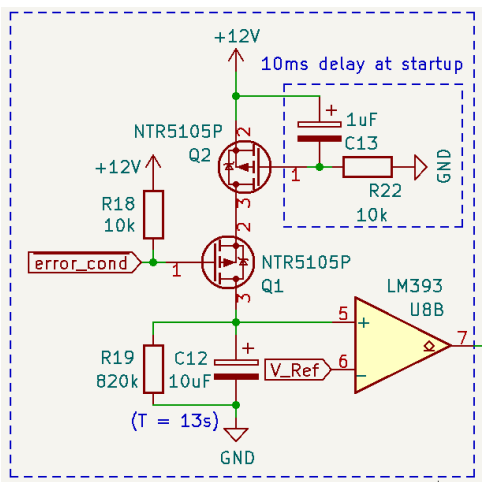
Test procedure:
1. Apply test current
2. Brake hard

Test current calculation:
I_{test} = 5kW/U_{max} = 5kW/454V = 11A



Delay between NOR port output rising and comparator output falling is given by the time required by voltage on C14 to go from 5V to V_{REF} = 2.5V, it is given by:

$$t = RC \cdot log\left(\frac{V_I}{V_F}\right) = 71.5k\Omega \cdot 10\mu F \log\left(\frac{5}{2.5}\right) = 0.495s$$



Delay between error_cond input rising and comparator output falling is given by the time required by voltage on C12 to go from 12V to V_{REF} = 2.5V, it is given by:

$$t = RC \cdot log\left(\frac{V_I}{V_F}\right) = 820k\Omega \cdot 10\mu F \left(\frac{12}{2.5}\right) = 12.9s$$

Delay for Reset on powerup is given by considering RC time:

$$\tau = RC = 10k\Omega \cdot 1\mu C = 10ms$$