Certified Performance Analysis for Embedded Systems Designers

TeleBench™ 1.1 Errata Sheet



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Errata Sheet for TeleBench™ 1.1

This document provides an overview of known errors within TeleBench™ Version 1.1 and describes workaround procedures, where available, to allow verification of results and a corrected implementation. Resolution of these errors will be made at the time of the next TeleBench version release. Suggested patches for all currently reported errors, which may be used for internal evaluations, are available to EEMBC members and licensees on the EEMBC Web site. Since these corrections could result in performance changes of the benchmarks, the original version of this code can still be used for certifications until the release of TeleBench Version 2.0.



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<u>FFT</u>

1. Expected results are incorrect

The reference FP output that is currently included with the benchmark does not match the expected output. Acceptable output for out of the box certification is the only output that matches exactly with the reference output on an x86 machine with GCC (checked by CRC). For optimized implementations, a reference that matches the expected result from MATLab, or that which has a high SNR to such a result will be accepted.

2. Array out of bounds access

CosineV[] array is accessed out of bounds on xsine256i.data t buf should allocate double the amount of memory in non interleaved mode.