

77317-104-28LF

X2-1 X2-2 X2-3 X2-4 X2-5 X2-6 X2-7 X2-8 X2-9 X2-10 X2-11 X2-12 X2-13 X2-14 X2-15 X2-16 X2-17 X2-18 X2-19 X2-20 X2-21 X2-22 X2-23 X2-24 X2-25 X2-26 X2-27 X2-28

DIB_SCL
DIB_CSA
DIB_SCLK
DIB_MOSI
DIB_A0
DIB_A1
+5V
GND
C3
EEEFK1V470P
GND

DIB_NRESET
DIB_SYNC
DIB_SDA
DIB_IRQ
DIB_CSB
DIB_MISO
DIB_A2
+5V
UART_RX
GND

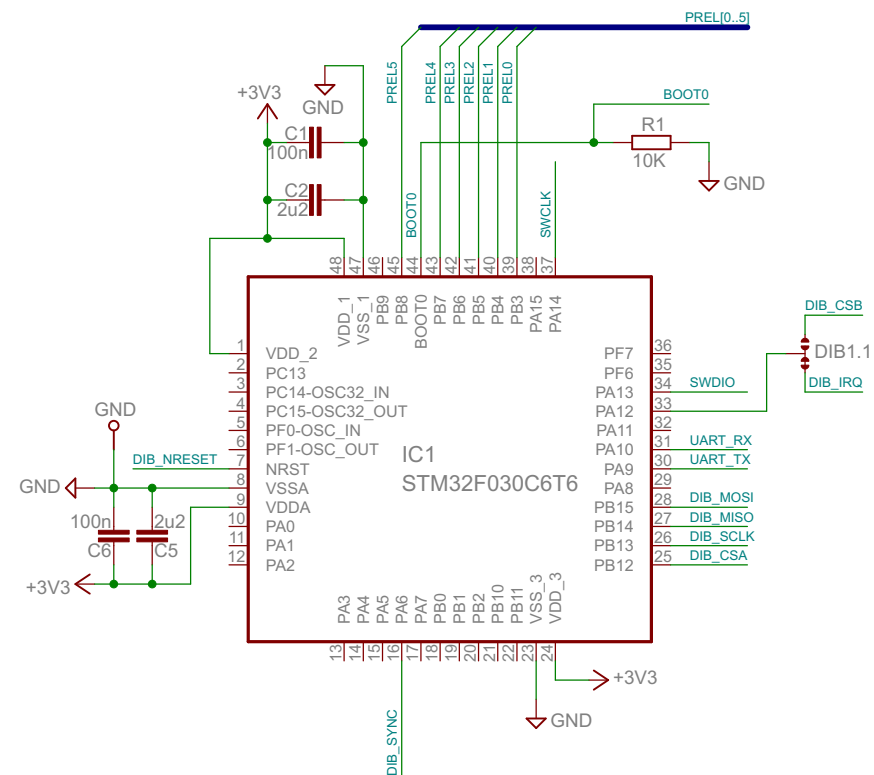
IC2A
74HC1G126GW,125
4
2
3V3
UART_TX

IC2B
DR1206-10K-4/8
3V3
GND

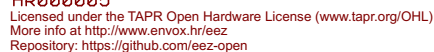
Module Pin	Function
4	NRESET
5	0E_SYNC
6	DIB_SDA
7	Gnd
8	DIB_CSA
9	DIB_SCLK
10	DIB_IRQ
11	DIB_CSB
12	DIB_MISO
13	DIB_MOSI
14	Gnd
15	DIB_A0
16	DIB_A1
17	DIB_A2
18	Gnd
19	+12V
20	+5V
21	+5V
22	Gnd
23	BOOT0
24	UART_RX
25	UART_TX

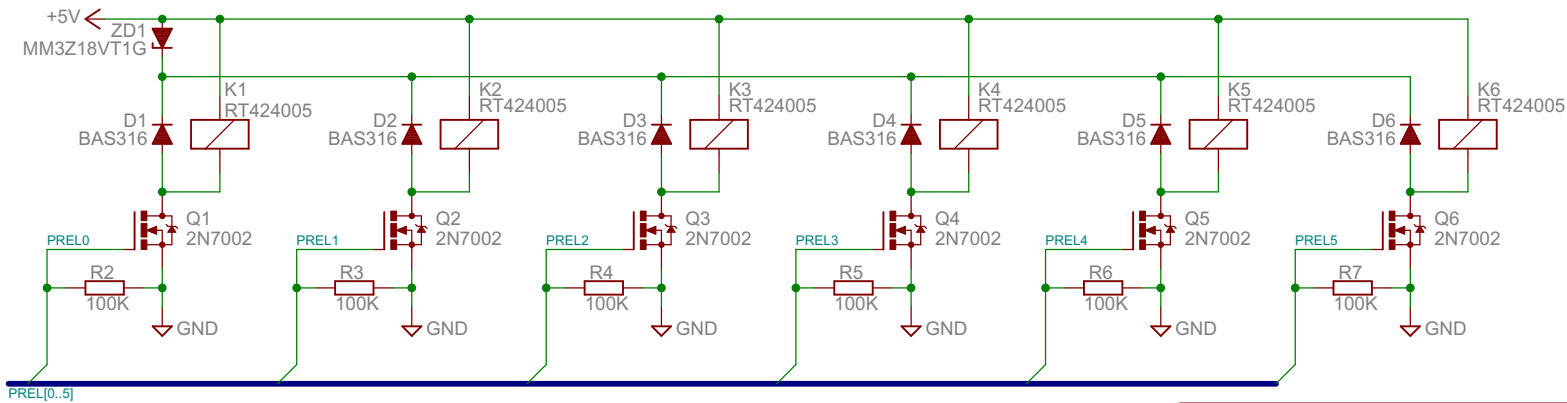
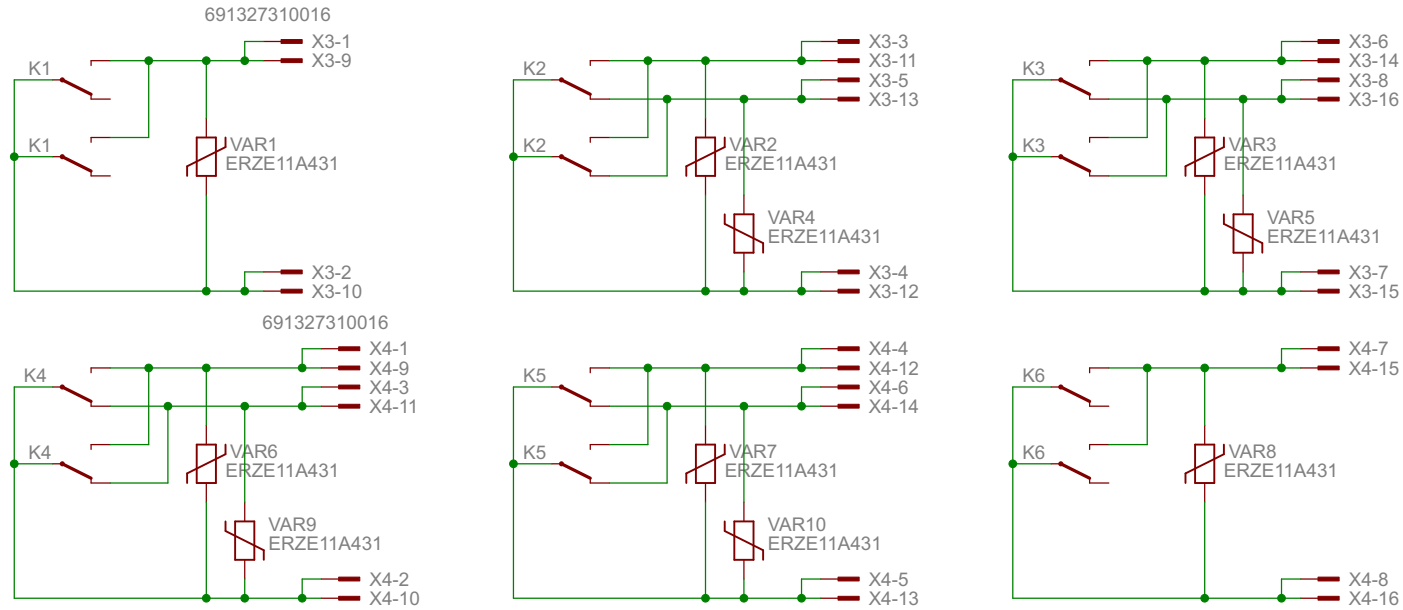
+3V3 1	□	□	2 +Vaux
NAULT3 2	□	□	4 NRESET
DIB_SCL5 3	□	□	6 OE_SYNC
Gnd 7	□	□	8 DIB_SDA
DIB_CSA9 4	□	□	10 DIB_IRQ
Gnd 11	□	□	12 DIB_CSB
DIB_SCLK13 5	□	□	14 DIB_MISO
DIB_MOSI15 6	□	□	16 Gnd
DIB_A017 7	□	□	18 DIB_A2
DIB_A119 8	□	□	20 Gnd
+12V 21	□	□	22 +12V
+5V 23	□	□	24 +5V
Gnd 25	□	□	26 BOOT0
UART_RX27 9	□	□	28 UART_TX

Diagram showing the connection of SWCLK and SWDIO signals to the X1-2, X1-4, and X1-3 pins. SWCLK is connected to X1-2, SWDIO is connected to X1-4, and GND is connected to X1-3.

KK1
PCB-MBKK2
PCB-MB

Sheet: 1/2





Power relays

TITLE: EEZ DIB PREL6 r1B4

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