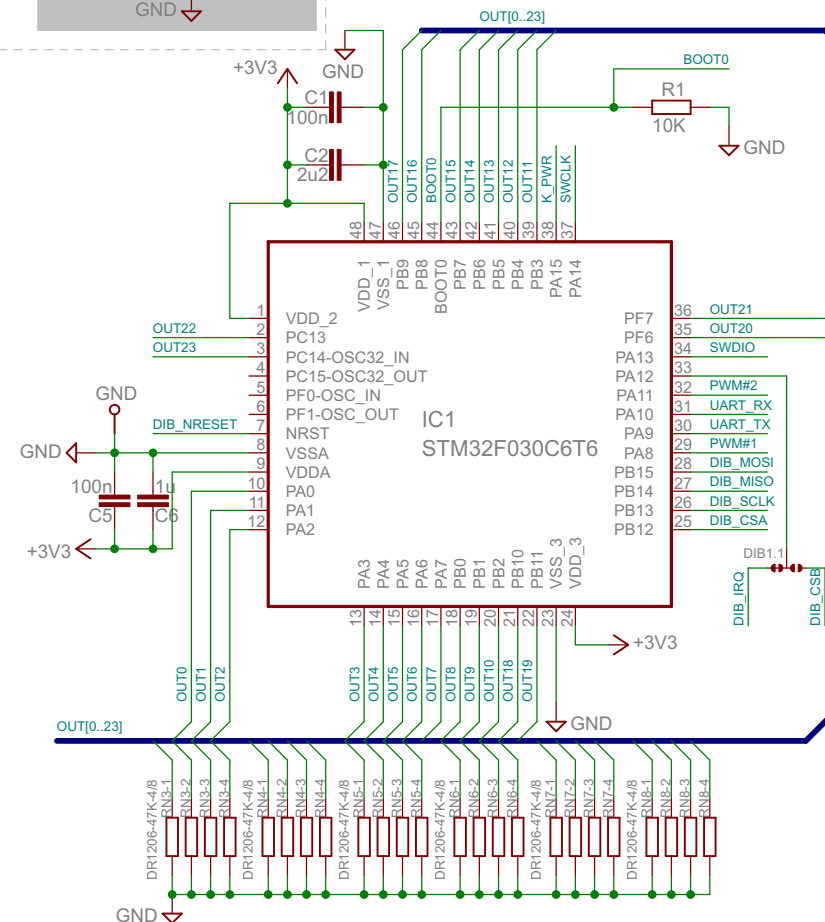


The schematic shows the wiring for the 7731C-104-28LF connector. It includes two power sections: one for +5V and +12V inputs connected to pins X2-25, X2-26, X2-27, and X2-28, and another for +3V3 output from IC2B connected to pins X2-19, X2-20, X2-21, and X2-22. A signal section connects various DIB_* signals to pins X2-1 through X2-18. Specifically, DIB_SCL goes to X2-1, DIB_NRESET to X2-2, DIB_SDA to X2-6, DIB_IRQ to X2-8, DIB_CSB to X2-10, DIB_MISO to X2-12, DIB_A2 to X2-16, and UART_RX to X2-24. The UART_TX signal is connected to pin X2-18 via IC2A (74HC1G126GW, 125) configured as a buffer. Other components include capacitors C3 (EEEFK1V470P) and C4 (100nF), and resistors RN1-4, RN1-1, RN1-2, and RN2-1.

+3V3 1	□	□	2 +Vaux
NAULT3 2	□	□	4 NRESET
DIB_SCL5 3	□	□	6 OE_SYNC
Gnd 7	□	□	8 DIB_SDA
DIB_CSA9 4	□	□	10 DIB_IRQ
Gnd 11	□	□	12 DIB_CSB
DIB_SCLK13 5	□	□	14 DIB_MISO
DIB_MOSI15 6	□	□	16 Gnd
DIB_A017 7	□	□	18 DIB_A2
DIB_A119 8	□	□	20 Gnd
+12V 21	□	□	22 +12V
+5V 23	□	□	24 +5V
Gnd 25	□	□	26 Gnd
UART_RX27 9	□	□	28 UART_TX

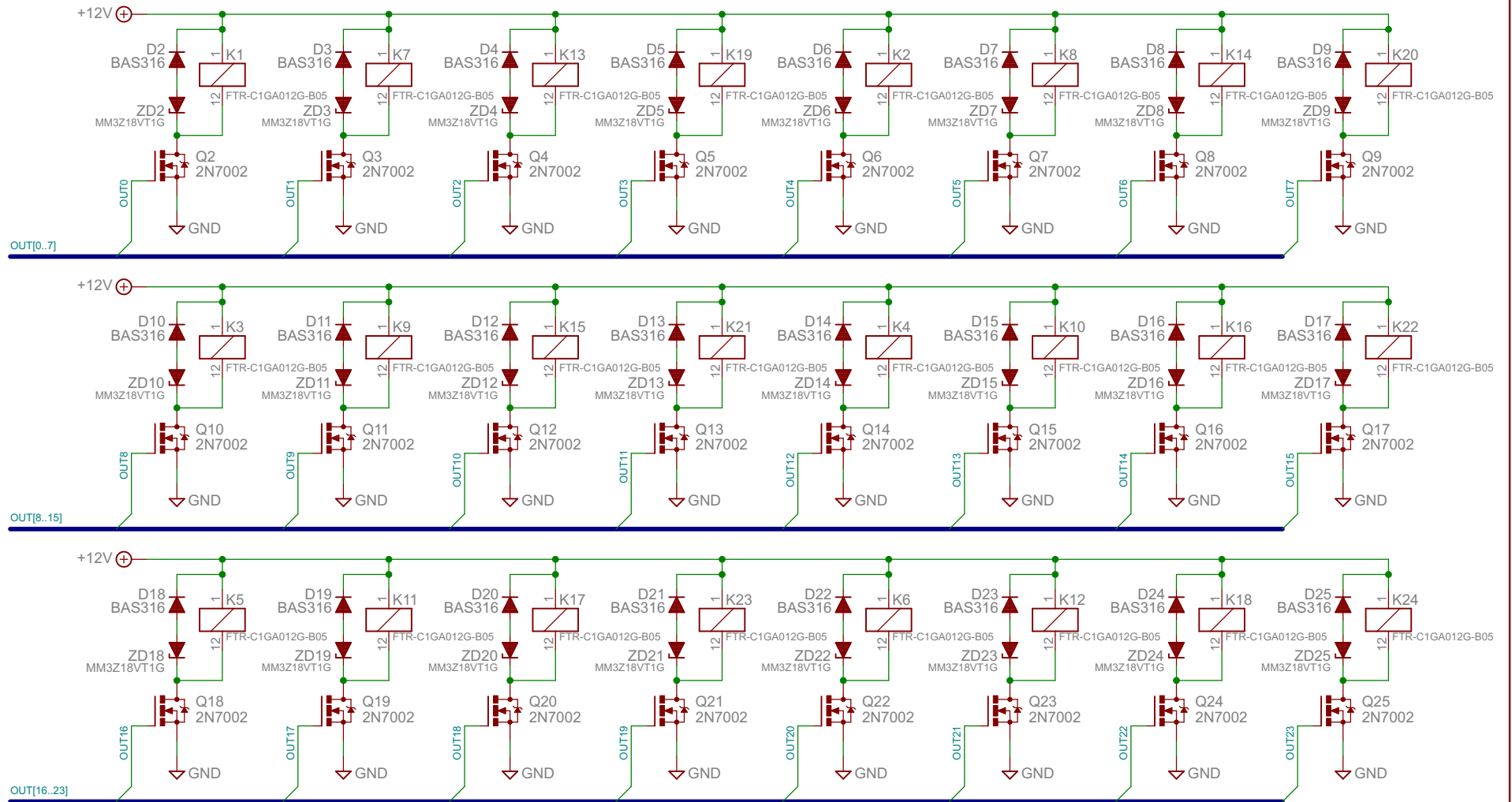
SWCLK
SWDIO
GND

X1-2
X1-4
X1-3

KK1
PCB-MB

KK2
PCB-MB

Sheet: 1/3



Switch matrix relays control

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Sheet: 3/3