1. Description

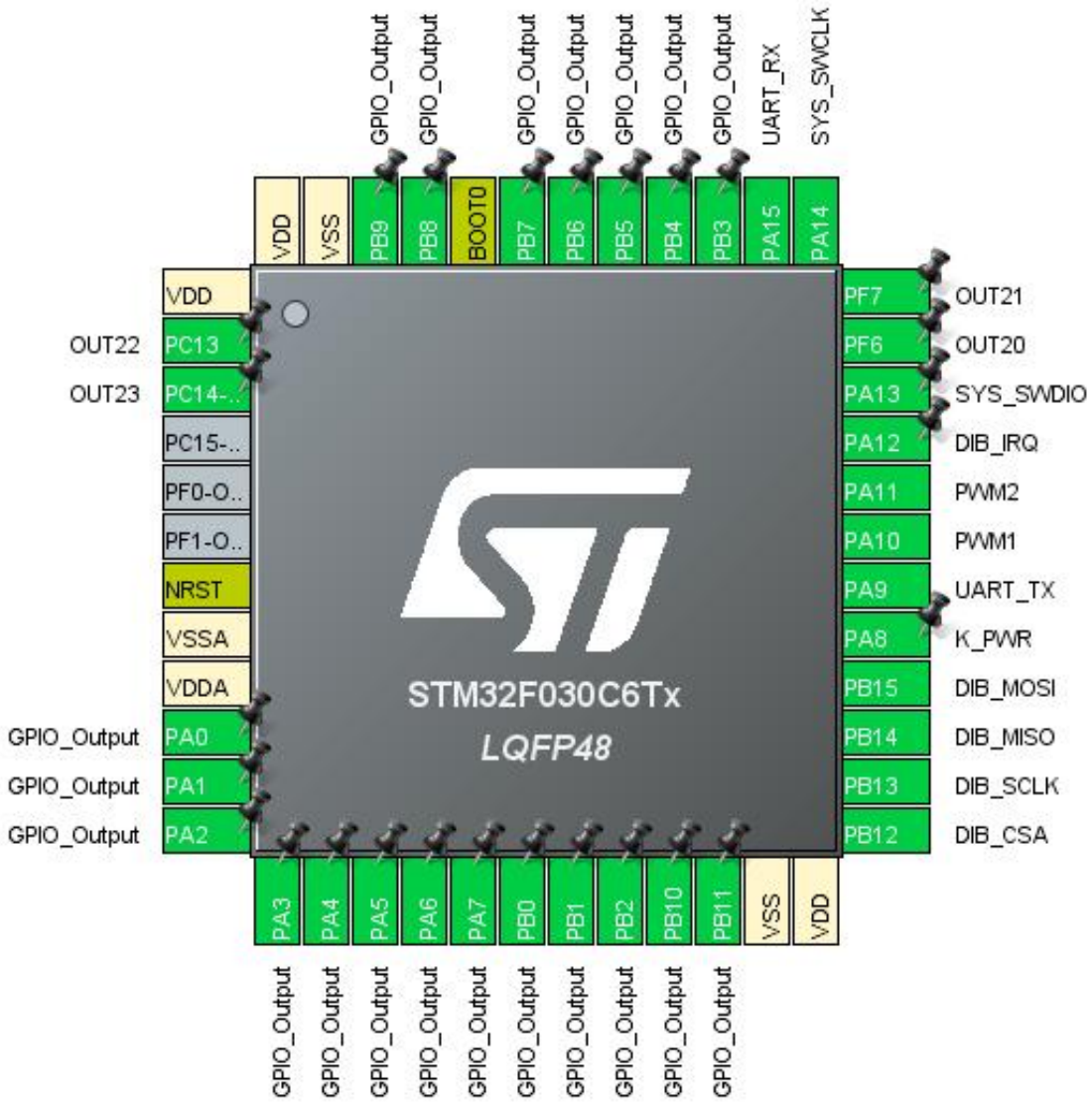
1.1. Project

Project Name	SMX46
Board Name	custom
Generated with:	STM32CubeMX 5.5.0
Date	02/15/2020

1.2. MCU

MCU Series	STM32F0
MCU Line	STM32F0x0 Value Line
MCU name	STM32F030C6Tx
MCU Package	LQFP48
MCU Pin number	48

2. Pinout Configuration



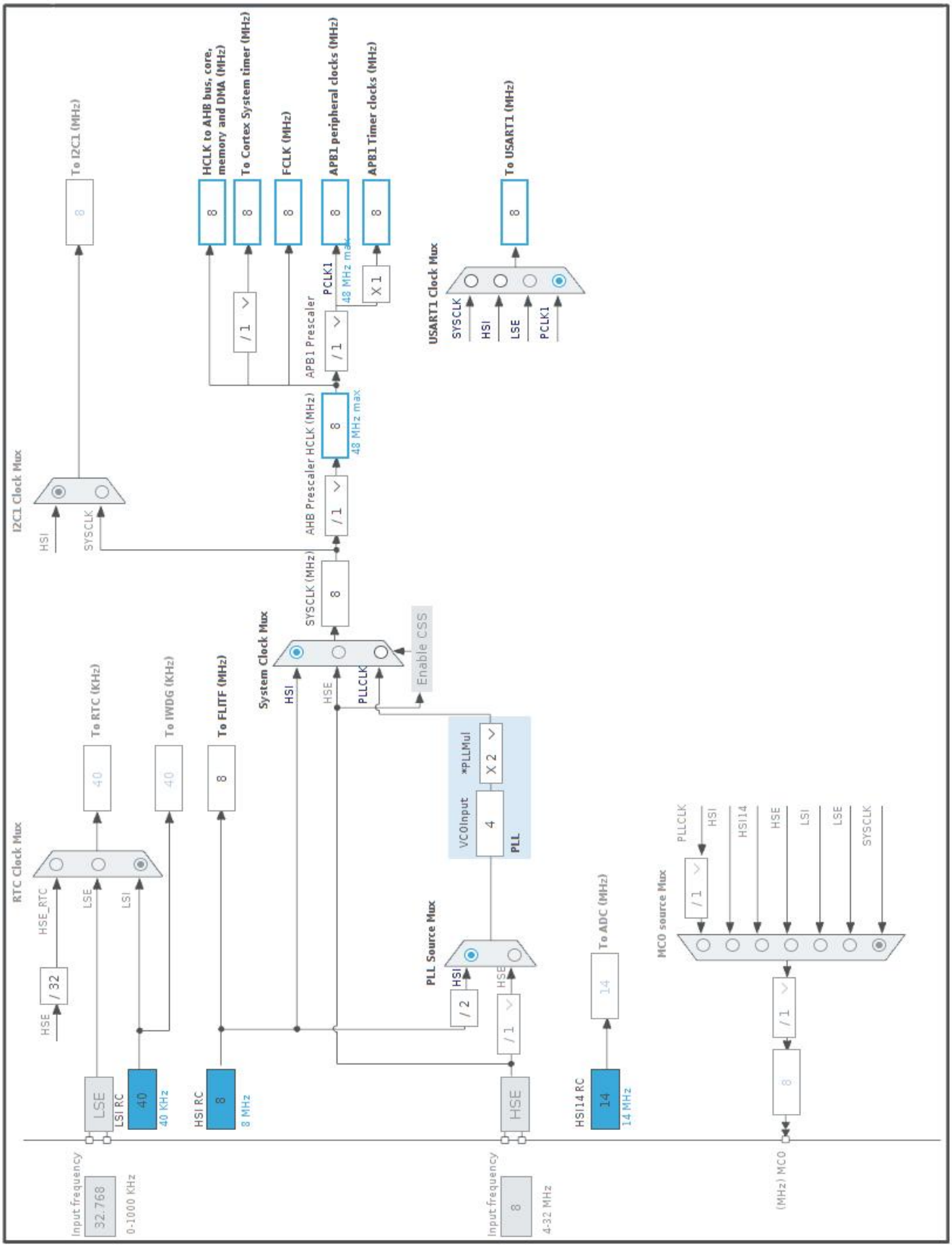
3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VDD	Power		
2	PC13 *	I/O	GPIO_Output	OUT22
3	PC14-OSC32_IN *	I/O	GPIO_Output	OUT23
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
10	PA0 *	I/O	GPIO_Output	
11	PA1 *	I/O	GPIO_Output	
12	PA2 *	I/O	GPIO_Output	
13	PA3 *	I/O	GPIO_Output	
14	PA4 *	I/O	GPIO_Output	
15	PA5 *	I/O	GPIO_Output	
16	PA6 *	I/O	GPIO_Output	
17	PA7 *	I/O	GPIO_Output	
18	PB0 *	I/O	GPIO_Output	
19	PB1 *	I/O	GPIO_Output	
20	PB2 *	I/O	GPIO_Output	
21	PB10 *	I/O	GPIO_Output	
22	PB11 *	I/O	GPIO_Output	
23	VSS	Power		
24	VDD	Power		
25	PB12	I/O	SPI1_NSS	DIB_CSA
26	PB13	I/O	SPI1_SCK	DIB_SCLK
27	PB14	I/O	SPI1_MISO	DIB_MISO
28	PB15	I/O	SPI1_MOSI	DIB_MOSI
29	PA8 *	I/O	GPIO_Output	K_PWR
30	PA9	I/O	USART1_TX	UART_TX
31	PA10	I/O	TIM1_CH3	PWM1
32	PA11	I/O	TIM1_CH4	PWM2
33	PA12 *	I/O	GPIO_Output	DIB_IRQ
34	PA13	I/O	SYS_SWDIO	
35	PF6 *	I/O	GPIO_Output	OUT20
36	PF7 *	I/O	GPIO_Output	OUT21
37	PA14	I/O	SYS_SWCLK	
38	PA15	I/O	USART1_RX	UART_RX
39	PB3 *	I/O	GPIO_Output	

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
40	PB4 *	I/O	GPIO_Output	
41	PB5 *	I/O	GPIO_Output	
42	PB6 *	I/O	GPIO_Output	
43	PB7 *	I/O	GPIO_Output	
44	BOOT0	Boot		
45	PB8 *	I/O	GPIO_Output	
46	PB9 *	I/O	GPIO_Output	
47	VSS	Power		
48	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value
Project Name	SMX46
Project Folder	/home/denis/BACKUP/EEZ/Digital control/MCU/STM32/Projects/SMX46
Toolchain / IDE	EWARM V8.32
Firmware Package Name and Version	STM32Cube FW_F0 V1.11.0

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32F0
Line	STM32F0x0 Value Line
MCU	STM32F030C6Tx
Datasheet	024849_Rev2

6.2. Parameter Selection

Temperature	25
Vdd	3.6

7. IPs and Middleware Configuration

7.1. CRC

mode: Activated

7.1.1. Parameter Settings:

Basic Parameters:

Default Polynomial State	Enable
Default Init Value State	Enable

Advanced Parameters:

Input Data Inversion Mode	None
Output Data Inversion Mode	Disable
Input Data Format	Bytes

7.2. GPIO

7.3. SPI1

Mode: Full-Duplex Slave

Hardware NSS Signal: Hardware NSS Input Signal

7.3.1. Parameter Settings:

Basic Parameters:

Frame Format	Motorola
Data Size	4 Bits
First Bit	MSB First

Clock Parameters:

Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

Advanced Parameters:

CRC Calculation	Disabled
NSS Signal Type	Input Hardware

7.4. SYS

mode: Debug Serial Wire

Timebase Source: SysTick

7.5. TIM1

Channel3: PWM Generation CH3

Channel4: PWM Generation CH4

7.5.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	0
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	0
Internal Clock Division (CKD)	No Division
Repetition Counter (RCR - 8 bits value)	0
auto-reload preload	Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

Break And Dead Time management - BRK Configuration:

BRK State	Disable
BRK Polarity	High

Break And Dead Time management - Output Configuration:

Automatic Output State	Disable
Off State Selection for Run Mode (OSSR)	Disable
Off State Selection for Idle Mode (OSSI)	Disable
Lock Configuration	Off

PWM Generation Channel 3:

Mode	PWM mode 1
Pulse (16 bits value)	0
Output compare preload	Enable
Fast Mode	Disable
CH Polarity	High
CH Idle State	Reset

PWM Generation Channel 4:

Mode	PWM mode 1
Pulse (16 bits value)	0
Output compare preload	Enable
Fast Mode	Disable
CH Polarity	High
CH Idle State	Reset

7.6. USART1

Mode: Asynchronous

7.6.1. Parameter Settings:

Basic Parameters:

Baud Rate	38400
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples
Single Sample	Disable

Advanced Features:

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

*** User modified value**

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
SPI1	PB12	SPI1_NSS	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	DIB_CSA
	PB13	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	DIB_SCLK
	PB14	SPI1_MISO	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	DIB_MISO
	PB15	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	DIB_MOSI
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	
	PA14	SYS_SWCLK	n/a	n/a	n/a	
TIM1	PA10	TIM1_CH3	Alternate Function Push Pull	No pull-up and no pull-down	Low	PWM1
	PA11	TIM1_CH4	Alternate Function Push Pull	No pull-up and no pull-down	Low	PWM2
USART1	PA9	USART1_TX	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	UART_TX
	PA15	USART1_RX	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	UART_RX
GPIO	PC13	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	OUT22
	PC14-OSC32_IN	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	OUT23
	PA0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB10	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB11	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	K_PWR
	PA12	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DIB_IRQ
	PF6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	OUT20
	PF7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	OUT21
	PB3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
	PB8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB9	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	

8.2. DMA configuration

DMA request	Stream	Direction	Priority
SPI1_RX	DMA1_Channel2	Peripheral To Memory	Low
SPI1_TX	DMA1_Channel3	Memory To Peripheral	Low

SPI1_RX: DMA1_Channel2 DMA request Settings:

Mode:	Normal
Peripheral Increment:	Disable
Memory Increment:	**Enable ***
Peripheral Data Width:	Byte
Memory Data Width:	Byte

SPI1_TX: DMA1_Channel3 DMA request Settings:

Mode:	Normal
Peripheral Increment:	Disable
Memory Increment:	**Enable ***
Peripheral Data Width:	Byte
Memory Data Width:	Byte

8.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
DMA1 channel 2 and 3 interrupts	true	0	0
Flash global interrupt	unused		
RCC global interrupt	unused		
TIM1 break, update, trigger and commutation interrupts	unused		
TIM1 capture compare interrupt	unused		
SPI1 global interrupt	unused		
USART1 global interrupt	unused		

*** User modified value**

9. Software Pack Report