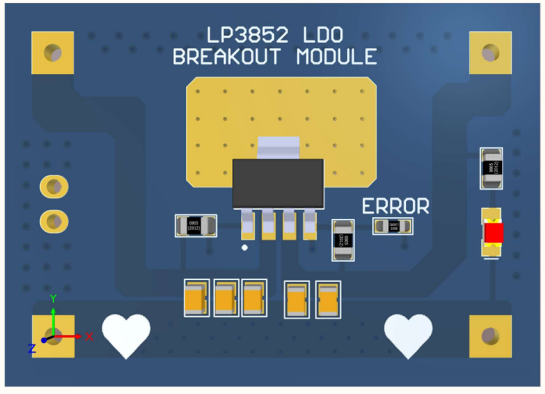
Project

Variant: [No Variations]

06/04/2022
v1.0



Hardware Specs

--> Input Supply Voltage: 2.5 V to 7 V
--> Ultra-Low Dropout Voltage
--> Specified Output Current of 1.5 A DC
--> Overtemperature/Overcurrent Protection
--> Built-in LNA for better sensitivity
--> −40°C to 125°C Junction Temperature Range
--> Output Voltage Accuracy ± 1.5%
--> 10-nA Quiescent Current in Shutdown Mode

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Designed By: Enes Mercan		ApprovedBy:	
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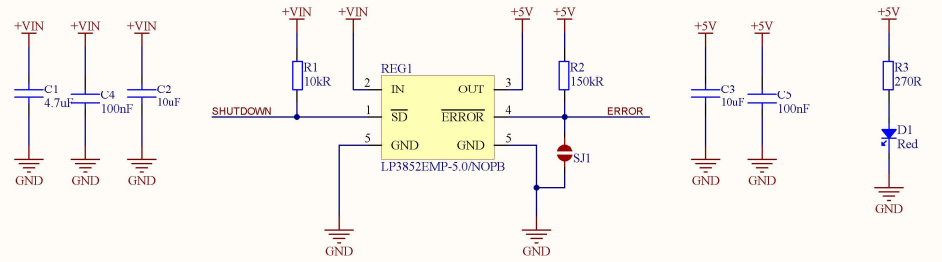
LP3852 LDO REGULATOR



Pin−7.5V (max)

SD and ERROR pins must be pulled high through a 10-kΩ pull-up resistor. Connect the ERROR pin to ground if this function is not used

Logic low level signal at the SD pin will turn off the regulator. SD must be actively terminated through a 10-kΩ pullup resistor for a proper operation.

LP3852 produce a logic low signal at the ERROR pin when the output drops out of regulation due to low input voltage, current limiting, or thermal limiting.

You can use SHUTDOWN pin to shutdown the regulator by giving low signal. Do not pull the ERROR line high since you may damage the pin.

RθJa=66°C/W
Vd=320mV (max) @1.5A
Junction temperature = 125°C (max)

If the ERROR function is not used, remove pull-up resistor and solder the SJ2 pads. If used, solder RX and remove SJ1

Solder the regulator first

Solder all the filtering caps C1, C2, C3, C4

If ERROR function is used, solder RX, leave SJ1 unsoldered

If ERROR function is not used, solder SJ2 and remove RX

SOLDER R1

NOTES FOR USER/CUSTOMER

DESIGN RECOMMENDATIONS

CRITICAL NOTES AND WARNINGS

ASSEMBLY NOTES

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