

# LATCHING SOLENOID DRIVERS WITH COMMON HALF BRIDGE

A

B

C


D

E

A0 A1 A2 E3 E2 ET : DESC  
X X X 0V X X : ALL DISABLED  
X X X X 5V X : ALL DISABLED  
X X X X X 5V : ALL DISABLED  
X X X 5V X X : ENABLE 17mA  
5V 0V 0V 5V 0V 0V : BOOST  
0V 5V 0V 5V 0V 0V : SET K1  
5V 5V 0V 5V 0V 0V : RESET K1  
0V 0V 5V 5V 0V 0V : SET K2  
5V 0V 5V 5V 0V 0V : RESET K2  
0V 5V 5V 5V 0V 0V : SET K3  
5V 5V 5V 5V 0V 0V : RESET K3

Reference Designators:  
B Battery  
C Capacitor  
D Diode  
F Fuse/Breaker  
H Hardware & Outline  
J Connector/Link  
K Relay  
L Inductor  
M Motor (and Related)  
P Power supply  
Q Transistor  
R Resistor  
S Switch  
T Transformer  
TP Test point  
U Integrated circuit/module  
Y Crystal or oscillator

## SMPS: BOOST CONVERTER

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|--|--|---|--|--|--|---|
| <b>RPU BUS</b>   |  | RPUBUS.ORG<br>GITHUB.COM/EPCCS  |  |  Copyright (C) 2017 Ronald Sutherland<br>Released under the Creative Commons<br>Attribution-ShareAlike 4.0 International<br>(CC BY-SA 4.0)<br><a href="http://creativecommons.org/licenses/by-sa/4.0/">http://creativecommons.org/licenses/by-sa/4.0/</a> |  | SIZE<br><b>A</b>  |
| BK black GN green<br>BN brown BU blue<br>RD red VT violet<br>OG orange GY gray<br>YE yellow WH white |  | Symbols = RD<br>Names = BU<br>Values = GY<br>Label/Info = VT<br>Busses = Dark BU<br>Net/Wire = GN<br>Guide = YE |  | DATE: 2/5/17<br>NAME: RON S<br>ADDR: 1627 W INVERNESS DR<br>TEMPE AZ 85282<br>E-MAIL: ronald.sutherland@gmail.com<br>PHONE: (480) 967-2851   |  | DESCRIPTION:<br><b>LATCHING SOLENOID DRIVER 3X</b><br>MODEL: K3<br>DRAWING: 16276<br>2/5/2017 1:28 PM |
| ^2 ENABLE CURR SOURCE<br>NMOS S-D FLIP, RM DISCH<br>SHDWN HACK MOD, 2.54MM<br>HDR, Y0 IS CHRG OUTPUT |  | 2/5/17<br>11/1/16   |  |  |  |   |
| REV BUGZILLA ISSUE #   |  | JDATE 1/1   |  |  |  |   |
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