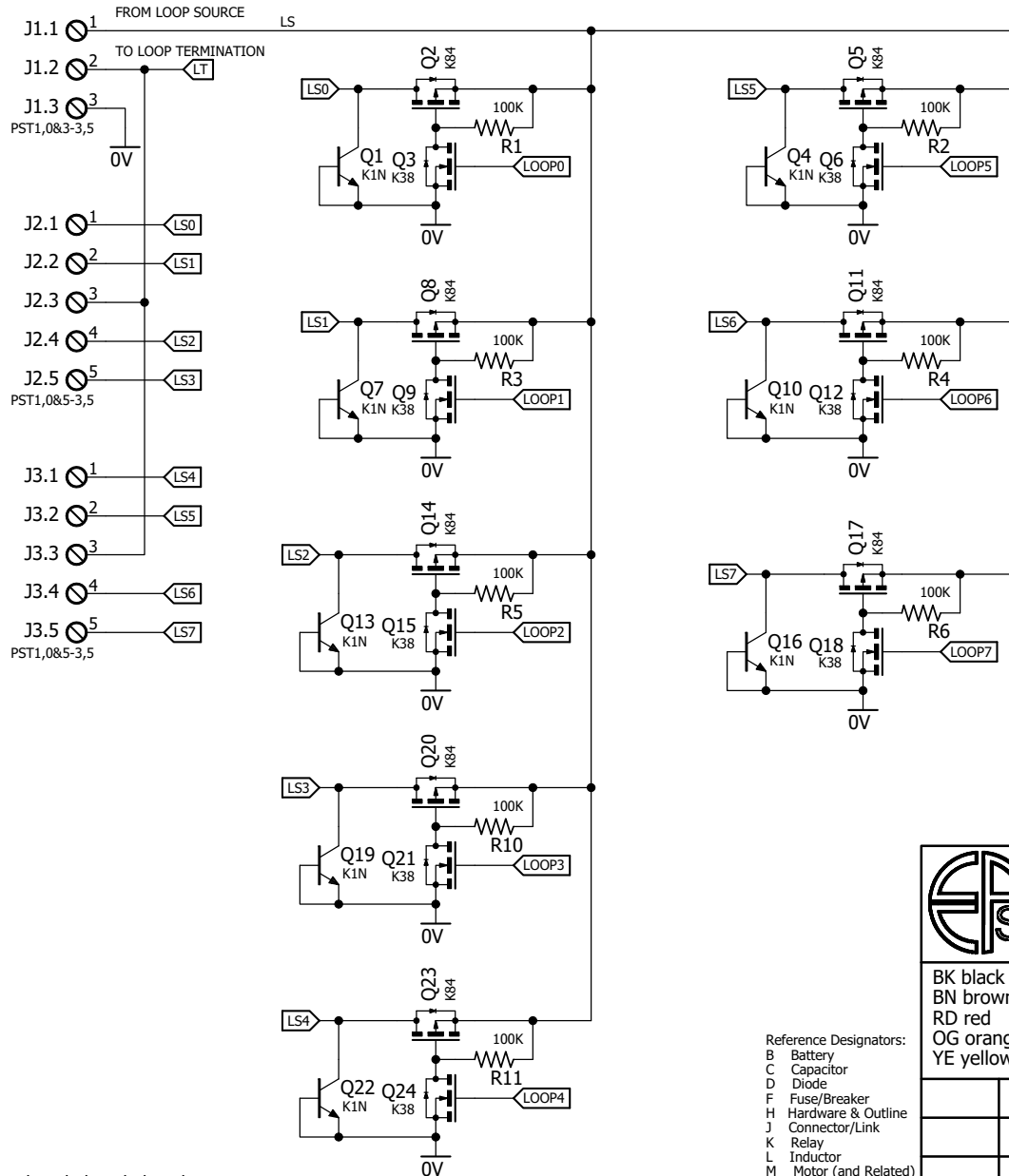


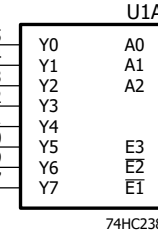
SELECT 1 OF 8 CURR LOOPS



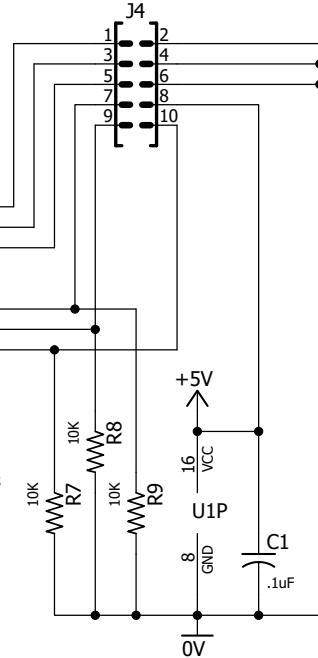
A0 A1 A2 En \overline{EN} : DESC
X X X 0V X : ALL DISABLED
X X X X 5V : ALL DISABLED
0V 0V 0V 5V 0V : LOOP0
5V 0V 0V 5V 0V : LOOP1
0V 5V 0V 5V 0V : LOOP2
5V 5V 0V 5V 0V : LOOP3
0V 0V 5V 5V 0V : LOOP4
5V 0V 5V 5V 0V : LOOP5
0V 5V 5V 5V 0V : LOOP6
5V 5V 5V 5V 0V : LOOP7

HI WHEN ADDRESSED

LOOP0 15
LOOP1 14
LOOP2 13
LOOP3 12
LOOP4 11
LOOP5 10
LOOP6 9
LOOP7 7



DEFAULT IS
DISABLED
PULL UP E3
TO ENABLE



Reference Designators:
B Battery
C Capacitor
D Diode
F Fuse/Breaker
H Hardware & Outline
J Connector/Link
K Relay
L Inductor
M Motor (and Related)
P Power supply
Q Transistor
R Resistor
S Switch
T Transformer
TP Test point
U Integrated circuit/module
Y Crystal or oscillator



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SIZE

A

BK black
BN brown
RD red
OG orange
YE yellow

GN green
BU blue
VT violet
GY gray
WH white

Symbols = RD
Names = BU
Values = GY
Label/Info = VT
Busses = Dark BU
Net/Wire = GN
Guide = YE

DATE: 10/21/16
NAME: RON S

ADDR:
R. SUTHERLAND
1627 W INVERNESS DR
TEMPE AZ 85282
E-MAIL:
rsutherland at epccs dot com

REV BUGZILLA ISSUE # JDATE 1/1

TO:
FROM:

DESCRIPTION:
SELECT 1 OF 8
CURR LOOPS
MODEL:
CL8
DRAWING:
16296
10/22/2016 9:48 PM

