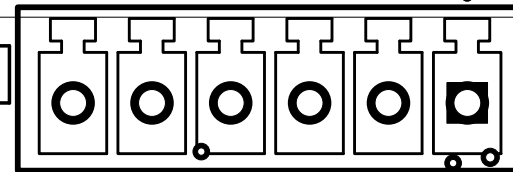


NC 0V ADC7 E3 ADC6 E2 NC NC ADC3 C3 ADC2 C2 ADC1 C1 ADC0 00 J5

J6 0V 0V 5V NC RESET IOREF 5V NC



TP1

0V MOSI SCK IOREF MISO J11 SPI

SHIELD VIN IS CNTRL'D BY D2 (IO2)

R54 R53

POWER MCU AT 13.1V DISCONNECT AT 11.58V

VIN

S1: VIN CONNECT S2: CONNECT BAT TO CC

B=4250 100K +BAT -BAT J7

12V SLA

PWR TP4

13.278V:50.7k at 40°C  
13.63V:100k at 25°C  
14.068V:357k at 0°C

14140~5  
EPCCS.ORG  
RPuno

10k B=3380 NTC  
CHRG IF > 0°C  
CHRG IF < 40°C



MPP IDEAL:ACTUAL  
13.8:14.5V-15.2k at 70°C  
15.8:15.7V-50.7k at 40°C  
17:16.8V-100k at 25°C  
19:18.7V-357k at 0°C

0V (\*E0) 5V (\*E1) D0 RX D1 TX D2 INT0 D3 INT1 D4 T0 D5 OC0B/T1 D6 OC0A D7 J9

B0 ICPL B1 OC1A B2 S9/OC1B B3 MOS/OC2A B4 MISO B5 SCK 0V NC C4 SDA C5 SCL J12

CHARGE RATE IS FUNCTION OF PV POWER UP TO MAX, ABOUT 0.11A PER WATT

36 CELL STRING

B=4250 100K

-PV +PV

ANALOG LOOP ADC0 0V ADC1 12V 22mA

FT/PULSE PL 10mA TO PL J8

DIGITAL IO 3 4 10 11 12 13 J2

