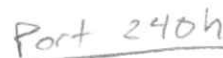


This circuit appears to be incomplete, but u42-5(cs) is high during normal operation. The circuit probably ensures cs is low when the PC is powered-down.

- | | | |
|-----|-----------|---------------------------|
| U20 | 74LS174 | Hex Flip-flop |
| U41 | 74LS02 | Quad NOR |
| U42 | MPD4990AC | RTC |
| U30 | 74LS138 | 3-to-8 demux |
| U31 | 74LS367 | 6-bit 3-state
buffer |
| U26 | PAL | - Address decode |
| U29 | 74LS32 | Quad OR |
| U15 | 74LS00 | Quad NAND |
| U37 | 74LS245 | Data bus
transceiver |
| U35 | 74LS244 | Octal 3-state
buffer |
| U36 | 74LS374 | Octal D-type
flip flop |
| U29 | 74LS32 | |



L Read only

