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STM32 Security Workshop

PSA-TFM presentation

ARM Platform Security Architecture

- PSA : is an ARM initiative which establishes the general method for securing devices from the very start of the product lifecycle.
- This standard pushed covering the entire IoT ecosystem, from chip designers and device developers to cloud and network infrastructure providers and software vendors.



ARM Platform Security Architecture

- In October 2018 ARM announce a PSA compliance program and publish the first version of PSA certification
- The Platform Security Architecture (PSA) is made up of four key stages:



Analyze



Architect



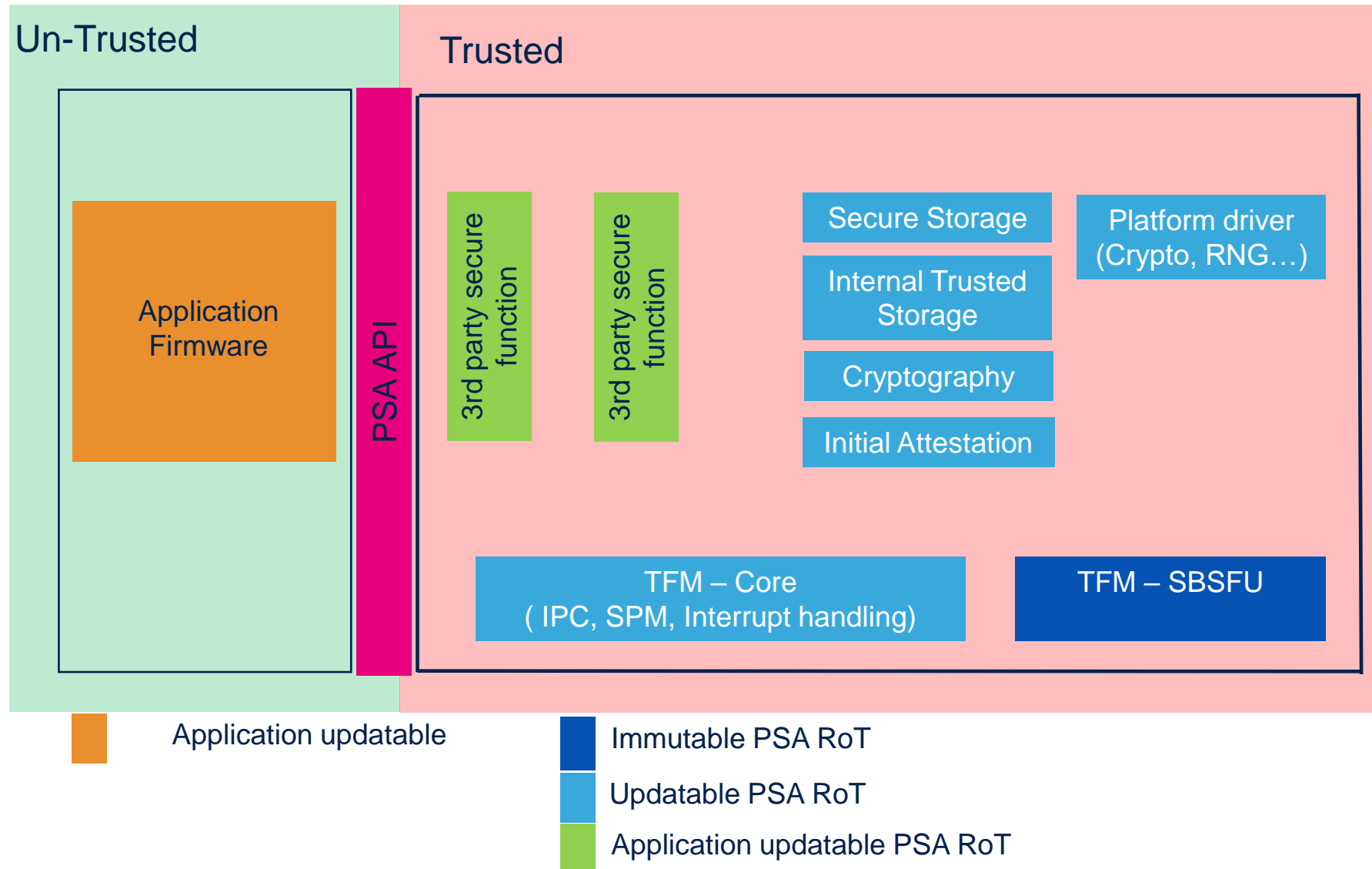
Implement



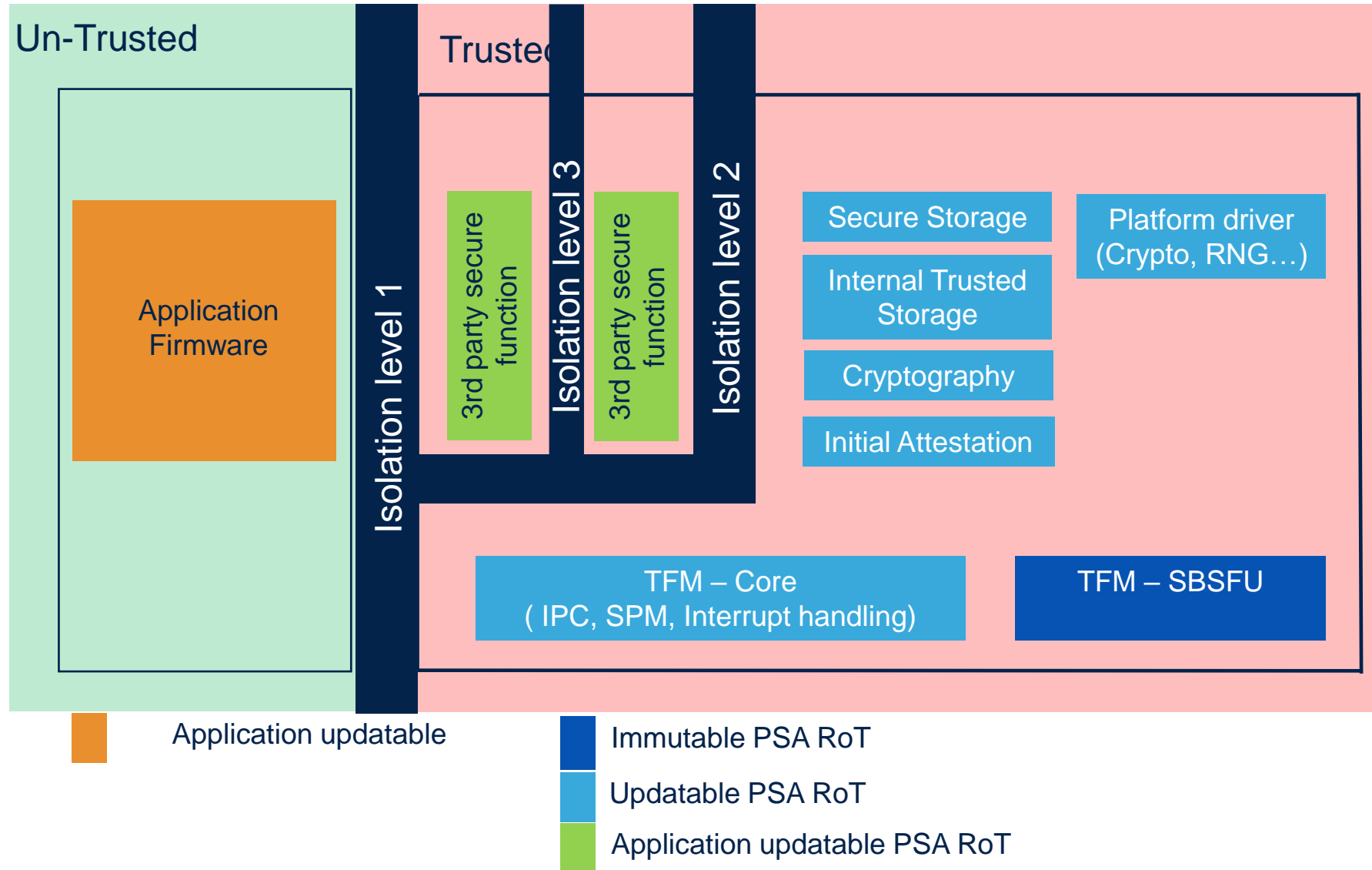
Certify

<https://developer.arm.com/architectures/security-architectures/platform-security-architecture>

TFM on Cortex-M33



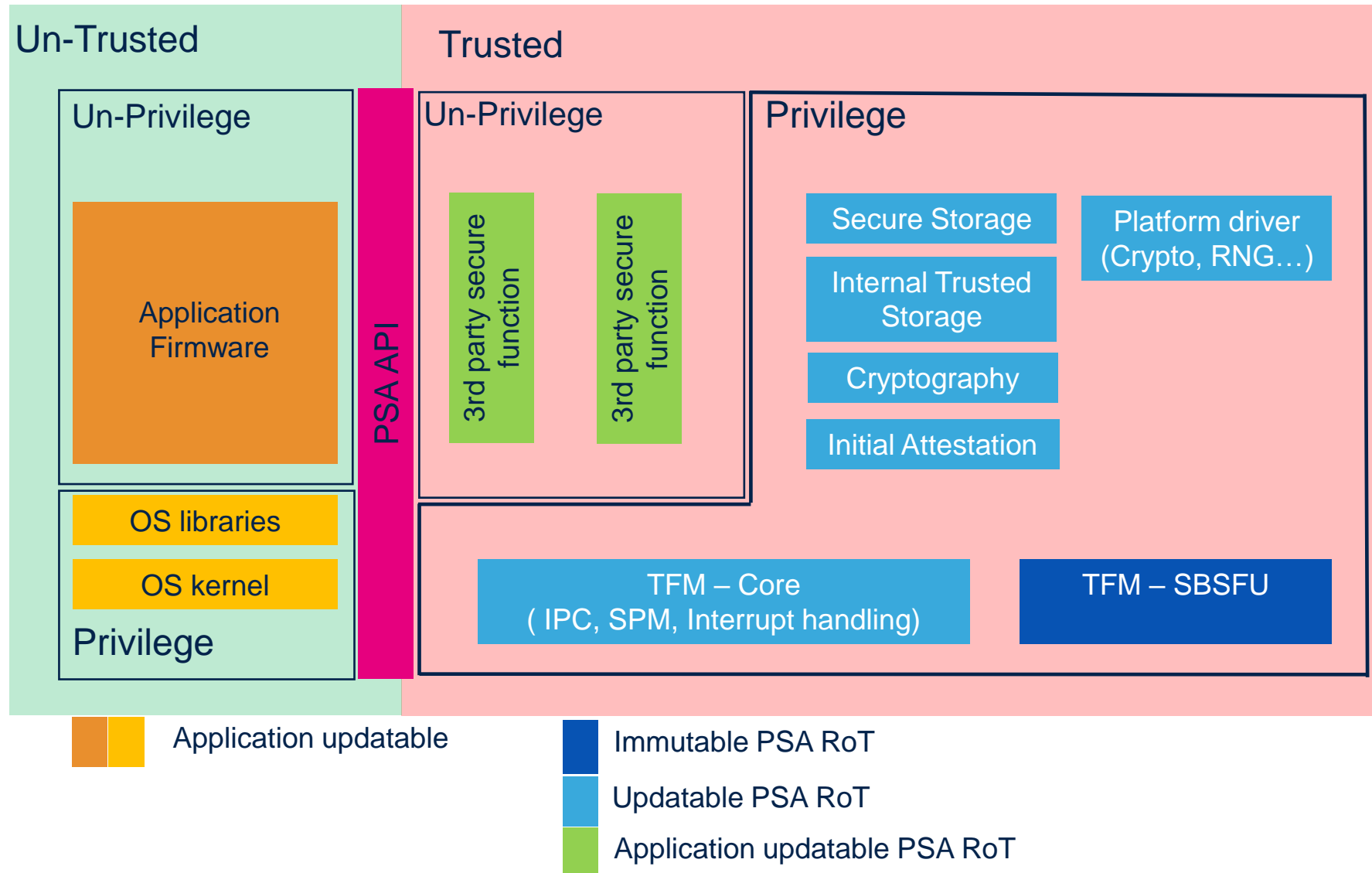
TFM isolation on Cortex-M33



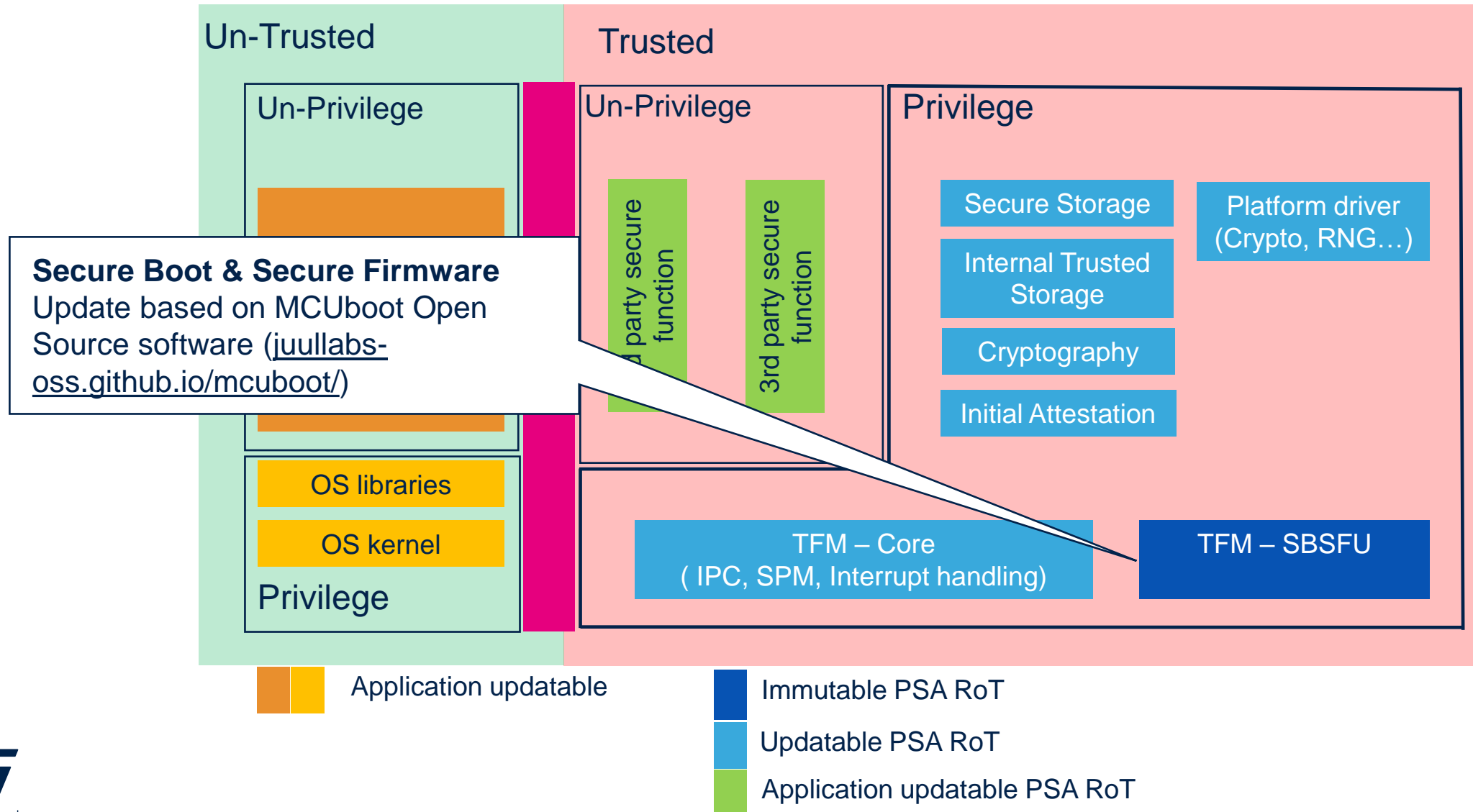
ARM Trusted Firmware-M (TF-M)

- Trusted Firmware-M (TF-M) providing a reference implementation of PSA standard on ARM-CM33
- Current version support PSA Level 1 and 2 isolation on Armv8-M.
- ST ported TF-M code on STM32L5 with isolation level 2

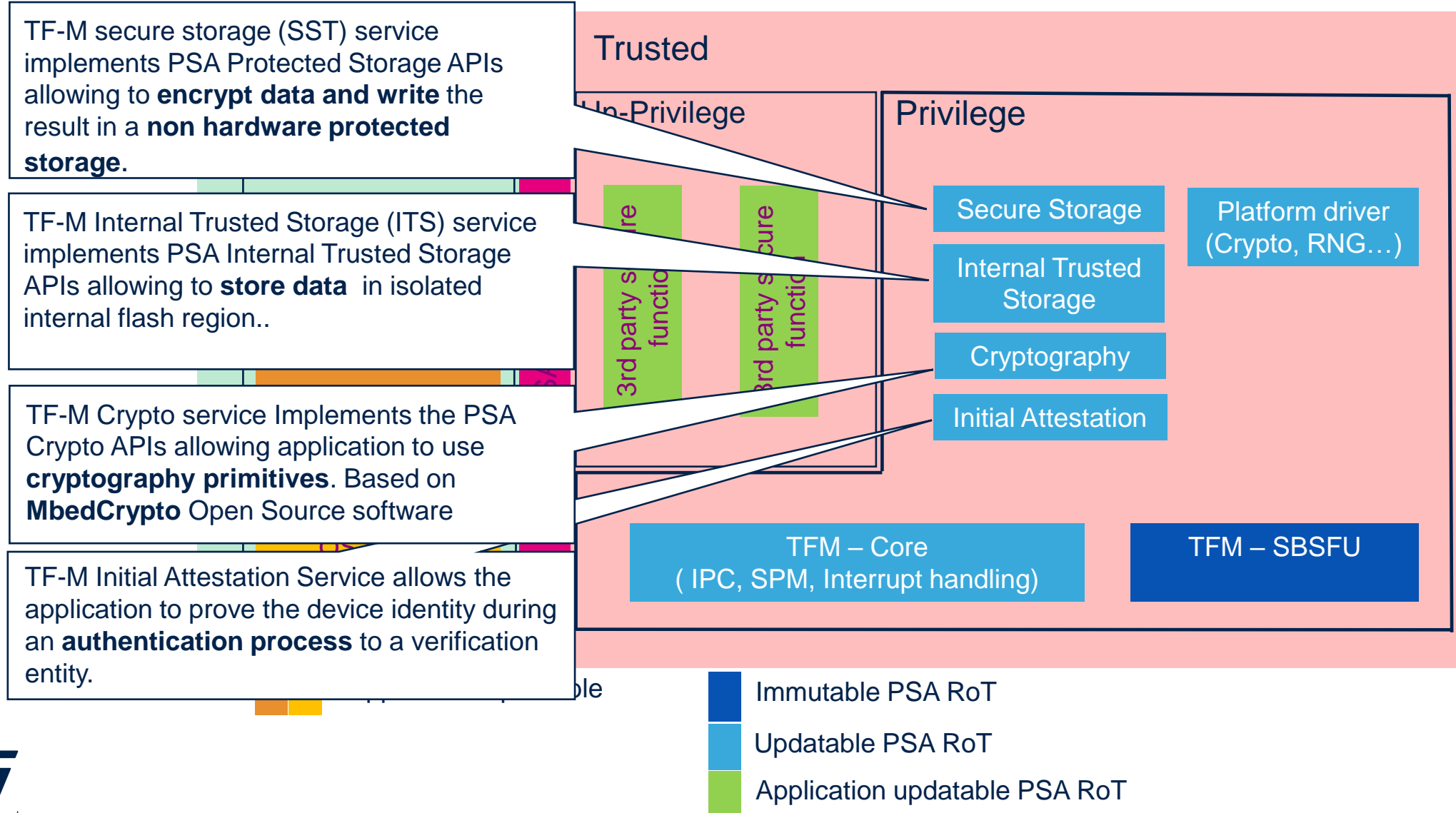
TFM isolation on Cortex-M33



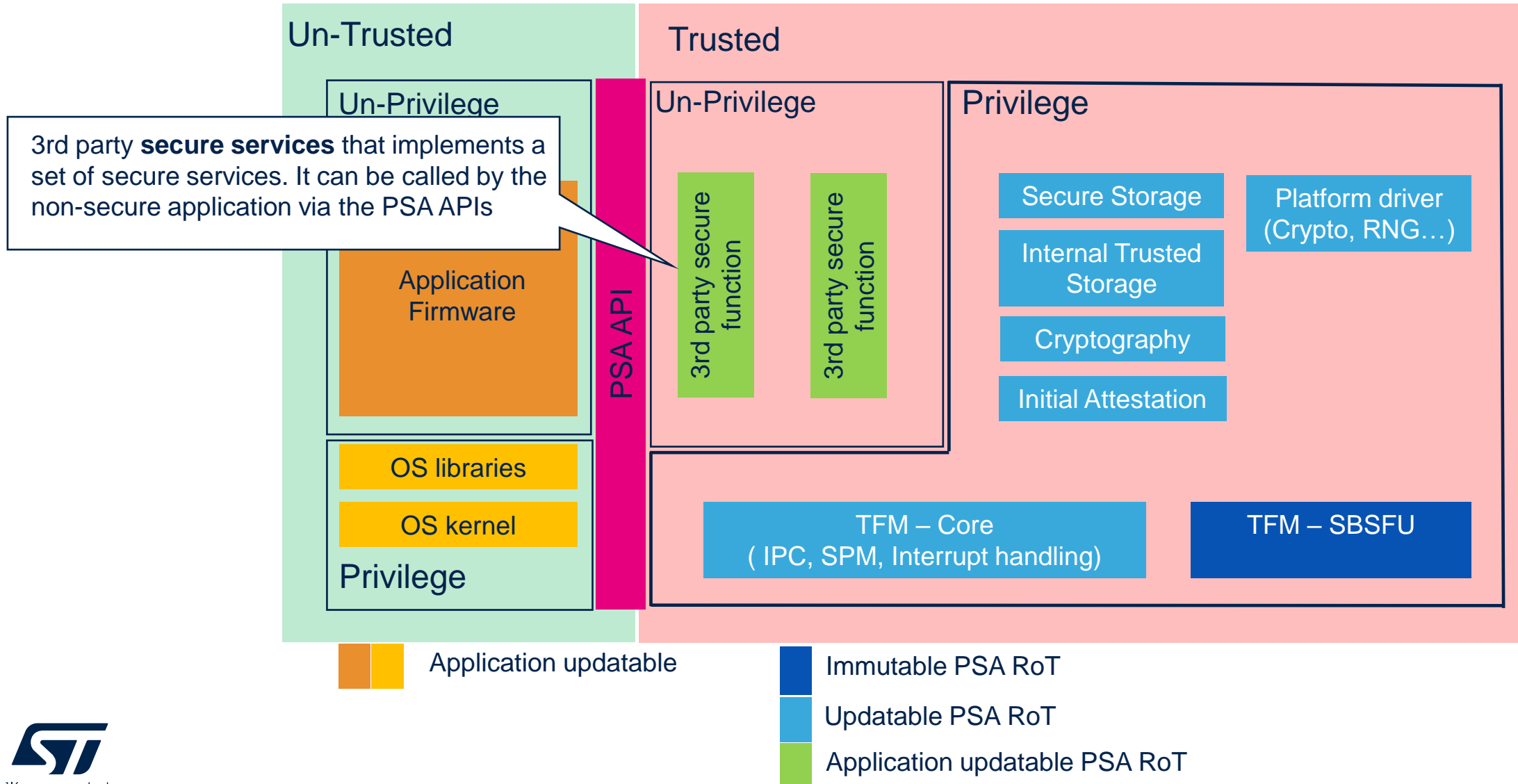
TFM isolation on Cortex-M33



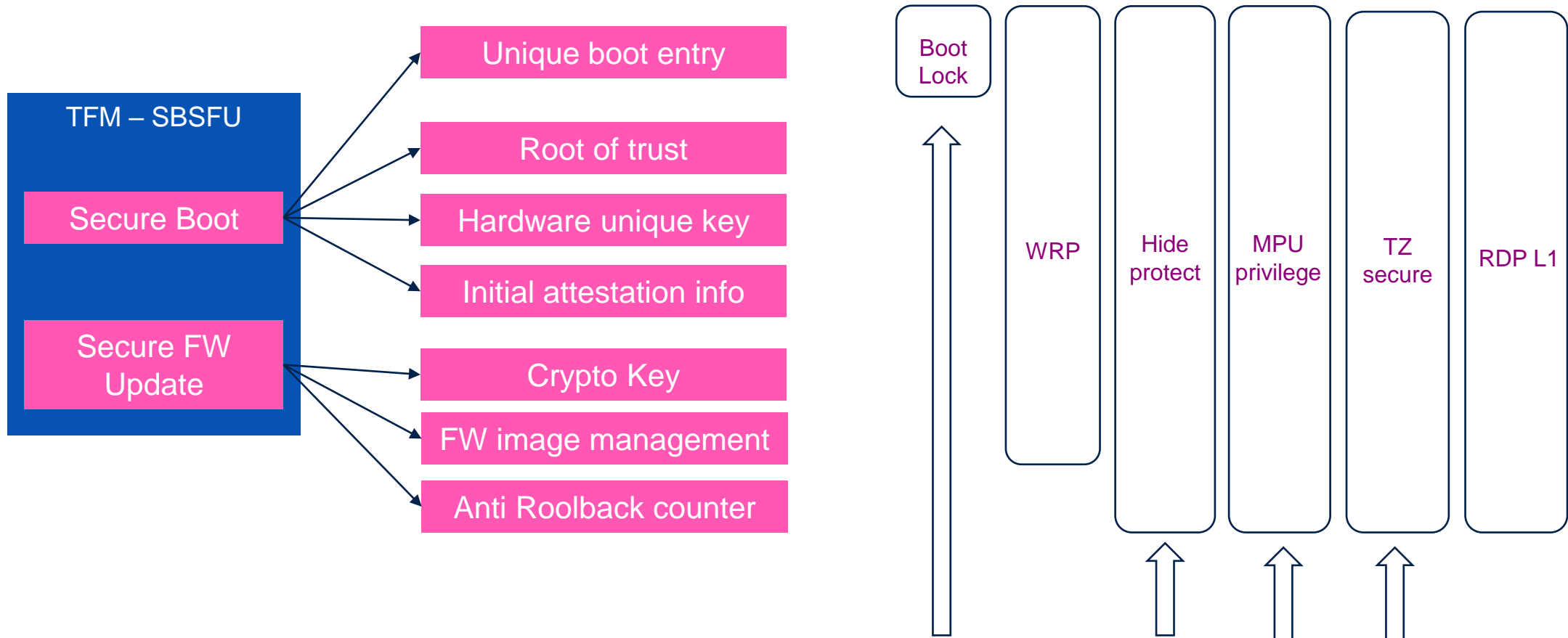
TFM isolation on Cortex-M33



TFM isolation on Cortex-M33

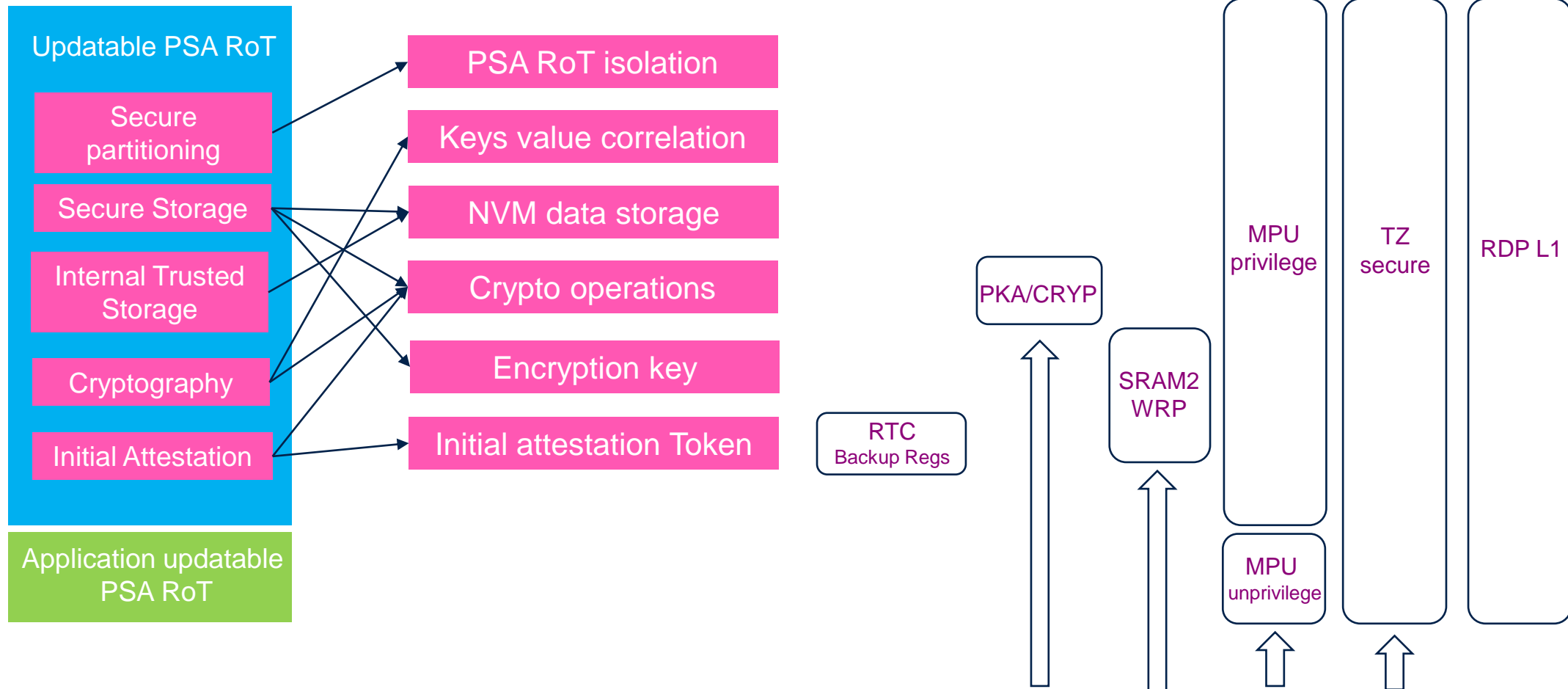


TFM-STM32L5 Protection



L5 improvement (Vs L4)

TFM-STM32L5 Protection



L5 improvement (vs L4)

TFM-STM32L5 delivery feature

- TFM-SBSFU (TFM_SBSFU_Boot)
 - Authentication : RSA 2048, RSA3072 or ECC256
 - SHA256 integrity check
 - Confidentiality : AES CTR with symmetric key encrypted in RSA-OAP or ECIES-P256
 - Dual slot mode
 - Dual image mode (1 image for secure application / 1 image for non secure application)
 - External memory support via OTFDEC (only the secure application primary slot in internal flash)

Default config in the package which could be modified

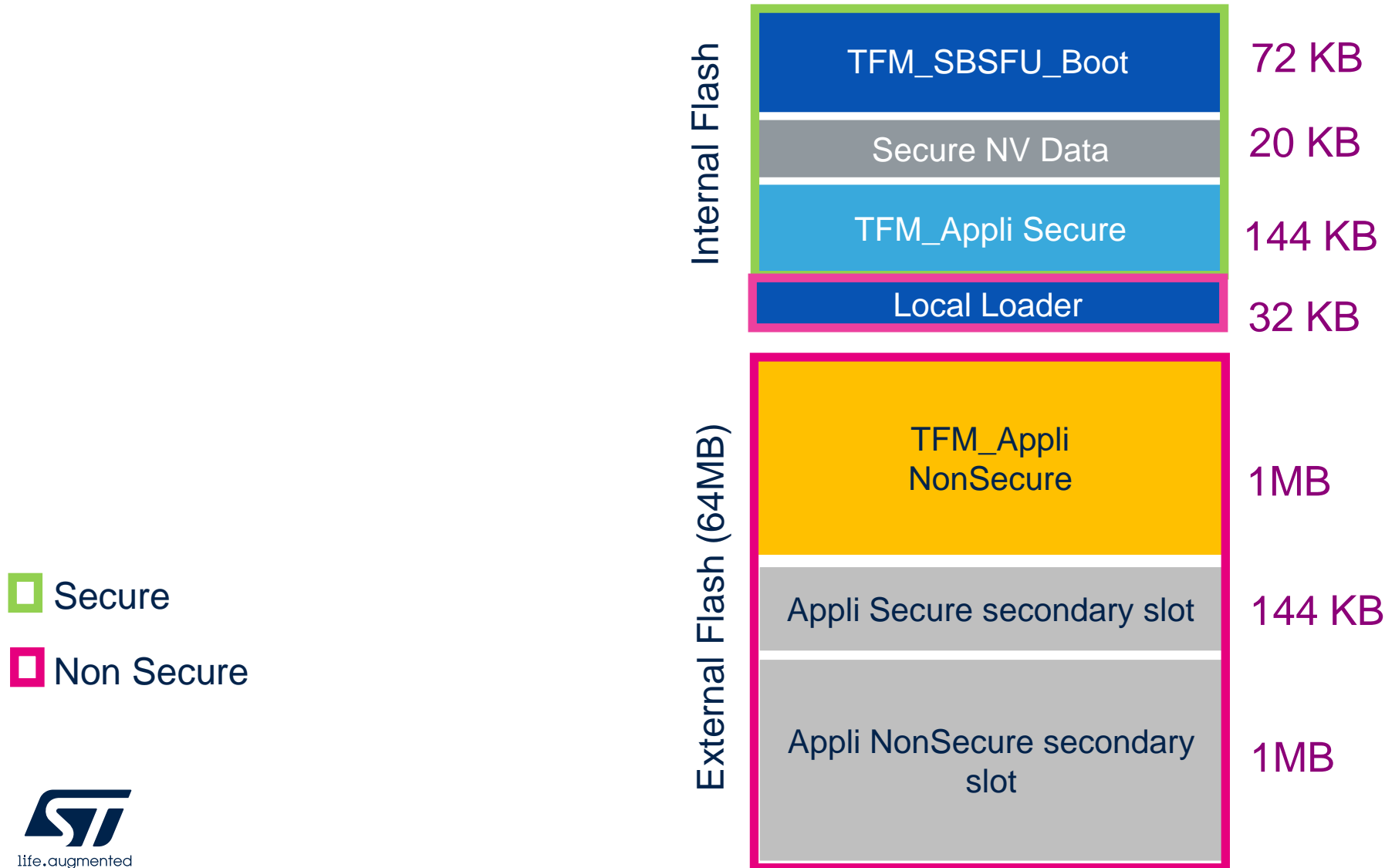
TFM-STM32L5 delivery feature

- TFM secure services (TFM_Appli secure)
 - PSA isolation level 2
 - Cryptography
AES-CBC, AES-CFB, AES-CTR, AES-OFB, AES-CCM, AES-GCM, RSA, ECDSA, ECDH, SHA1, SHA256, SHA512
software crypt or mix of software/hardware
 - Initial attestation
Entity token encoded CBOR (RFC7049)/signature SHA256 and ECDSA
 - Secure Storage
AES GCM based AEAD encryption
 - Internal Trusted Storage
- TFM local loader (TFM_Loader)
 - Non secure application immutable
 - YModem

Default config in the package which
could be modified

TFM STM32L5 Memory Layout

TFM-STM32L5 package default config



SBSFU-STM32L5 delivery

- SBSFU-STM32L5 package:
ST deliver a SBSFU_Boot which in fact is TFM example code where security services has been removed.
- SBSFU_Boot
 - Authentication : RSA 2048, RSA3072 or ECC256
 - SHA256 integrity check
 - Confidentiality : AES CTR with symmetric key encrypted in RSA-OAP or ECIES-P256
 - Single mode or Dual slot mode :
 - Single slot : the download slot is also the execution slot
 - Dual slot : the download slot and execution slot are distinct
 - One image mode or dual image mode :
 - One image : secure and non secure application are combined in one binary with one set of metadata
 - Dual image : secure and non secure application are distinct

Default config in the package which
could be modified

SBSFU-STM32L5 delivery

- Secure service (SBSFU_Appli Secure)
 - LED blinking example
- SBSFU local loader (SBSFU_Loader)
 - Secure and Non secure application
 - Immutable
 - YModem

Default config in the package which could be modified

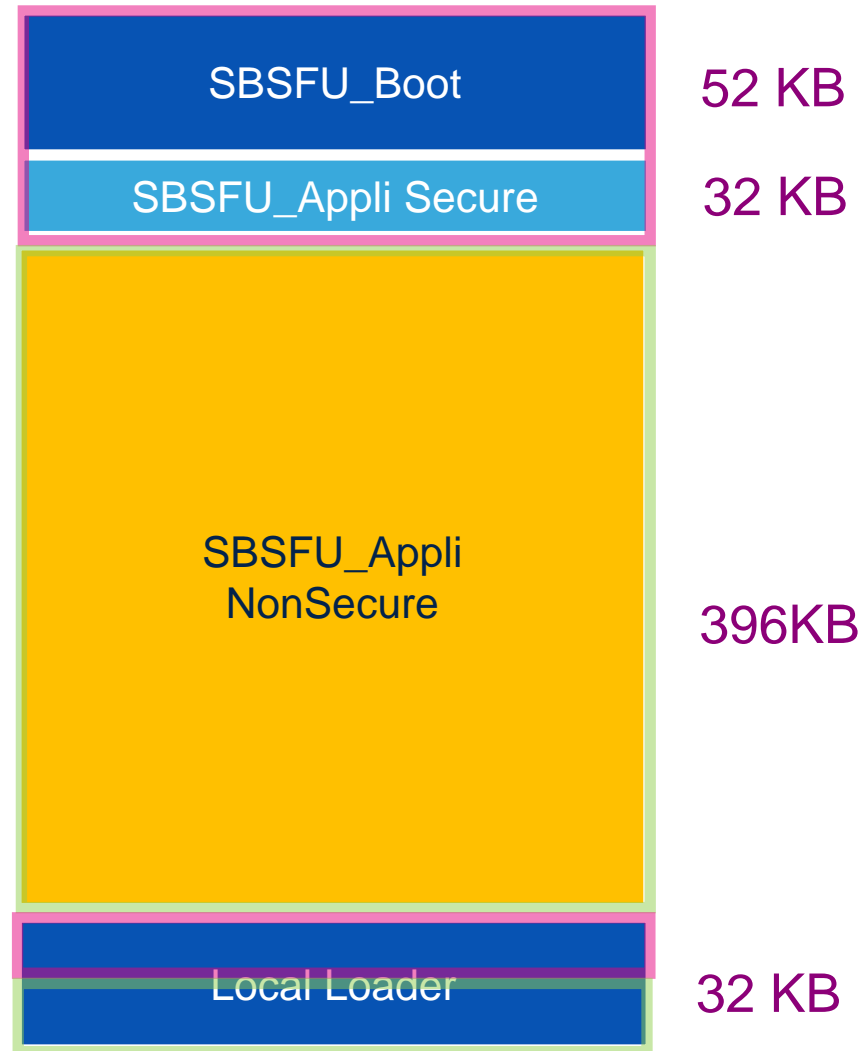
SBSFU STM32L5 Memory Layout

SBSFU-STM32L5 package

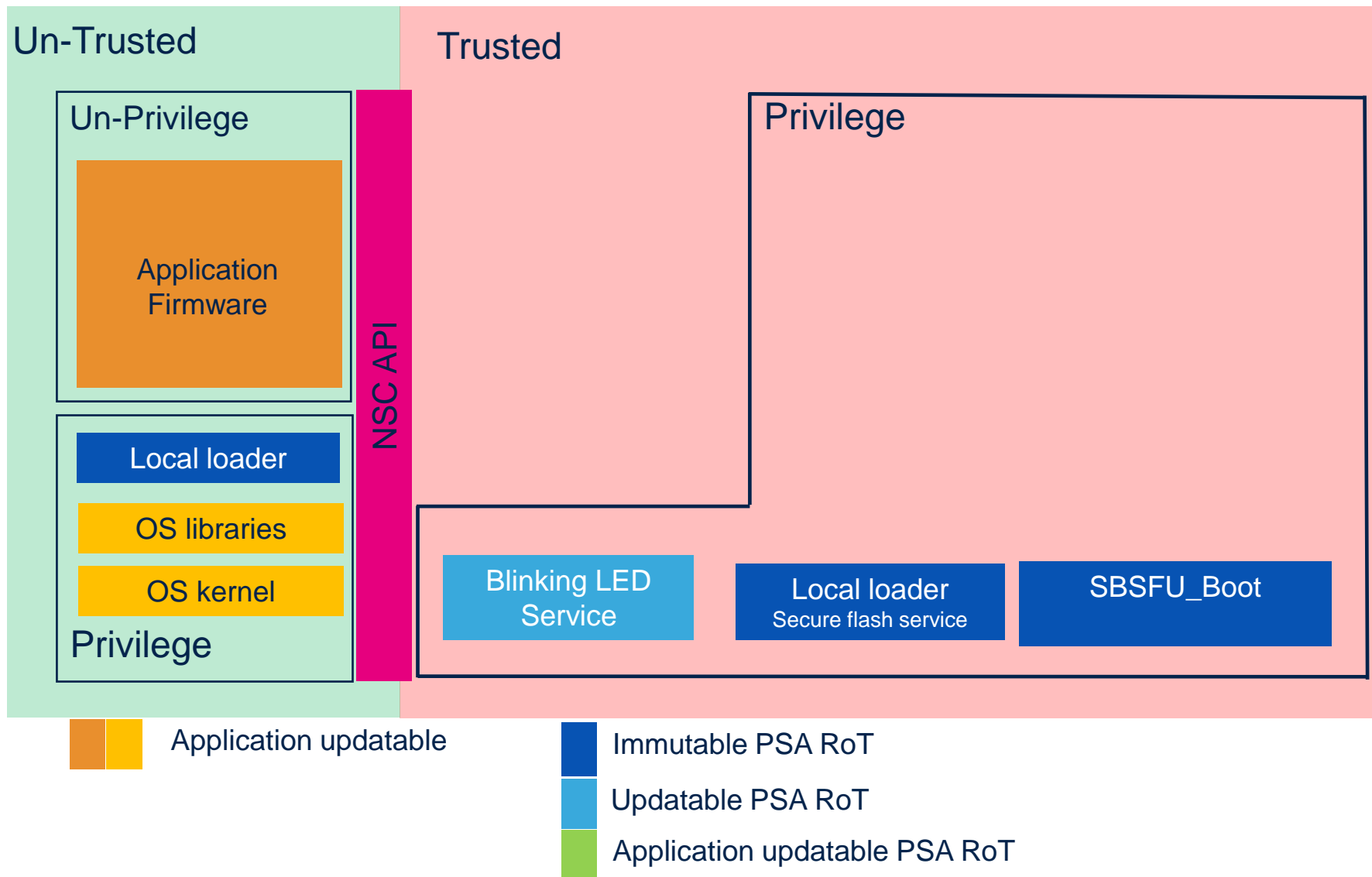
- single slot mode :
the download slot is the execution slot
- one image :
1 image for SBSFU_Appli secure +
Non secure and 1 signature for this
binary

Secure

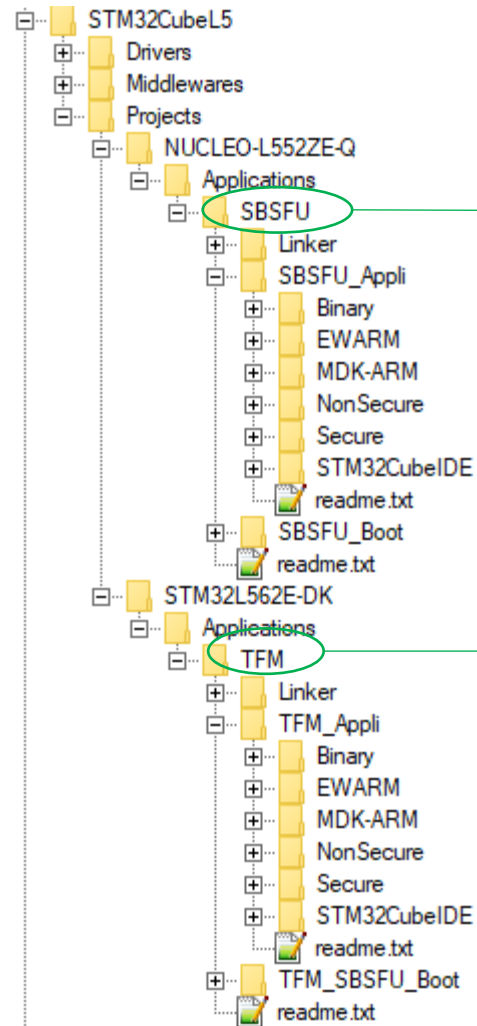
Non Secure



SBSFU-STM32L5



SBSFU/TFM-STM32L5 package

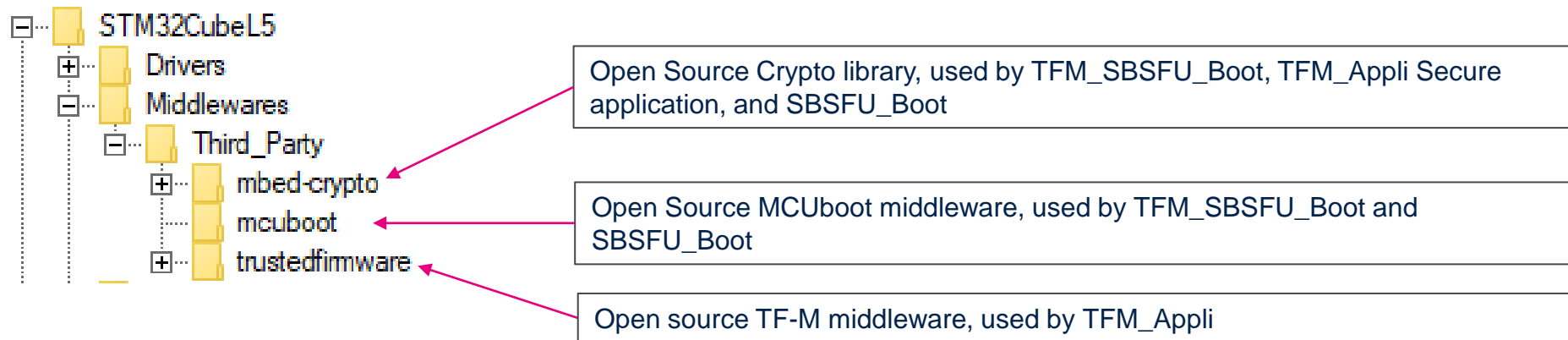


Secure Boot & Secure Firmware Update

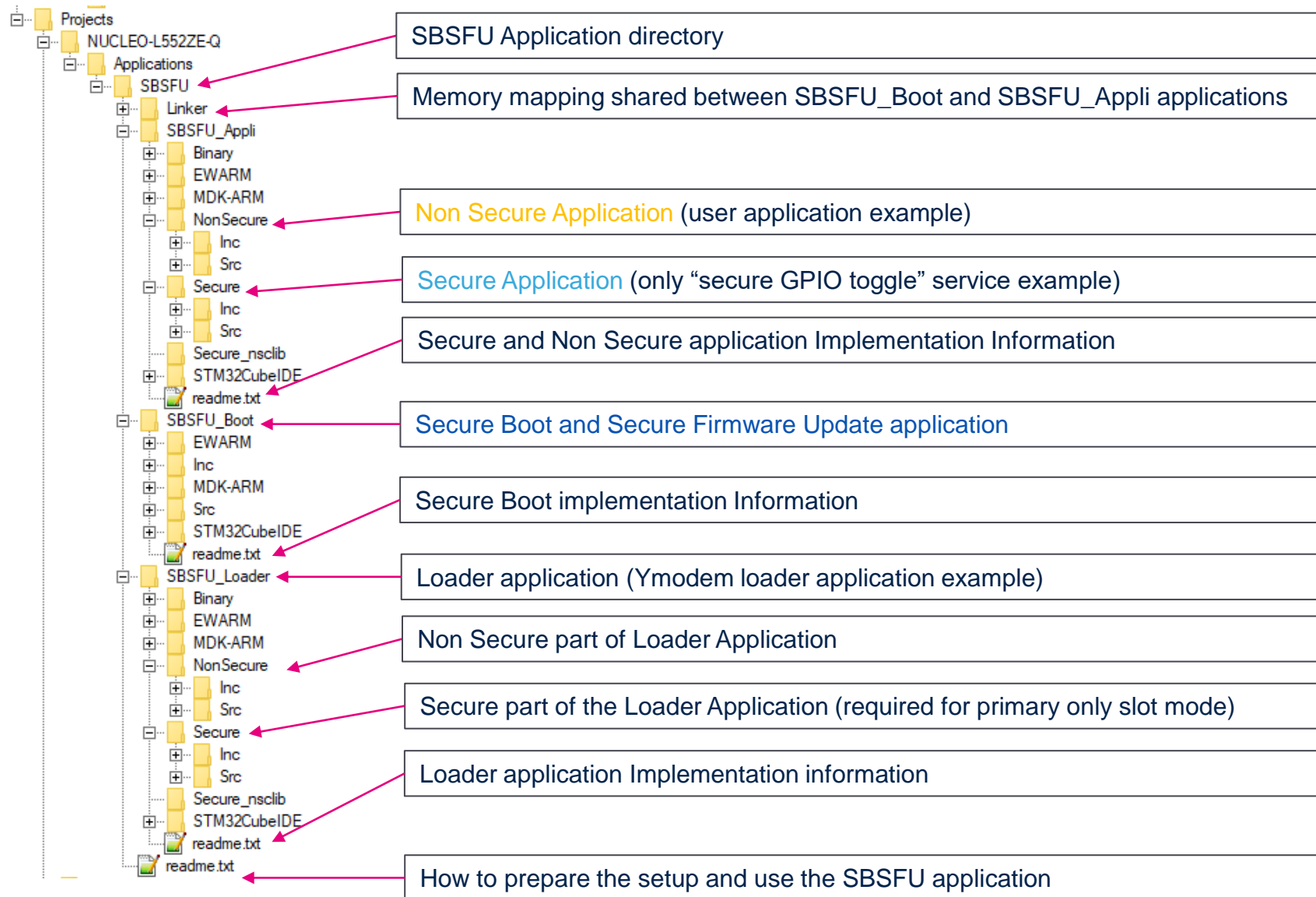
Starting example when coming from standard X-CUBE-SBSFU example.

Secure Boot & Secure Firmware Update + TFM secure services

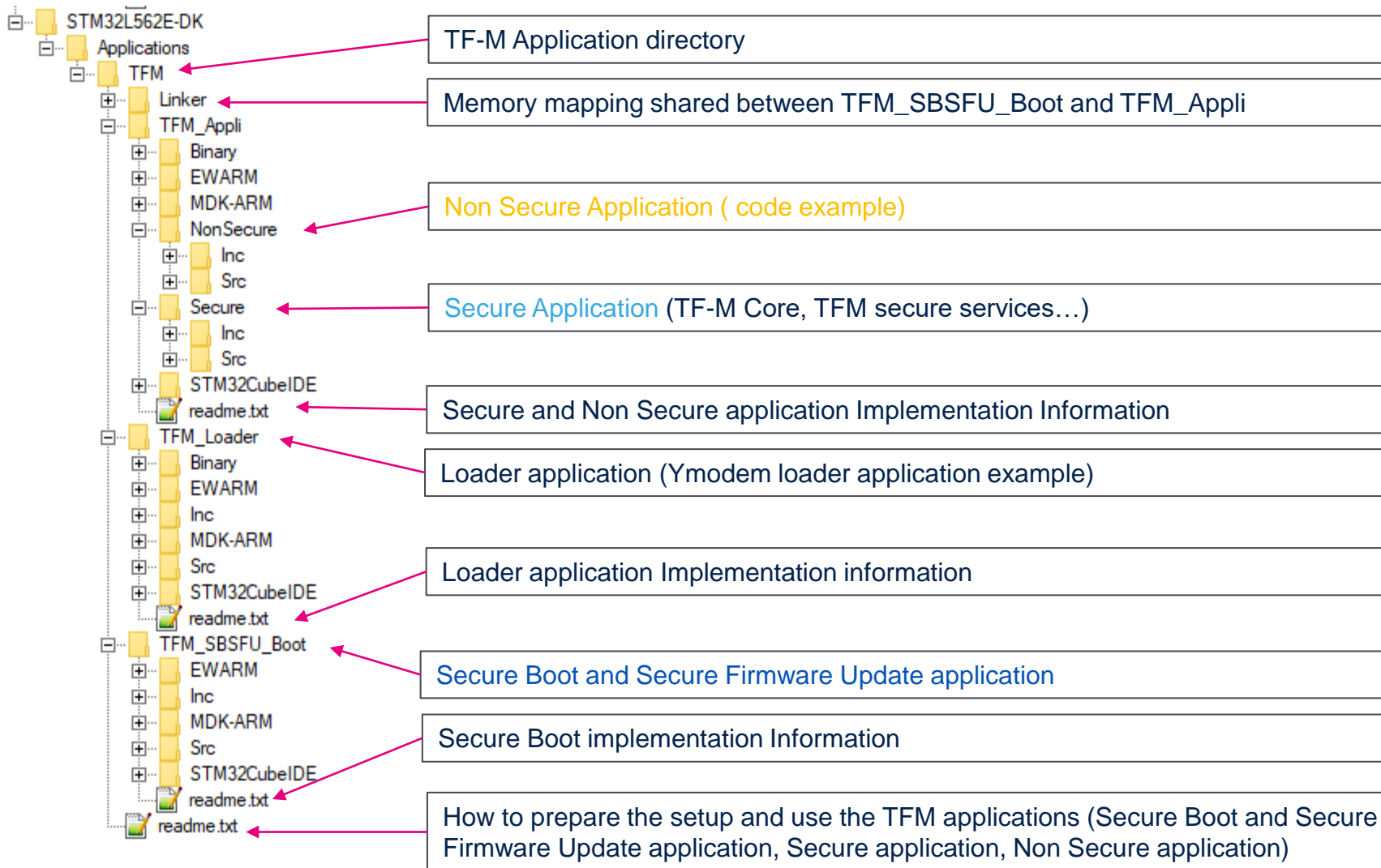
SBSFU-STM32L5 package



SBSFU-STM32L5 package

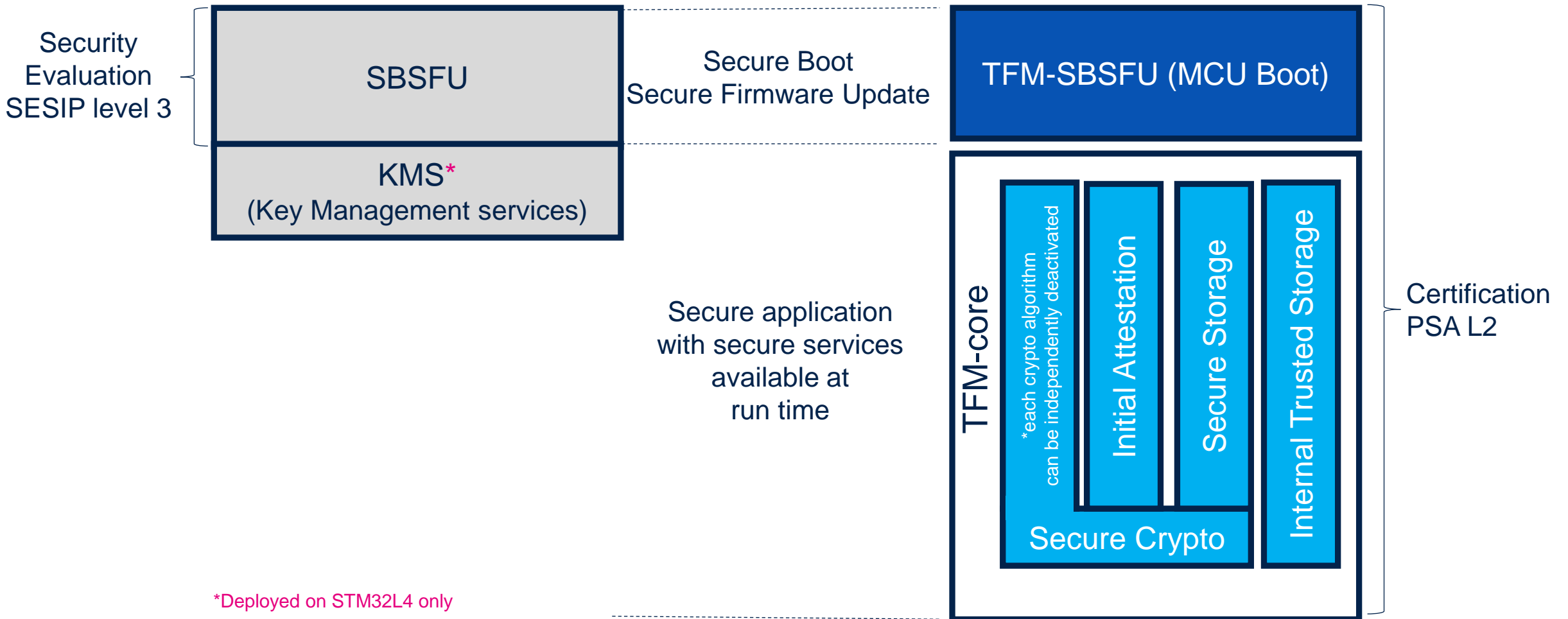


TFM-STM32L5 package



X-CUBE-SBSFU versus TFM

Certifications



Useful Links

- UM2671 : Getting started with STM32CubeL5 TFM Application
- AN5447 : Overview of Secure Boot and Secure Firmware Update solution on Arm® TrustZone® STM32L5 Series microcontrollers
- <https://developer.arm.com/architectures/security-architectures>
- <https://www.trustedfirmware.org/about/>

Conclusion

- TFM is a secure boot with secure firmware update capabilities but its also allows many security services during run-time. It's relying on PSA standard define by ARM and is open source based project.
- STM32L5 TFM implementation show usage of all the STM32L5 security features to achieve a PSA certification level 2.

Thank you

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