

10\$ Sip Phone Prototype

Baresip on ESP32

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Baresip

- SIP-stack for embedded devices
- BSD license
- platforms: linux, windows
- light-weight and less dependencies

- ESP-WROOM-32 WIFI+BT+BLE SoC system with freeRTOS
- Price \$6,00
- CPU: Xtensa dual-core (or single-core) 32-bit LX6 microprocessor, operating at 160 or 240 MHz
- Memory: 160 KiB static allocated + 160 KiB dynamic allocated DRAM
- Wi-Fi: 802.11 b/g/n