

10\$ Sip Phone Prototype

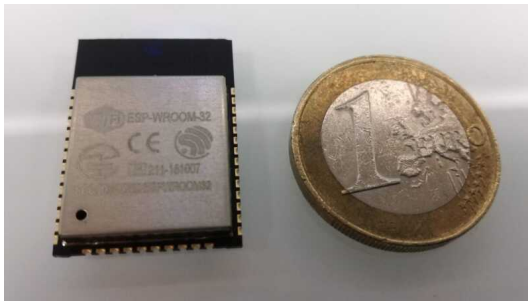
Baresip on ESP32

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- SIP-stack for embedded devices
 - light-weight and less dependencies
 - runs on wonderful ETH-908
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- Goal: baresip on ESP32 3\$ chip
 - Config: per cloud (mqtt)

ESP32



- ESP-WROOM-32 with Wi-Fi or Ethernet
- Price \$3,00
- CPU: Xtensa dual-core 32-bit, clock 2 x 240 MHz
- Memory: 320 KiB RAM (ESP32-WROVER 16MB RAM)

Challenges

- Build for free-RTOS
- network interface
- i2s driver for baresip
- low memory