

Mejorando el Rendimiento de un Controlador PID en Circuitos RLC mediante Optimización Metaheurística

Enhancing PID Controller Performance in RLC Circuits through Metaheuristic Optimization

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Abstract

This research explores the application of Genetic Algorithms (GA) to optimize the performance of PID controllers in RLC circuits. The proposed methodology leverages the principles of natural selection, crossover, and mutation to iteratively refine the PID gains. The fitness of each individual is determined by its ability to minimize the Mean Square Error (MSE) for a unitary step response in the RLC circuit. The results demonstrate that the GA-based optimization approach yields significant improvements in the performance of PID controllers, achieving a significant reduction in the MSE for a unitary step response. This research highlights the importance of metaheuristic optimization techniques in the context of control systems, exemplifying their potential to enhance the performance of PID controllers in RLC circuits and beyond.

Keywords: PID Controller; Genetic Algorithm; RLC Circuit; Optimization; Metaheuristic.

Introduction

In the modern era, control systems have become an integral part of our daily lives. These systems play a pivotal role not only in industrial applications (Thomas & Srinivasan, 2019) but also in various aspects of our technological ecosystem. Control systems are indispensable in ensuring that machines, processes, and devices operate efficiently, safely, and in accordance with predefined parameters. They provide the means to regulate variables such as temperature, pressure, speed, and voltage, to name just a few.

Among the key elements of control systems, PID¹ controllers stand out as versatile and fundamental tools. These controllers find extensive use in industries such as manufacturing, chemical processing, robotics, and even consumer electronics. They excel at tasks ranging from temperature regulation in industrial furnaces to maintaining the speed of a motorized vehicle. The adaptability and effectiveness of PID controllers have established them as a fundamental tool in the arsenal of control engineers, ensuring the stability, accuracy, and efficiency of numerous systems and processes.

However, achieving optimal control is not a trivial task; it requires meticulous tuning and optimization. This is where the significance of fine-tuning control systems, such as PID controllers, comes into play (Jakhar & Gaur, 2015; Aranza, Kustija, Trisno, & Hakim, 2016). Different control systems and processes demand tailored approaches to ensure stability, robustness, and performance. As a result, exploring various tuning techniques is indispensable in the realm of control systems. This research delves into the application of Genetic Algorithms to enhance the tuning of PID controllers, exemplifying their importance in the broader context of control theory and its myriad real-world applications.

¹ Proportional, Integral and Derivative Control

Theoretical Background

In this section, we delve into the fundamental technical concepts that underpin our research, laying the groundwork for understanding the interplay between RLC circuits and PID controllers. These concepts form the basis for our subsequent exploration of the application of metaheuristic optimization techniques, specifically Genetic Algorithms (GA), which will be detailed in the methodology section. To comprehend the significance of optimizing PID controllers in the context of RLC circuits, it is essential to first establish a solid foundation in these core elements of control theory.

RLC Circuit

The RLC circuit is a fundamental component in electrical engineering, consisting of passive electrical components: resistors (R), inductors (L), and capacitors (C). These circuits are widely used for various applications, including filtering, signal processing, and energy storage. The behavior of RLC circuits is governed by the principles of electrical impedance, which dictate how the circuit responds to different frequencies of electrical signals. A series RLC circuit is shown in Figure 1.

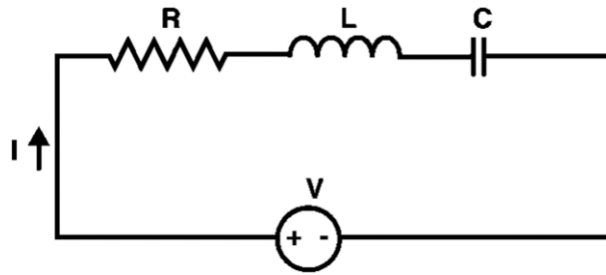


Figure 1. Series RLC Circuit.

Through a fundamental circuit analysis, we can derive the transfer function of the RLC circuit, which provides a mathematical representation of its behavior. This transfer function encapsulates the relationship between the input and output signals of the circuit, enabling us to understand how the circuit responds to various stimuli. The transfer function serves as a crucial tool in control system design and optimization, allowing us to analyze and fine-tune the system's dynamics for optimal performance.

$$\frac{V_c(s)}{V(s)} = \frac{1}{LCs^2 + RCs + 1}$$

where V_c is the voltage in the capacitor, V is the input voltage, L is the inductance, C is the capacitance and R is the resistance.

PID Controller

PID controllers, short for Proportional-Integral-Derivative controllers, are a cornerstone of control theory and automation. These controllers are a vital component in feedback control systems, and their versatile design makes them ubiquitous in a wide array of applications.

A PID controller consists of three main components: the Proportional (P), Integral (I), and Derivative (D) terms, each serving a distinct role in shaping the controller's response. The proportional term responds to the current error value, the integral term deals with the accumulation of past errors, and the derivative term anticipates future error trends based on the rate of change. By carefully adjusting the weights and contributions of these three terms, a PID controller can achieve a finely tuned balance between responsiveness and stability, making it a versatile tool for control in a wide range of dynamic systems.

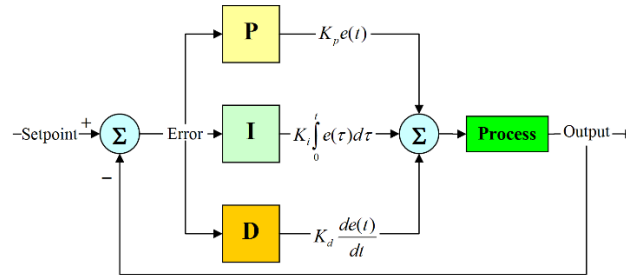


Figure 2. PID Workflow.

The control loop utilizing a PID controller, as shown in the Figure 2, typically operates as follows: First, the process variable (PV), representing the actual state of the system, is continuously measured and compared to the desired setpoint (SP). The resulting error signal, which quantifies the deviation between the PV and SP, is then fed into the PID controller. The controller processes this error signal through its proportional, integral, and derivative components, producing a control output (CO) that is applied to the system. The control output, in turn, adjusts the system's behavior, seeking to minimize the error and maintain the process variable as close to the setpoint as possible. This continuous feedback loop, governed by the PID controller's actions, enables precise control over a wide range of dynamic processes.

Proposed Methodology

Circuit

For the purpose of this study, a RLC circuit was designed to be used as a testbed for the optimization of PID controllers. The circuit consists of a RLC resonator in series with a DC voltage source and a load resistor. The values of the circuit based on the Figure 1 are shown in the Table 1.

Table 1. Values for the RLC Circuit

Element	Value	Unit
Resistor	10	Ohms
Inductor	100	milli Henry
Capacitor	100	nano Farad

Genetic Algorithm Optimization

The Genetic Algorithm (GA) is a powerful metaheuristic optimization technique that draws inspiration from the process of natural selection and evolution. It provides an effective approach to fine-tuning PID controllers. The typical steps of a GA, as shown in the Figure 3.

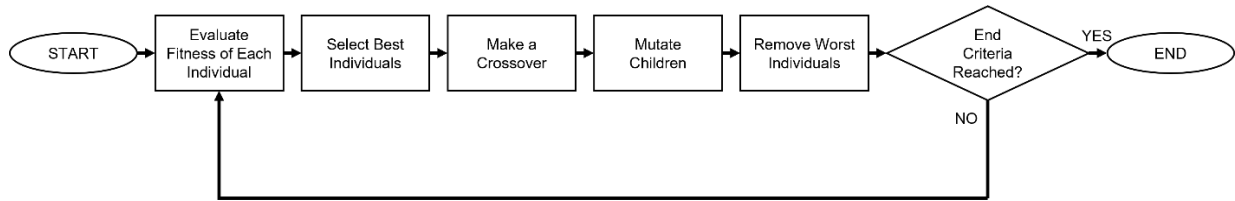


Figure 3. Block Diagram for a simple Genetic Algorithm.

1. **Evaluate Fitness:** In the first step, the fitness of each individual in the population is assessed. The fitness function quantifies how well a set of PID controller gains performs in optimizing the system. This evaluation guides the selection process by determining which individuals are more likely to be chosen as parents for the next generation.
2. **Selection:** The selection process mimics the idea of "survival of the fittest." Individuals with higher fitness scores have a greater chance of being selected as parents for reproduction. This promotes the propagation of promising solutions and drives the optimization process.
3. **Crossover:** Crossover involves combining the genetic information of two selected individuals to create new offspring. This mimics genetic recombination in natural evolution and introduces diversity into the population, potentially yielding better solutions.
4. **Mutation:** Mutation introduces random changes in an individual's genetic information. This step helps explore new regions of the solution space, preventing premature convergence to suboptimal solutions.
5. **Remove Worst:** To maintain a constant population size, individuals with lower fitness are removed to make way for the newly created offspring. This step ensures that the population remains dynamic and adaptable.

The algorithm iterates through these steps until a satisfactory solution is found or, in our case, a predefined number of generations is reached.

Metaheuristic Implementation

The adaptation of the Genetic Algorithm (GA) for optimizing PID controller gains is a fundamental aspect of our methodology. In this section, we elucidate how the GA workflow depicted in Figure 3 was tailored to suit the unique nature of PID controller parameters. Each individual within the GA population represents a set of PID gains, reflecting a potential solution to the optimization problem. The algorithm leverages the principles of natural selection, crossover, and mutation to iteratively refine these sets of gains. The fitness of each individual is determined by its ability to minimize the Mean Square Error (MSE) for a unitary step response in the RLC circuit.

The Mean Square Error (MSE) is a pivotal performance metric employed to assess the quality of PID controller tuning. It quantifies the disparity between the desired unitary step response and the actual system response generated by a set of PID gains. Specifically, the MSE measures the average of the squared differences between the desired response and the system's actual response over a predefined time interval. A lower MSE value signifies a closer match between the desired and achieved responses, indicative of superior control system performance. Therefore, the objective of our GA-based optimization is to minimize this MSE, ultimately leading to PID gains that result in more accurate and precise control of the RLC circuit. In the subsequent sections, we delve into the specifics of our modified algorithm and how it efficiently navigates the solution space to achieve these optimization objectives.

Results and Conclusions

In this section, we present the outcomes of our research, highlighting the achievements and implications of the modified Genetic Algorithm (GA) for PID controller tuning within the context of RLC circuits.

Using the algorithm outlined in this research, we successfully optimized the PID controller gains for the RLC circuit. The Figure 4 illustrates the significant improvements achieved in the PID gains, demonstrating how the Genetic Algorithm effectively fine-tuned the controller for enhanced performance.



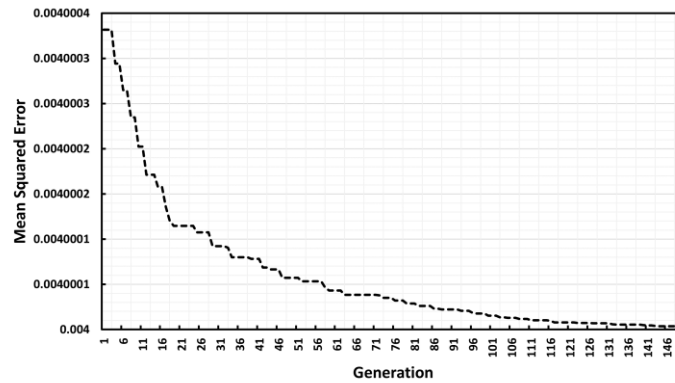


Figure 4. Fitness Evolution

To gauge the practical impact of our optimization efforts, we conducted a comparative analysis of the performance between the non-optimized and optimized PID controllers within the same RLC circuit. This evaluation encompassed various input scenarios and assessed key performance metrics, such as settling time, overshoot, and steady-state error. The results, depicted in the Figure 5, vividly illustrate the superior performance of the optimized PID controller, reaffirming the efficacy of our approach in achieving precise control and robust system response.

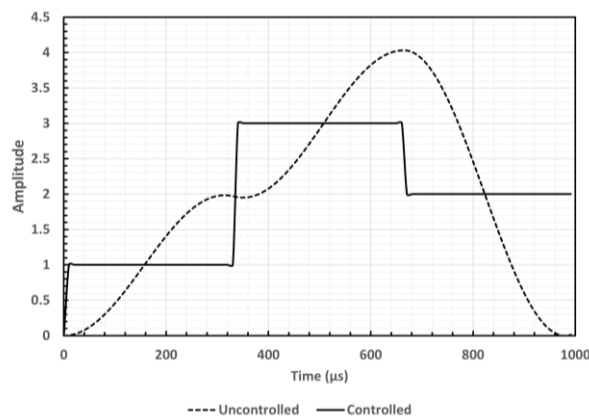


Figure 5. Comparison to Different Inputs.

The success of this research underscores the potential of Genetic Algorithms as a valuable tool in the field of control theory. The application of metaheuristic optimization techniques, such as GAs, holds promise for fine-tuning control systems, not only in RLC circuits but also across a spectrum of engineering domains. As we continue to explore advanced optimization methodologies, we open doors to improved system performance, reduced energy consumption, and enhanced system robustness.

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