Smart Response XE Extension port

2	4	6	8	10	12	14	16	18	20
Serial0 (FTDI interface)						ISP + SPI			I²C
Serial1		JTAG or I/O (analogue and digital)							
1	3	5	7	9	11	13	15	17	19
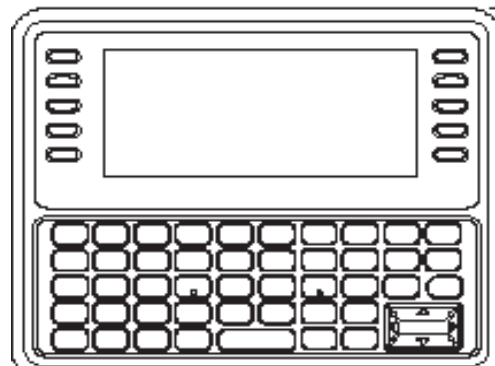
J2-6	J2-6		J3-3	J3-1	J2-5 1µF	J2-2	J2-4	J2-6	U5-7
2	4	6	8	10	12	14	16	18	20
GND	GND	NC	RXD0	TXD0	DTR	VCC	MOSI	GND	SDA
TXD1	RXD1	TCK (F4)	TMS (F5)	TD0 (F6)	TDI (F7)	MISO	SCK	RST	SCL
1	3	5	7	9	11	13	15	17	19
R3	Z1	J1-1	J1-5	J1-3	J1-9	J2-1	J2-3	J2-5	U5-6

ISP is first for bootloader programming but it also exposes the SPI I/Os
JTAG signals can be used as I/O too (both digital and analogue), providing JTAG is deactivated with the fuses
Serial lines for programming or I/O (beware, lines are shared with keyboard matrix and/or other functions)
Pin 6 is Not Connected. This point is Vcc on FTDI cable. But it usually delivers 5V and as the terminal is only 3.3V compatible it is more safe to not connect this pin. This implies the terminal is powered another way.
There is 3.3V on Vcc pin. Beware there is no protection on this pin.
12 is connected to 17 throught a 1µF capacitor and then to J2-5.