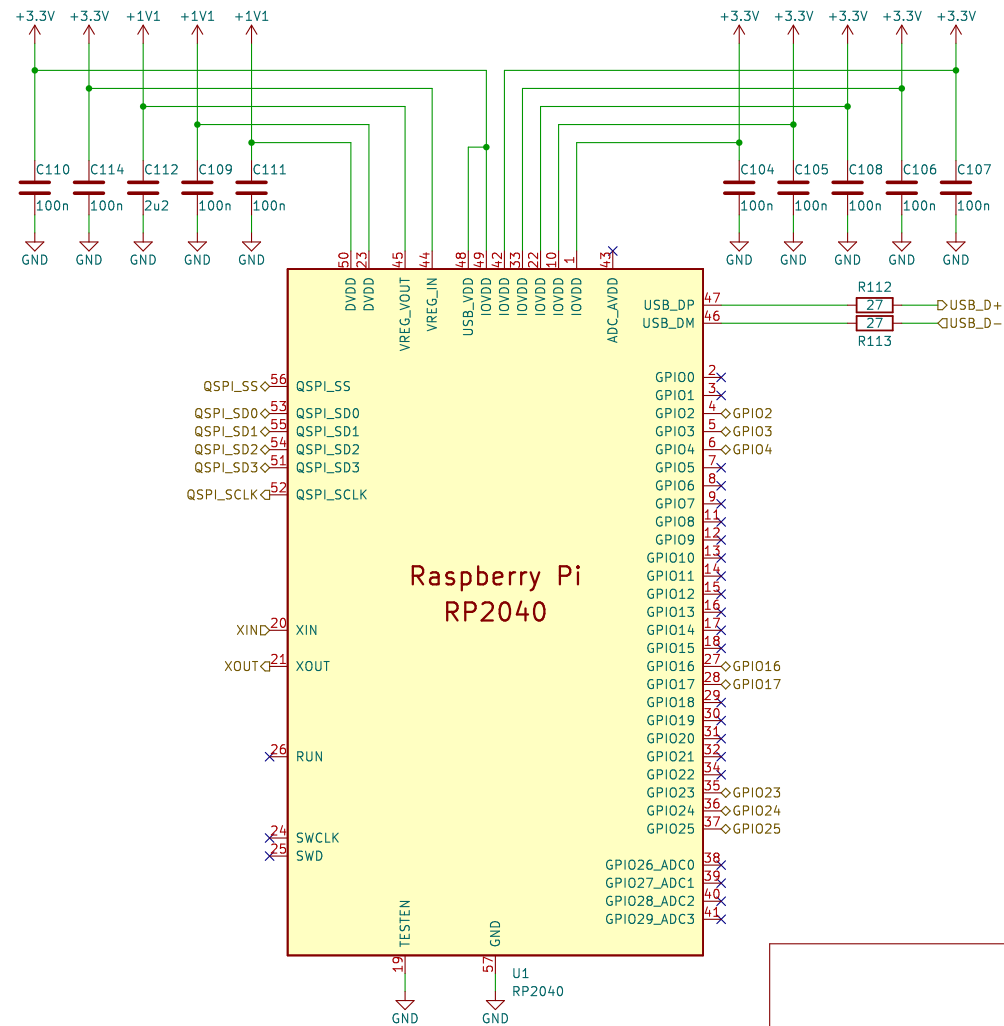


MCU



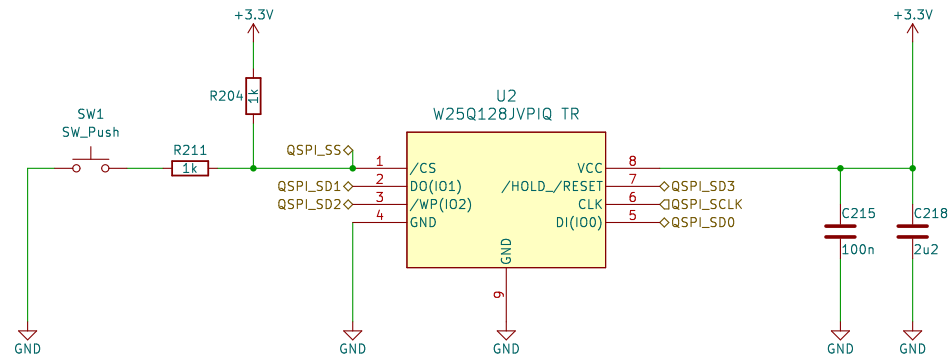
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Flash and Boot Select



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Power and Data

The schematic diagram illustrates a power and data management circuit. Key components include:

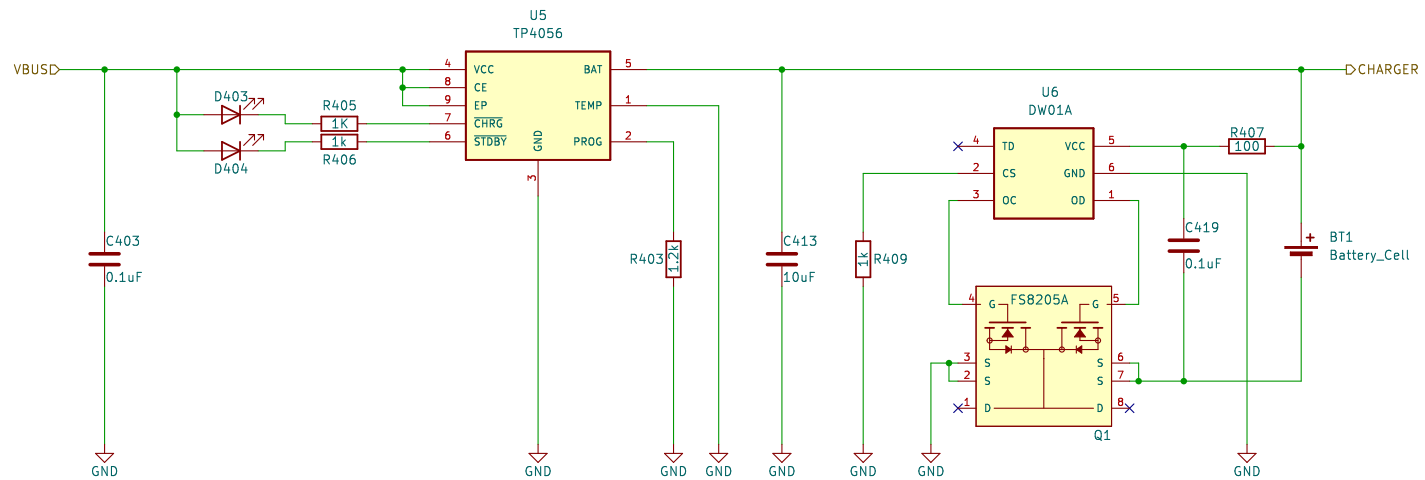
- J1 USB_B_Micro Shield**: A USB connector interface.
- U4 DMG2305UX-13**: A module, likely a USB-to-UART bridge or similar interface.
- CR1, CR2 MBR120VLSFT1G**: Diodes used for signal conditioning or protection.
- U3 RT6150B-33GQW**: A DC-DC buck converter for voltage regulation.
- R301, R302, R308, R310**: Resistors for current limiting and signal termination.
- C301, C302**: Capacitors for decoupling and filtering.
- TP1, TP2, TP3, TP4**: Test points for debugging and measurement.

The circuit is powered by a CHARGER input and provides a regulated +3V3 output. It also interfaces with a microcontroller via GPIO pins (GPIO23, GPIO24, GPIO25).

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Battery Charger



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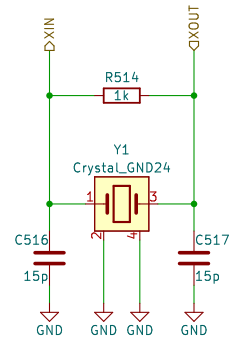
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Id: 5/8

Crystal



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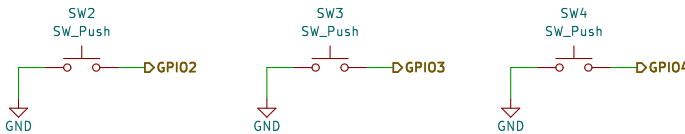
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Switches



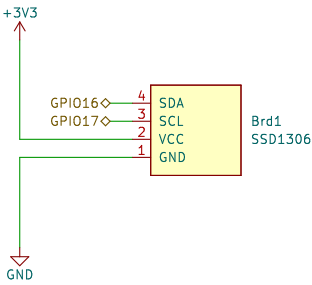
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Id: 7/8

Screen



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