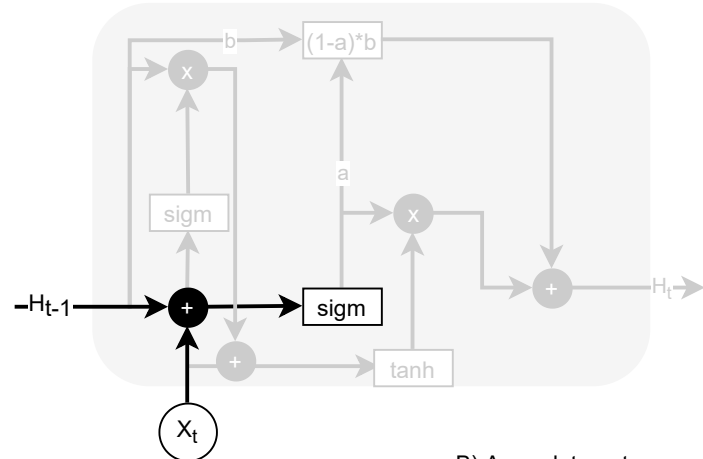
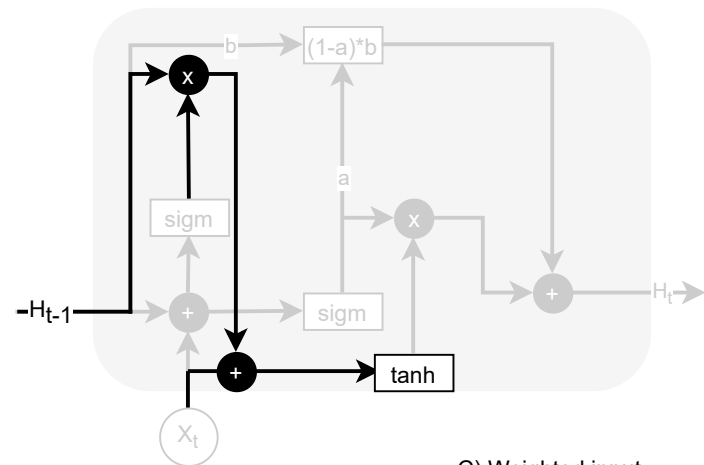


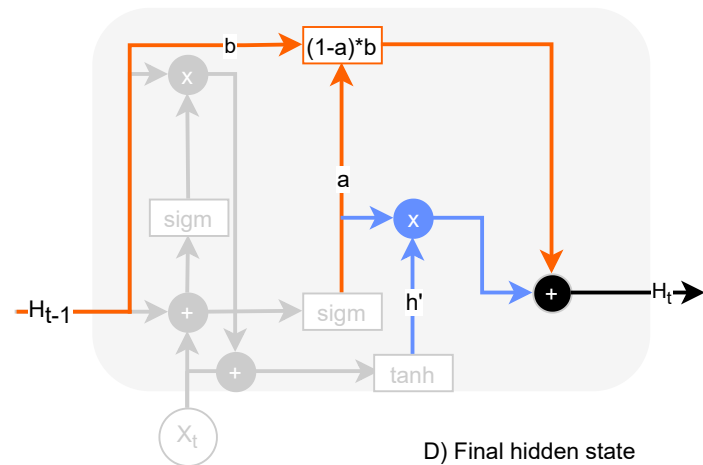
A) A reset gate



B) An update gate



C) Weighted input



D) Final hidden state  
 $(1-a)*H_{t-1} + a*h'$