	Та	sk (3ate									
31		16 1	5 14 13	12	8	7					0	
		F	D P L	0 0 1 0	1							
31		16 1	5								0	
	TSS Segment Selector											
Interrupt Gate												
31		16 1	5 14 13	3 12	8	7		5	4		0	
	Offset 3116	F	D P L	0 D 1 1	0	0	0	0				
31		16 1	5								0	
	Segment Selector			Offset 150								
31	Tra	-	Sate 5 14 13	112	Ω	7		5	1		0	
	Offset 3116		D P L	0 D 1 1			0		7		j,	
31		16 1	5	1							0	
	Segment Selector		Offset 150									
	DPL Descriptor Privilege Level Offset Offset to procedure entry point P Segment Present flag Selector Segment Selector for destination code segment D Size of gate: 1 = 32 bits; 0 = 16 bits Reserved											