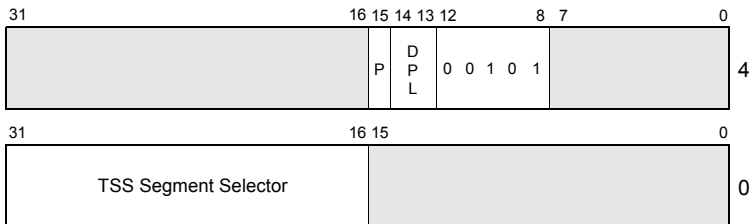
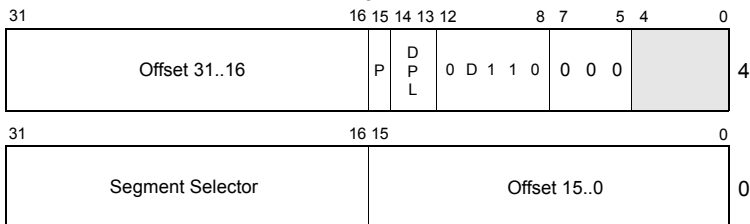


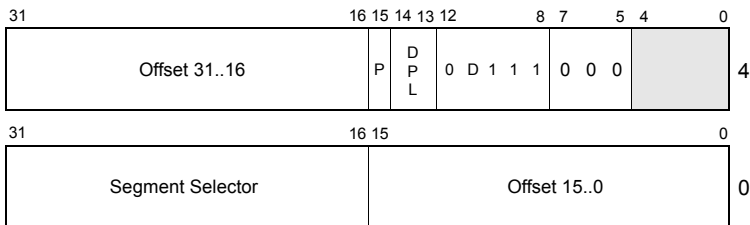
Task Gate



Interrupt Gate



Trap Gate




- DPL Descriptor Privilege Level
 Offset Offset to procedure entry point
 P Segment Present flag
 Selector Segment Selector for destination code segment
 D Size of gate: 1 = 32 bits; 0 = 16 bits
 Reserved

Figure 5-2. IDT Gate Descriptors