| Address | 31 24 | 23 16 | 15 12 | 118 | 7 6 | 5 52 | 1 | 0 | |
|-----------|----------------------|-------|--------|-----|-----|--------|---|---------------------|---------------------|
| Base | front buffer address | | | | | | | | Buffer register |
| Base + 4 | back buffer address | | | | | | | | Backbuffer register |
| Base + 8 | Y | | X | | | | | Resolution register | |
| Base + 12 | m | n | Unused | BS | SB | Unused | A | S | Status register |