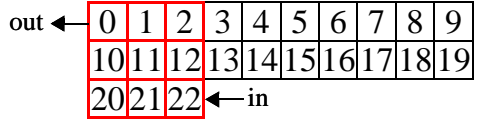
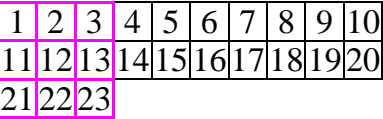


Shift Register at Cycle X



Shift Register at Cycle X+1



Shift Register at Cycle X+2

