Truth Table

AND		
Input1	Input2	Result
0	0	0
1	0	0
0	1	0
1	1	1

OR		
Input1	Input2	Result
0	0	0
1	0	1
0	1	1
1	1	1

NAND		
Input1	Input2	Result
0	0	1
1	0	1
0	1	1
1	1	0

NOR		
Input1	Input2	Result
0	0	1
1	0	0
0	1	0
1	1	0

Inverter	
Input1	Result
0	1
1	0

XOR		
Input1	Input2	Result
0	0	0
1	0	1
0	1	1
1	1	0

XNOR		
Input1	Input2	Result
0	0	1
1	0	0
0	1	0
1	1	1