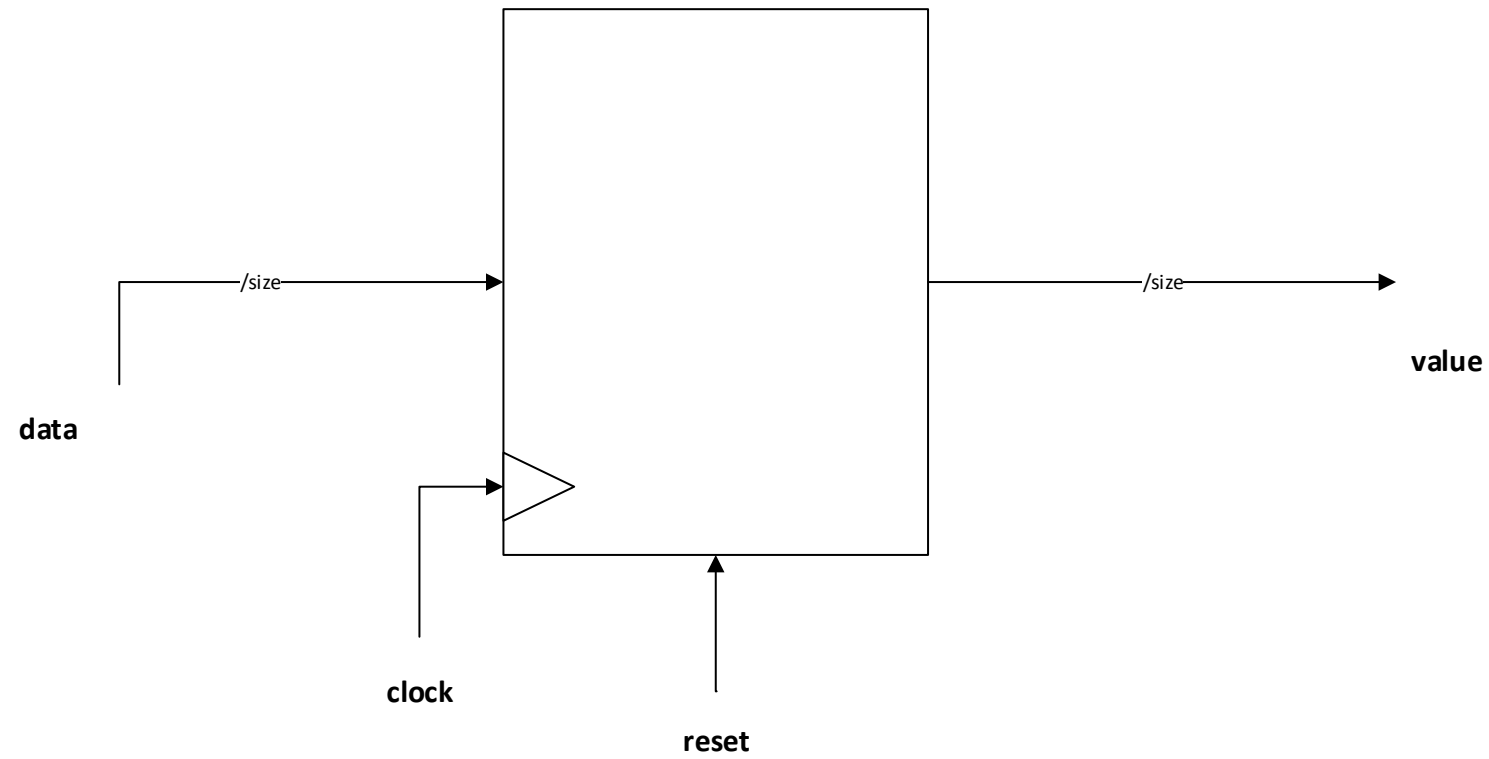


register

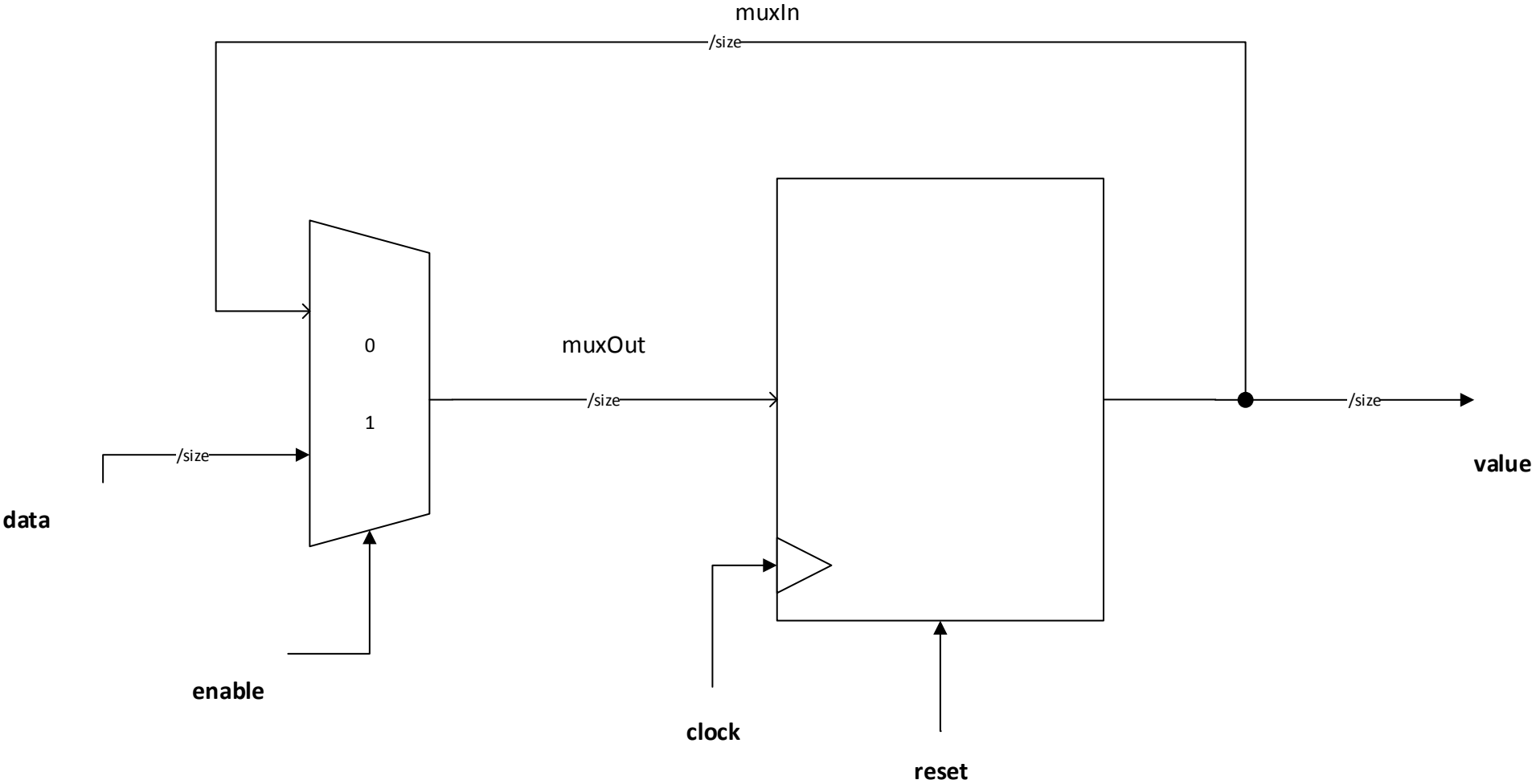


Generic:

size

Generic register

geregisteren

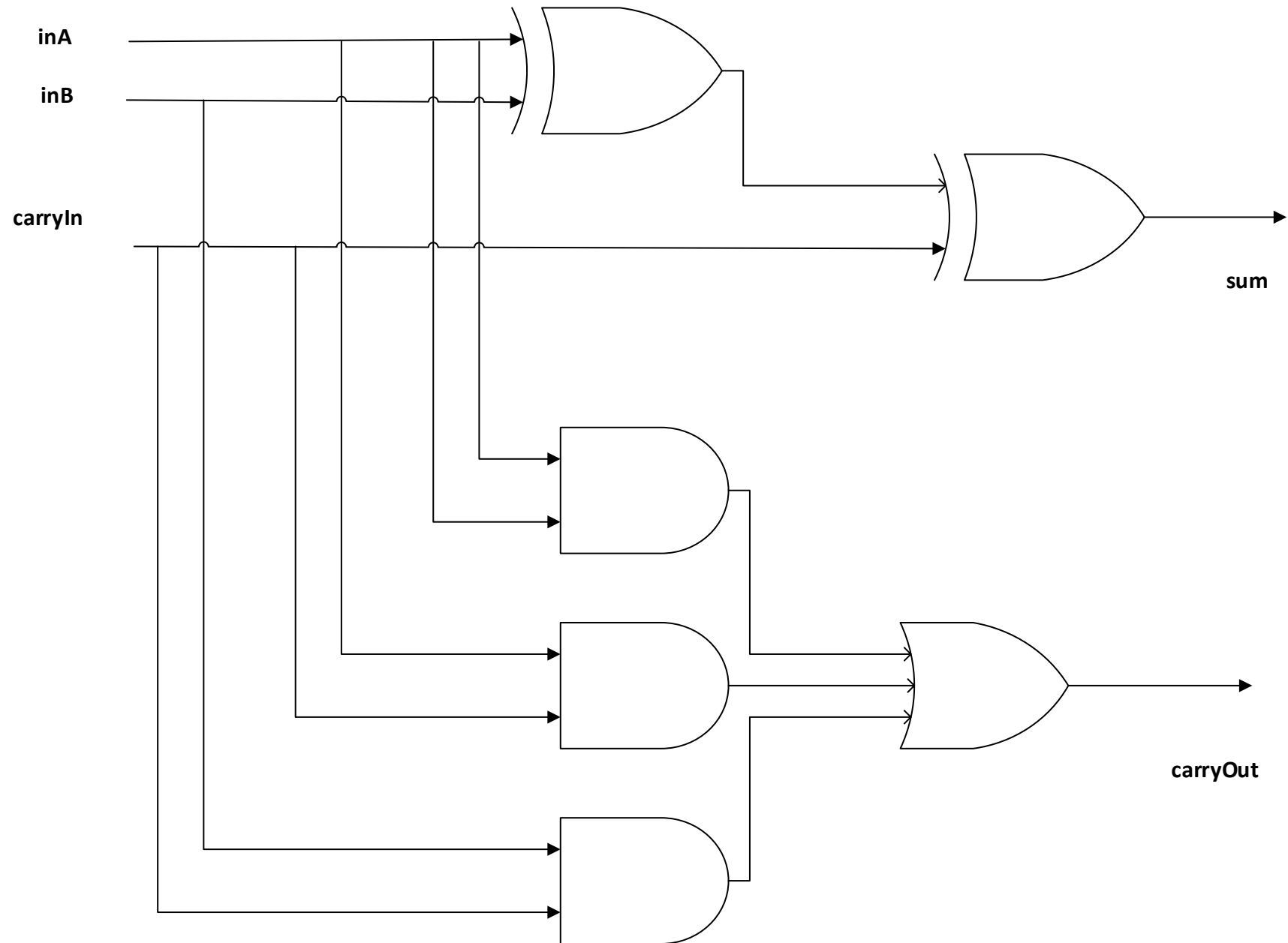


Generic:

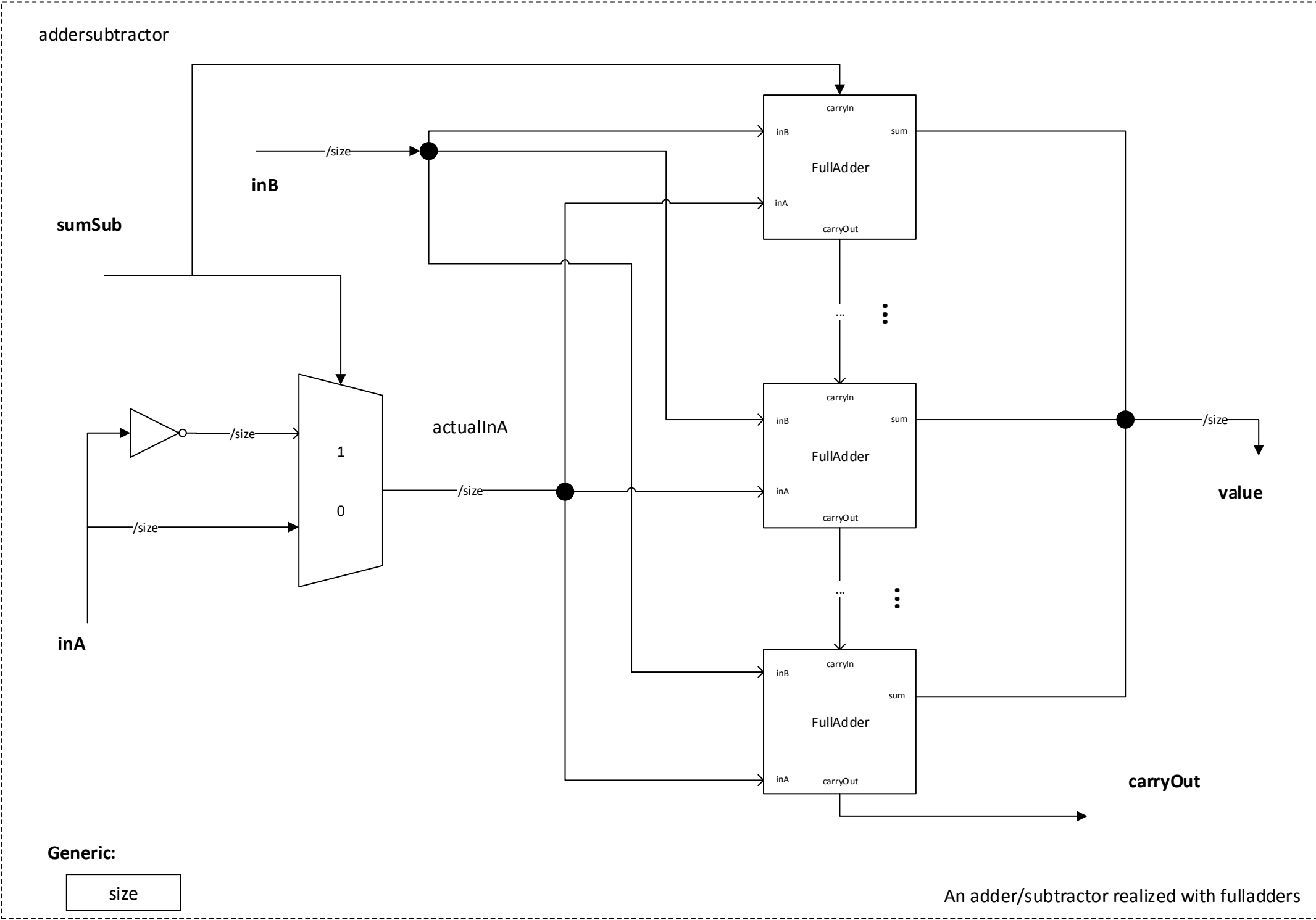
size

Generic register with enable

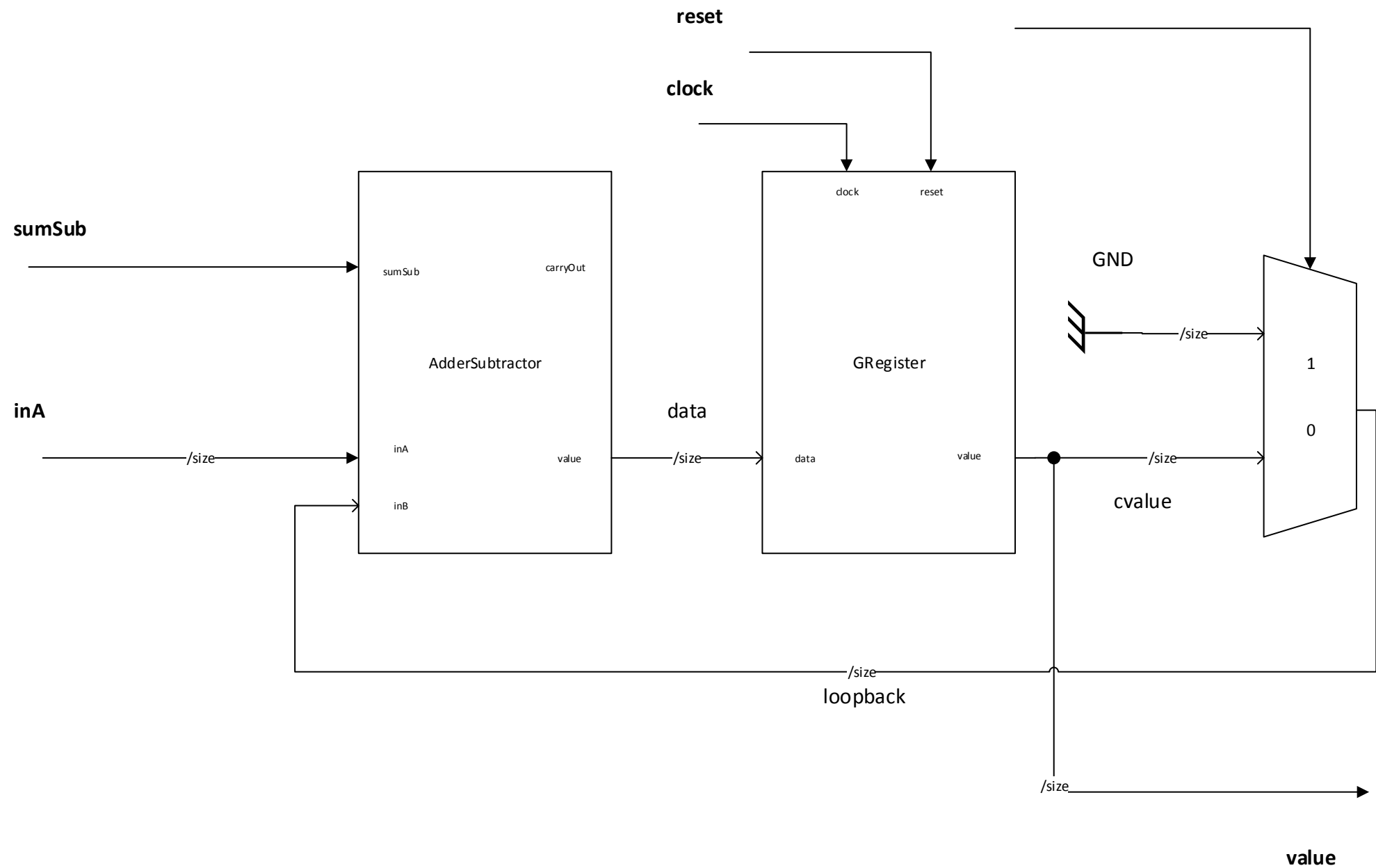
fulladder



Full adder with carry



accumulator

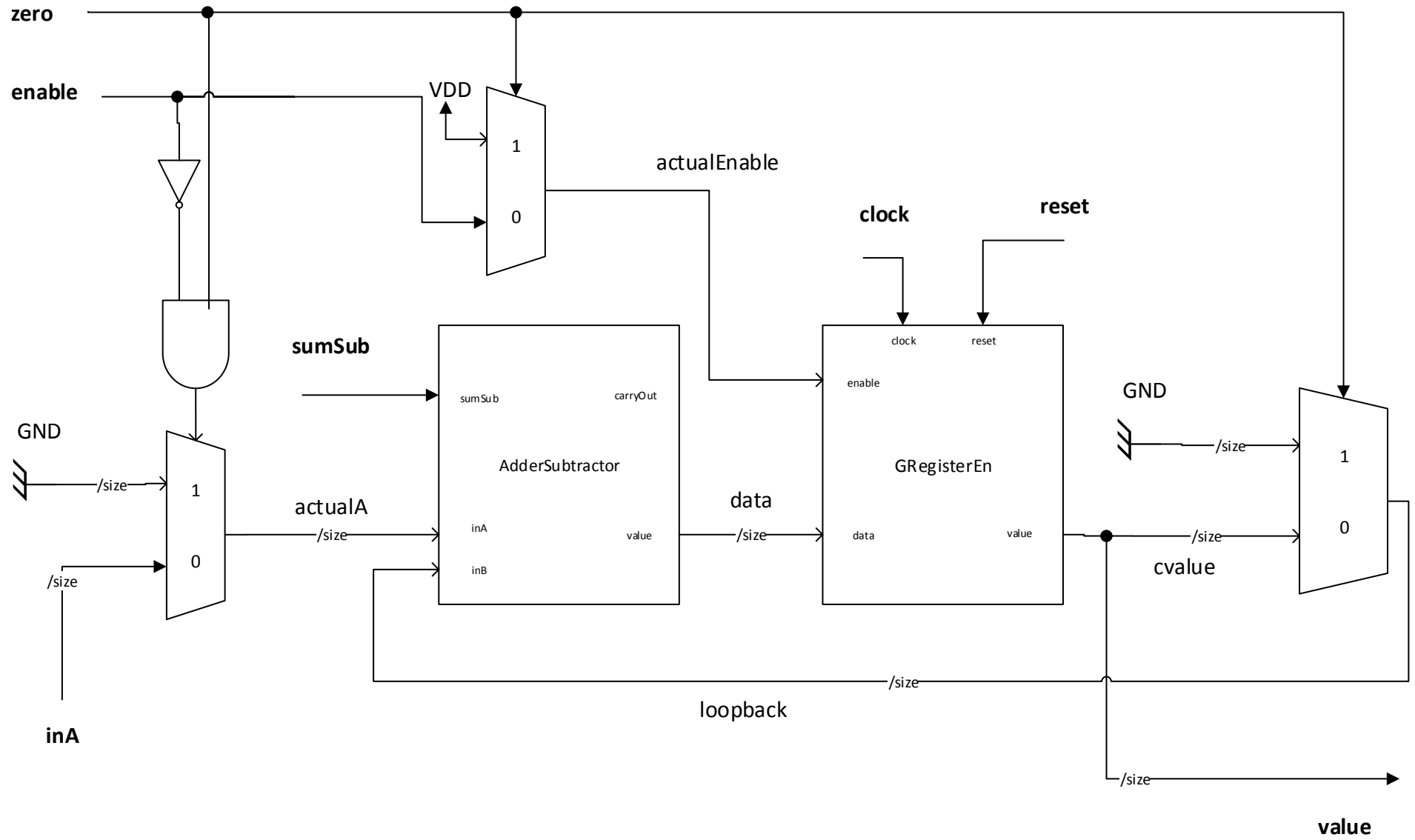


Generic:

size

Accumulator with asynchronous reset

accumulatore

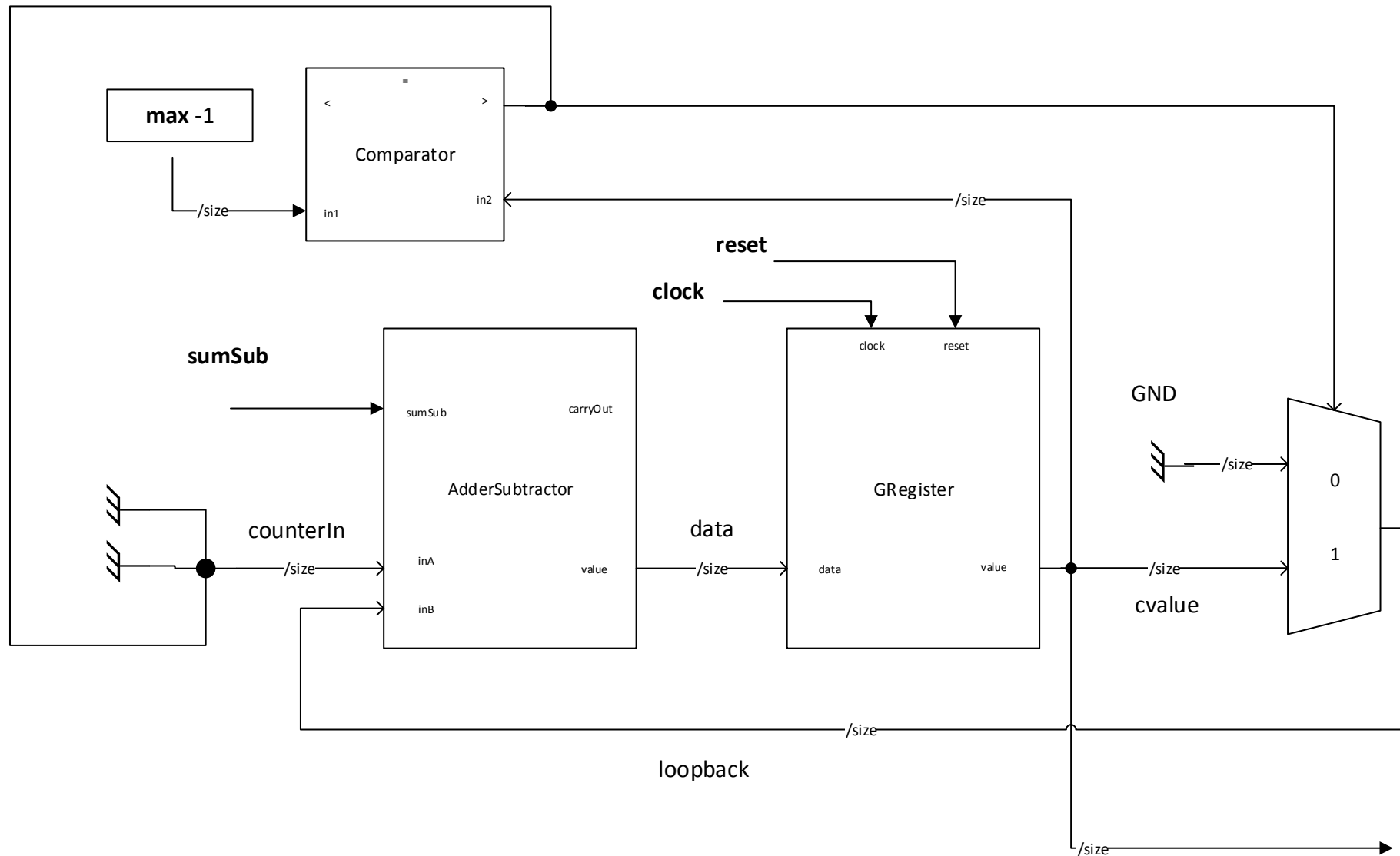


Generic:

size

Accumulator with asynchronous reset and enable signal

counterwithmax



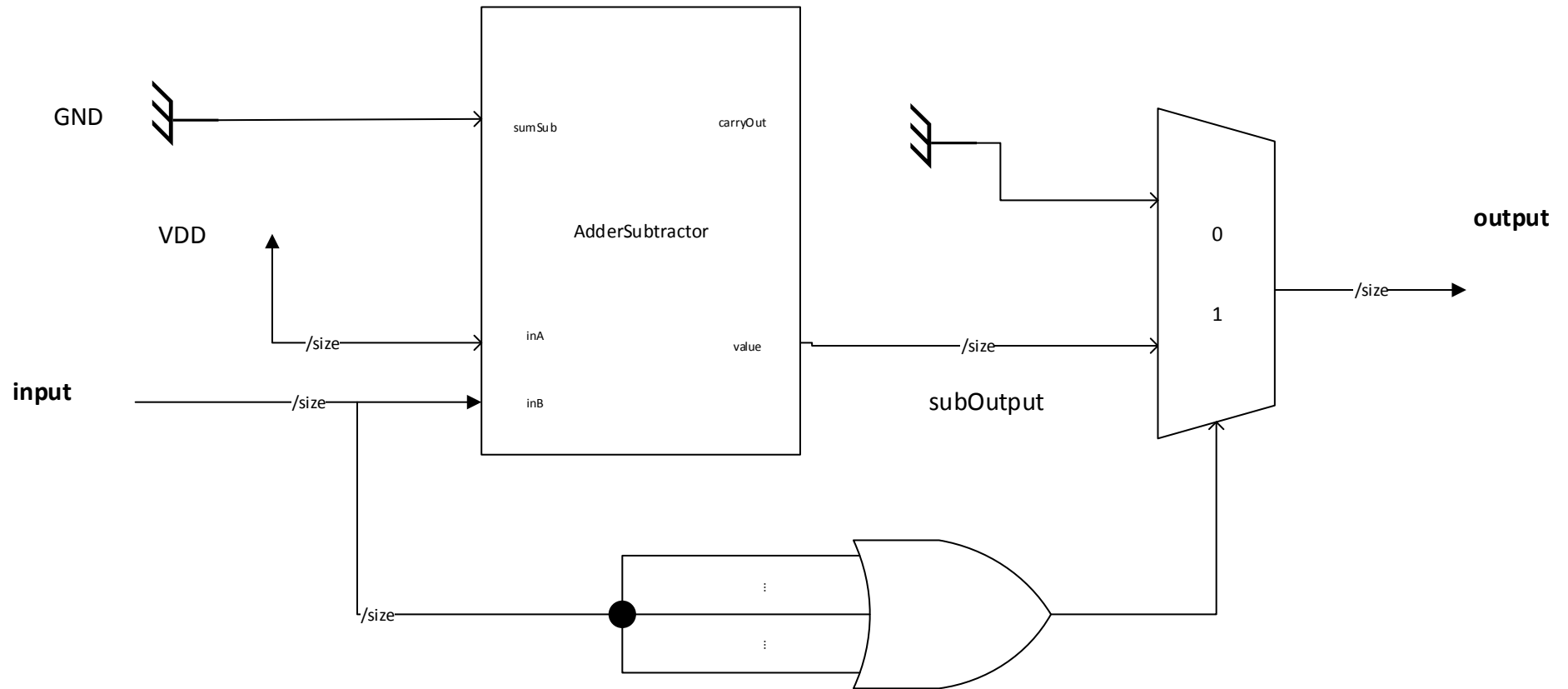
Generic:

size

max

Counter with upper bound

shiftadjuster

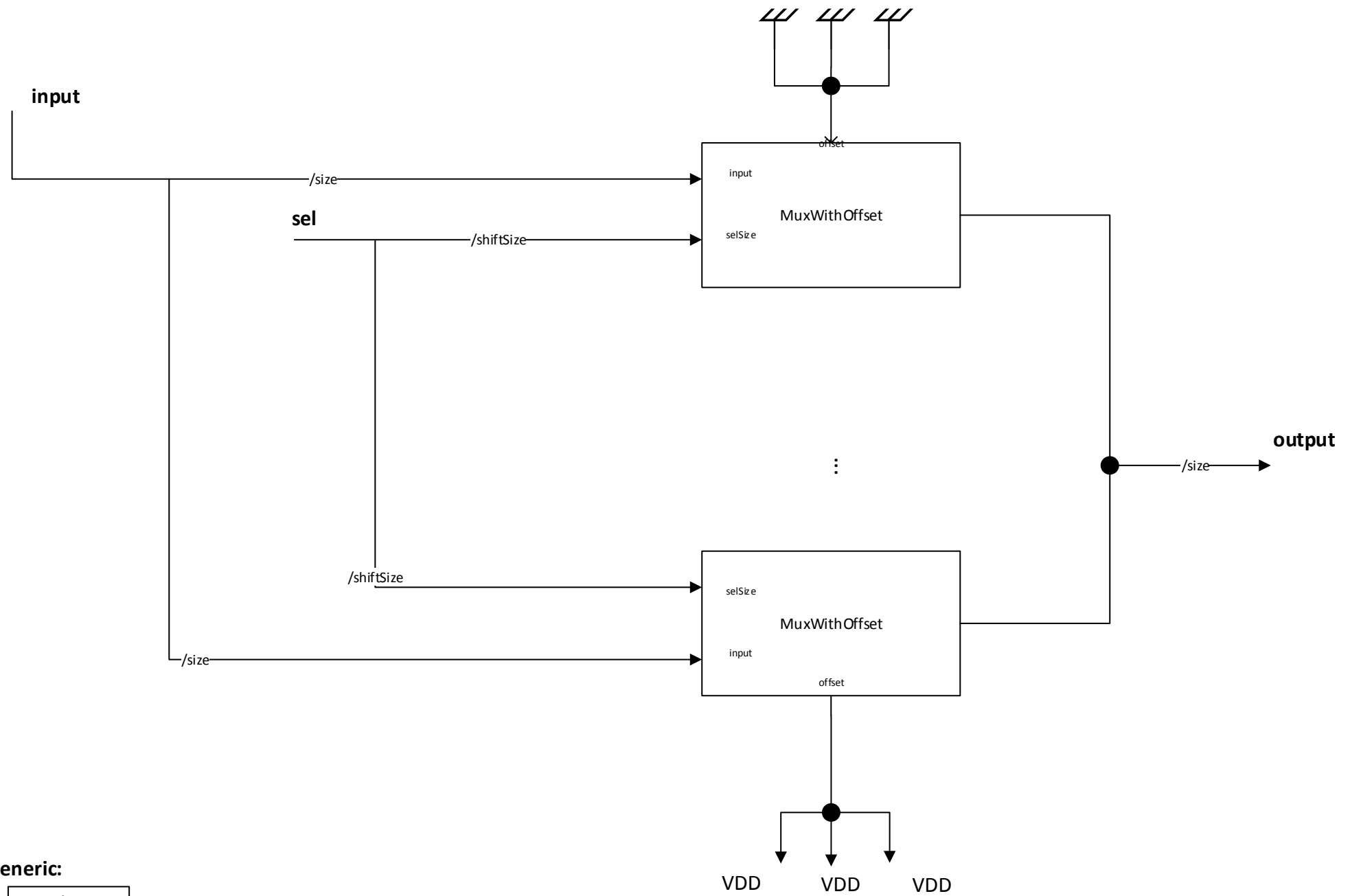


Generic:

size

Subtract 1 from input (except from 0)

arithmeticshifter



Generic:

size

shiftSize

Variable arithmetic shifter

muxwithoffset

input

/size

sel

offset

/selSize

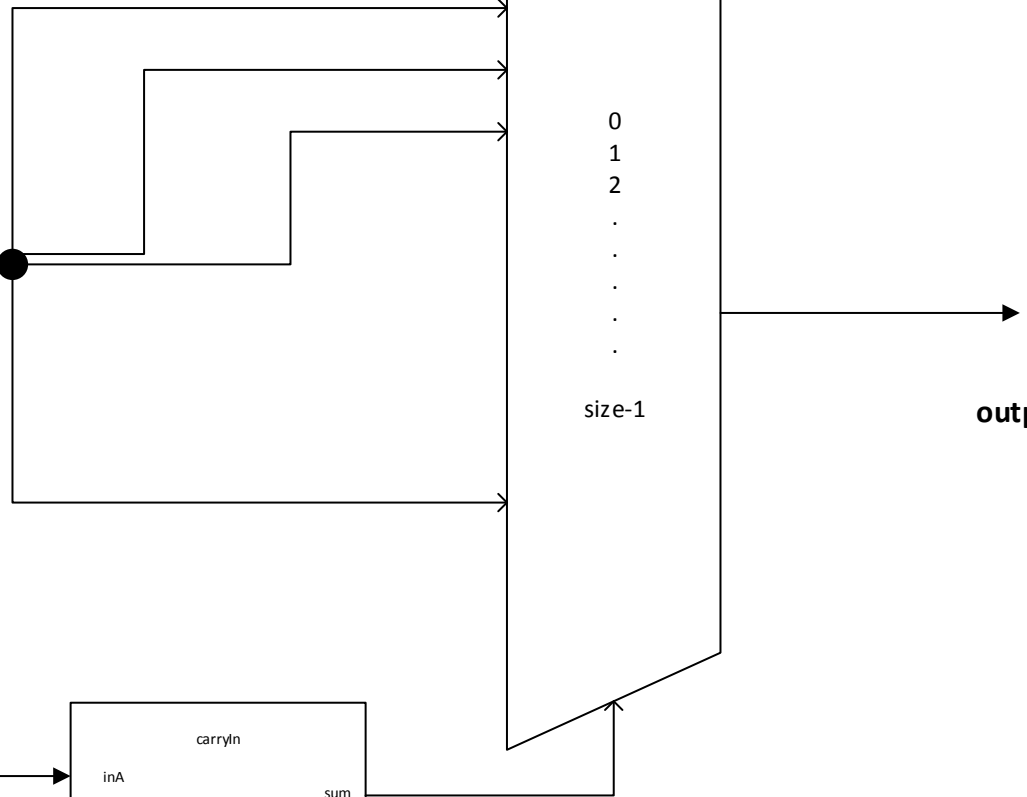
/selSize

Generic:

size

selSize

offset

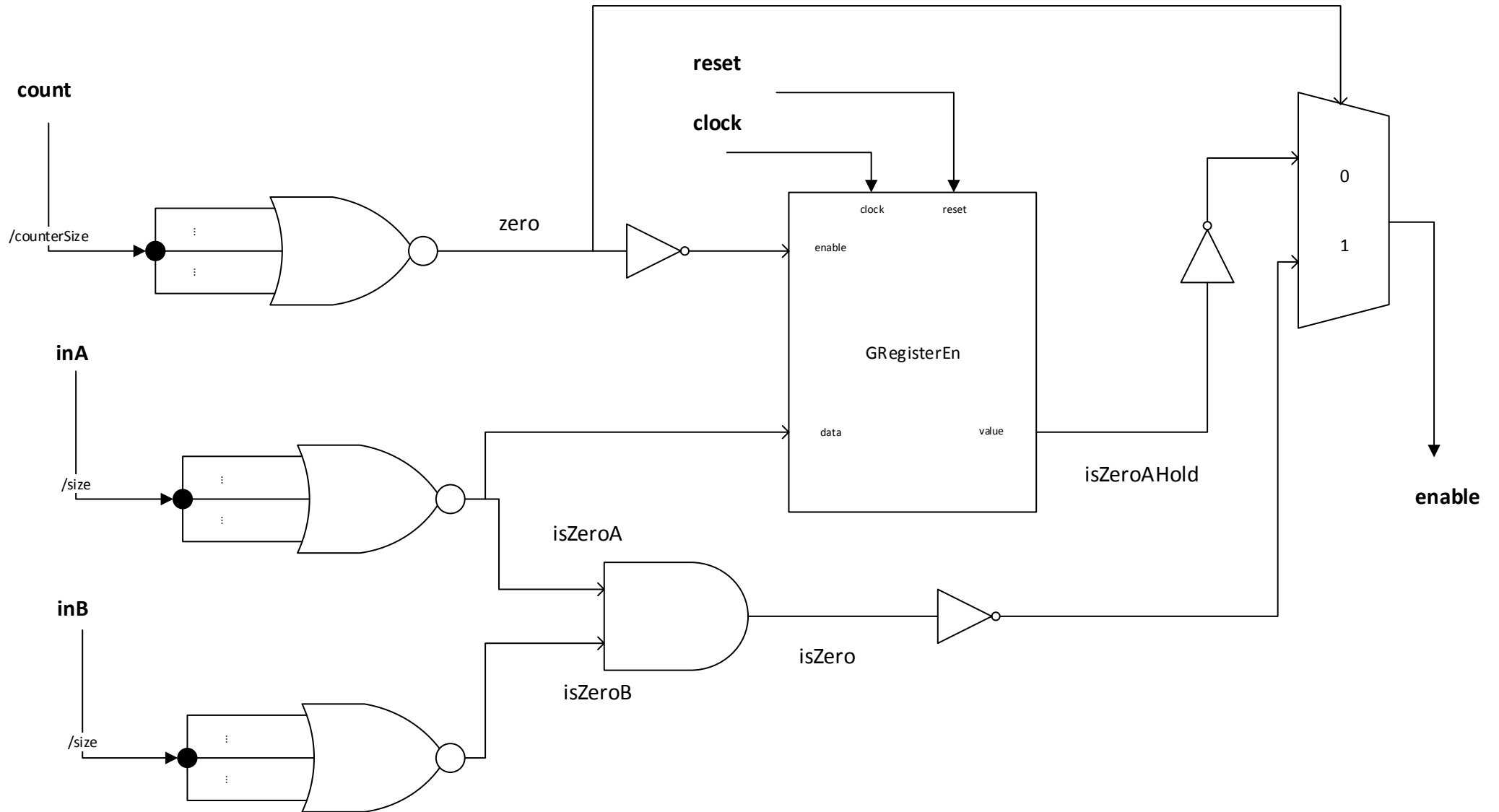


output

Adder

A mux with an offset

specialcase



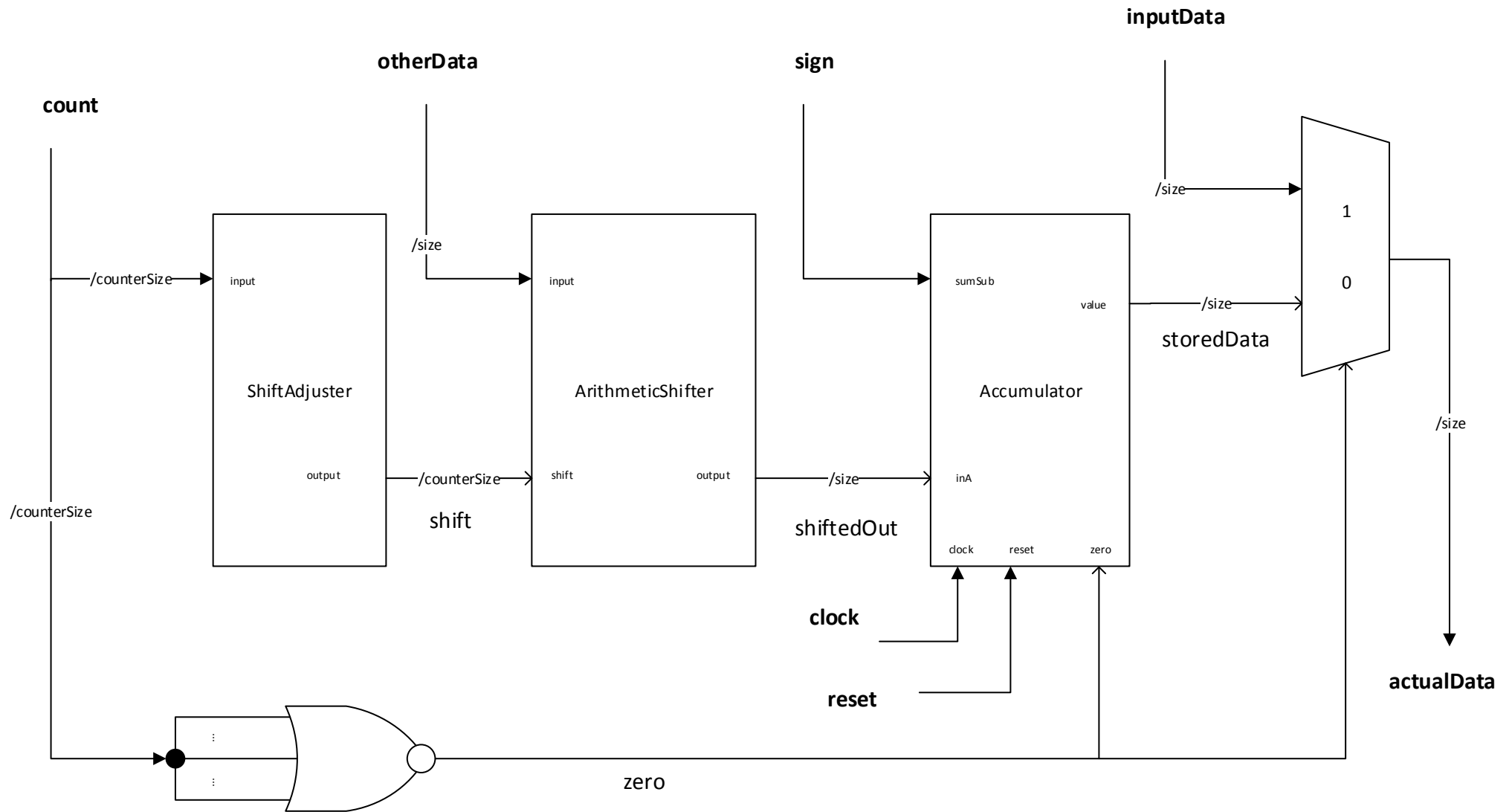
Generic:

size

counterSize

Special case checker

rotator

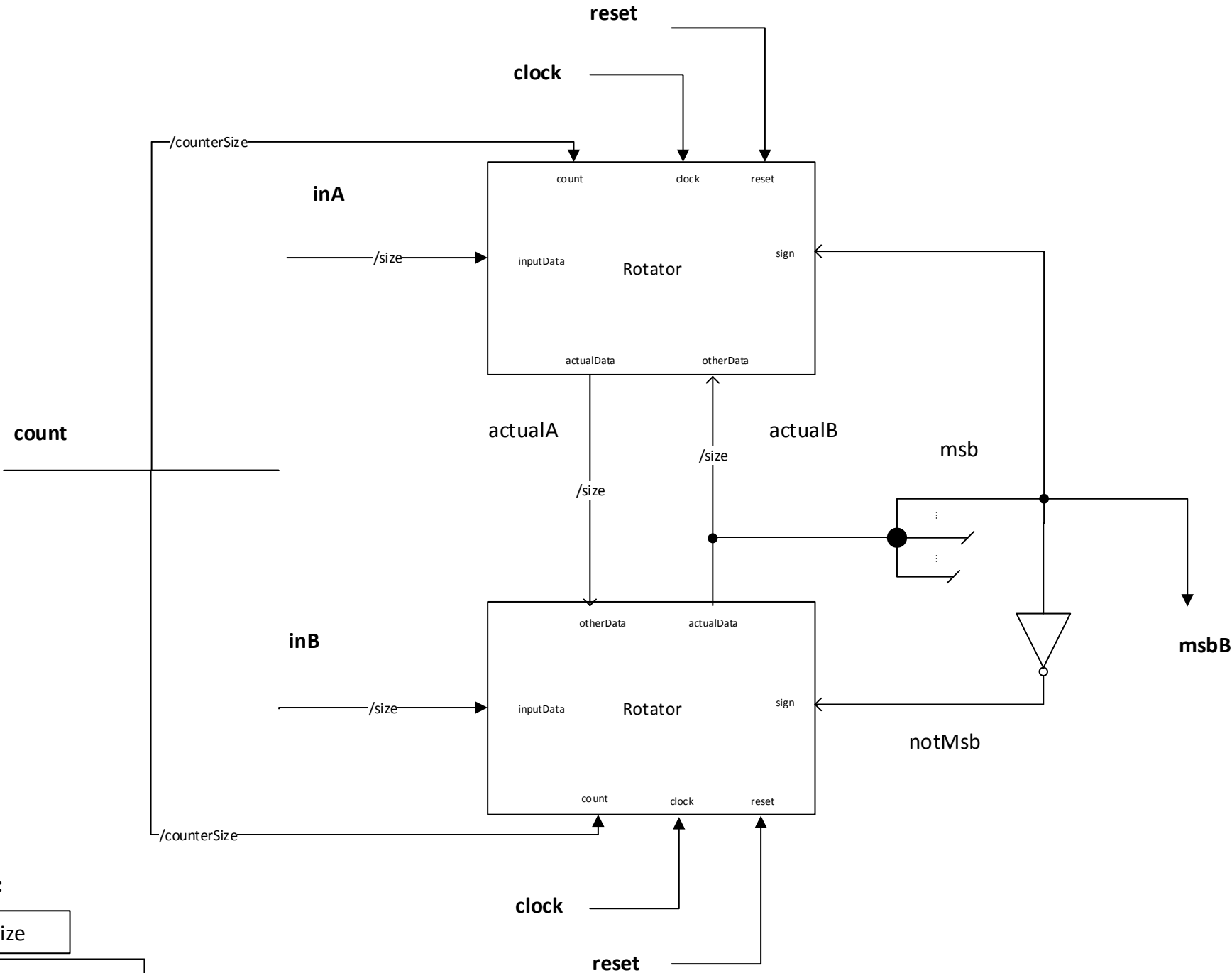


Generic:

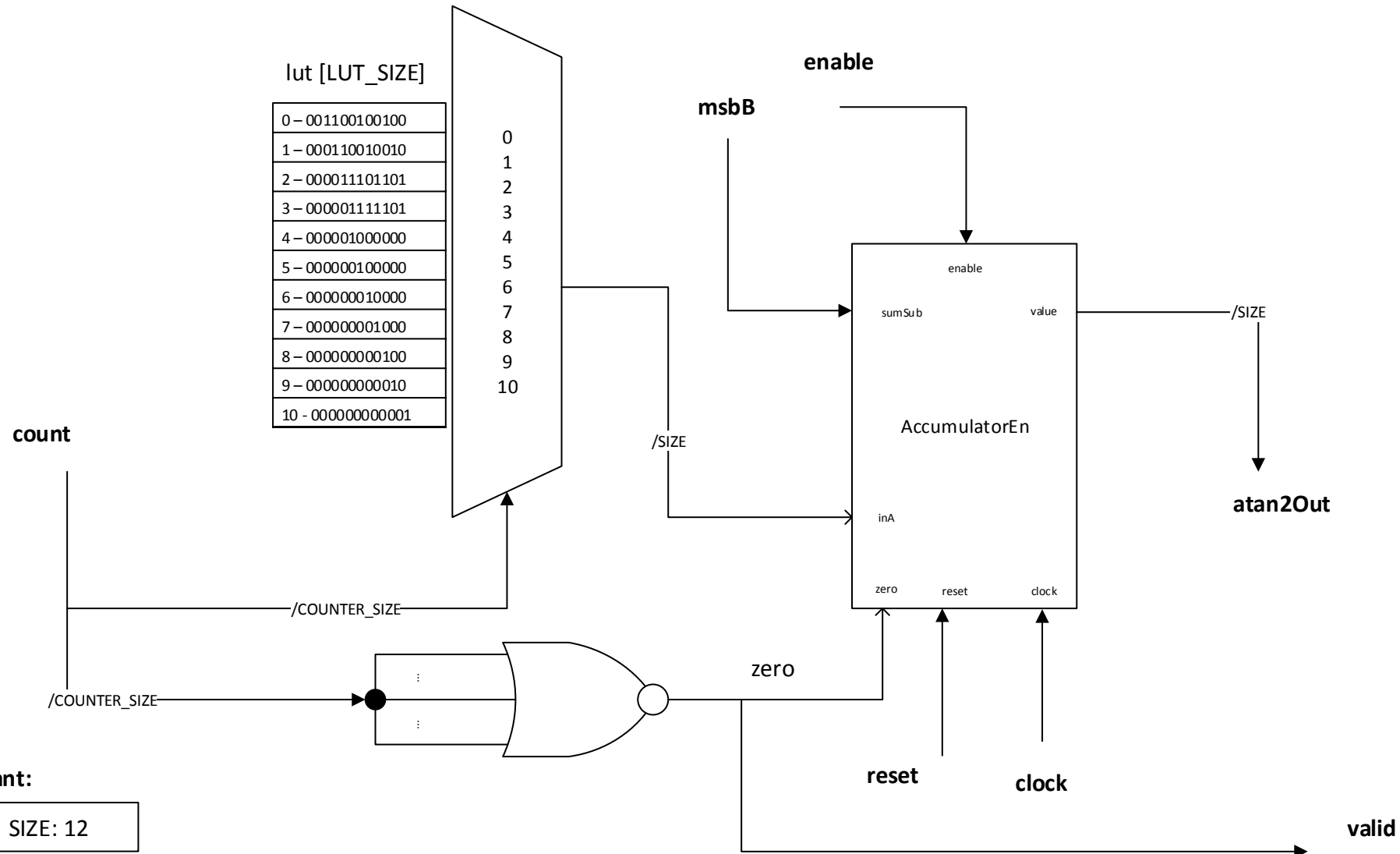
size

counterSize

cordic



atan2



Constant:

- SIZE: 12
- LUT_SIZE: 11
- COUNTER_SIZE: 4

cordicatan2

