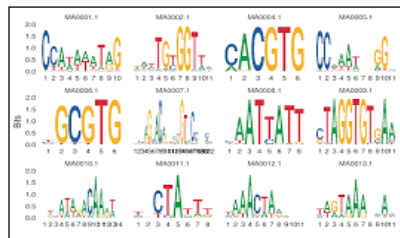


Input sequences



Selecting fragments from
input sequences based on
kernels

Selecting segments
belonging to kernels with
the top five most numerous
active segments

Transforming segments
into PWMs

Training the vConv-
based network

Selecting activated
kernels from the
vConv-based network

Output:
vConv-based network
and PWM