

WEB: <a href="http://www.winstar.com.tw">http://www.winstar.com.tw</a>

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### **SPECIFICATION**

70QTIFGDBNO	<b>#</b>
SION: DATA	:
	SION: DATA

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
			葉虹蘭

ISSUED DATE: 2014/09/17

TFT Display Inspection Specification: http://www.winstar.com.tw/service.php



RECORDS OF REVISION				DOC. FIRST ISSUE			
VERSION	DATE	REVISED PAGE NO.	SUI	MMARY			
0	2013/02/20		Fi	rst issue			
A	2013/03/06		Uŗ	odate General			
			Specification				
			Electrical Characteristics				
			Interface				
			Block Diagram				
			То	uch panel Information			
			$\mathbf{C}$	ontour Drawing			
			Ini	tial Code For Reference			
В	2013/12/17		$\mathbf{C}$	orrect VDD.			
C	2014/09/17		Ac	ld size & Surface.			
			M	odify Pixel Data Format			
			&	Block Diagram& Static			
			ele	ectricity test.			

WF70QTIFGDBN0#

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- 10.Block Diagram
- 11.Reliability
- 12.Contour Drawing
- 13.Initial Code For Reference

# **1.Module Classification Information**

W	F	70	Q	Т	I	F	G	D	В	N	0	#
1	2	3	4	(5)	6	7	8	9	10	(11)	12	13

	D 1.37/D	CTAD	DICD	I AN CODI	OOD ATTION						
①	Brand: WINSTAR DISPLAY CORPORATION										
2	Display Type : F→TFT Type, J→Custom TFT										
3	Display Size: 7.0" TFT										
4	Model serials no.										
(5)	Backlight Typ	pe:	F→C	CFL, Whit	e	T→LED,	T→LED, White				
			$S \rightarrow L$	ED, High I	Light White						
6	LCD Polarize I→Transmissive, W. T, 6:00										
	Type/ Temper	rature	L→Т	ransmissiv	e, W.T,12:00						
	range/ Gray S	Scale	Ζ→Т	ransmissiv	e, W.T, Wide V	iewing Ang	gle for O-FILM	[			
	Inversion Direction Y→Transmissive, W.T, Wide View										
7	A: TFT LCD G: TFT+FR										
	B: TFT+FR-	+CON	ΓROL	BOARD	H: TFT	H:TFT+D/V BOARD					
	C: TFT+FR-	+A/D I	BOAR	RD		I: TFT	-FR+D/V BC	OARD			
	D: TFT+FR	+A/D I	BOAF	RD+CONTI	ROL BOARD	J: TFT-	POWER BD				
	E: TFT+FR-	+POW	ER :	BOARD							
	F: TFT+CO	NTRO	L B	OARD							
8	Solution:										
	A: 128160	B:3202	34	C:320240	D:480234	E:480272	F: 640480	G: 800480			
	H:1024600	I:32048	30	J:240320	K:800600	L:240400	M:1024768	P:1280800			
9	D: Digital	$\Gamma : \Gamma I$	/DS				·				
10	Interface:	N: w	ithout	control box	ard A:8Bit	B: 16B	it				
11)	TS: N	With	out TS	T: res	istive touch par	nel C:	capacitive tou	ch panel			
12	Version										
13	Special Code		#:Fit	in with RO	HS directive re	gulations					
	1		l								

# 2.Summary

This technical specification applies to 7.0' color TFT-LCD panel. The 7.0' color TFT-LCD panel is
designed for camcorder, digital camera application and other electronic products which require high
quality flat panel displays. This module follows RoHS.

# **3.General Specifications**

Item	Dimension	Unit			
Size	7.0	inch			
Dot Matrix	800 x RGB x 480(TFT)	dots			
Module dimension	165.0(W) x 100.0(H) x 11.5(D)	mm			
Active area	154.08 x 85.92	mm			
Dot pitch	0.0642 x 0.179	mm			
LCD type	TFT, Normally White, Transmissive				
View Direction	12 o'clock				
Gray Scale Inversion Direction	6 o'clock				
Backlight Type	LED,Normally White				
Controller IC	SSD1963				
Interface	Digital 8080 family MPU 8bit/16bit				
With /Without TP	Without TP				
Surface	Anti-Glare				

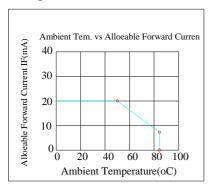
<sup>\*</sup>Color tone slight changed by temperature and driving voltage.

## **4.Absolute Maximum Ratings**

Item	Symbol	Min	Тур	Max	Unit
Operating Temperature	TOP	-20	_	+70	$^{\circ}\! \mathbb{C}$
Storage Temperature	TST	-30	_	+80	$^{\circ}\mathbb{C}$

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp.  $\leq 60^{\circ}$ C, 90% RH MAX. Temp.  $> 60^{\circ}$ C, Absolute humidity shall be less than 90% RH at  $60^{\circ}$ C



## **5.Electrical Characteristics**

### 5.1. Operating conditions: (CON2.Pin1=GND, Pin2=VDD)

Item	Symbol	Condition	Min	Тур	Max	Unit	Remark
Supply Voltage For LCM	VDD	_	3.0	3.1	3.3	V	_
Supply Current For LCM	IDD	_	_	300	450	mA	Note1

Note 1: This value is test for VDD=3.3V, Ta=25°C only

### 5.2. Backlight driving conditions (CON2.Pin33,34=VLED-, Pin35,36=VLED+)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Operation Current For LED Driver	VLED=5V	400	_	600	mA	Note 1,2
Power Consumption	VLED=5V	2000	_	3000	mW	Note 1,2
Supply Voltage For LED Driver	VLED+	_	5	_	V	_
LED Life Time	_	_	50,000	_	Hr	Note 2,3,4

Note 1 : Base on VLED= 5V for the back light driver IC specification

Note 2 : Ta = 25 °C

Note 3: Brightness to be decreased to 50% of the initial value

Note 4: The single LED lamp case

# **6.DC CHARATERISTICS**

Parameter	Symbol	Rating		Unit	Condition	
1 ar ameter	Symbol	Min	Тур	Max	Omt	Condition
Low level input voltage	VIL	0	-	0.3VDD	V	
High level input voltage	V <sub>IH</sub>	0.7VDD	-	VDD	V	

## 7.Interface timing

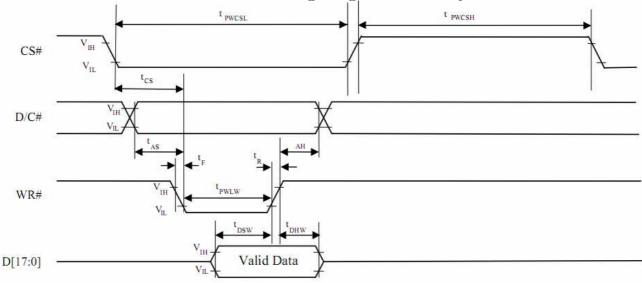
### 7.1. 8080 Mode

The 8080 mode MCU interface consist of CS#, D/C#, RD#, WR#, Data Bus and TE signals (Please refer to Table 6-1 for pin multiplexed with 6800 mode). This interface use WR# to define a write cycle and RD# for read cycle. If the WR# goes low when the CS# signal is low, the data or command will be latched into the system at the rising edge of WR#. Similarly, the read cycle will start when RD# goes low and end at the rising edge of RD#.

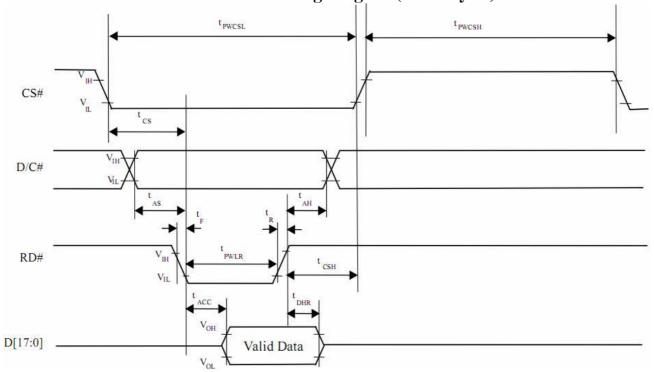
7.2. 8080 Mode Write Cycle

Symbol	Parameter	Min	Тур	Max	Unit
fMCLK	System Clock Frequency	1	-	110	MHz
tMCLK	System Clock Period	1/ fMCLK	ı	-	ns
tPWCSH	Control Pulse High Width Write Read	13 30	1.5* tMCLK 3.5* tMCLK	ı	ns
tPWCSL	Control Pulse Low Width Write (next write cycle) Write (next read cycle) Read	13 80 80	1.5* tMCLK 9* tMCLK 9* tMCLK	1	ns
tAS	Address Setup Time	1	ı	-	ns
tAH	Address Hold Time	2	ı	-	ns
tDSW	Write Data Setup Time	4			ns
tDHW	Write Data Hold Time	1	-	-	ns
tPWLW	Write Low Time	12			ns
tDHR	Read Data Hold Time	1	-	-	ns
tACC	Access Time	32			ns
tPWLR	Read Low Time	36	-	-	ns
tR	Rise Time	-		0.5	ns
tF	Fall Time	-	ı	0.5	ns
tCS	Chip select setup time	2		-	ns
tCSH	Chip select hold time to read signal	3	-	-	ns

### 7.3. Parallel 8080-series Interface Timing Diagram(Write Cycle)



### 7.4. Parallel 8080-series Interface Timing Diagram(Read Cycle)



### 7.5. Pixel Data Format

Interface	Cycle	D[15]	D[14	D[13]	D[12]	D[11]	D[10]	D[9]	D[8]	D[7]	D[6]	D[5]	D[4]	D[3]	D[2]	D[1]	D[0]
16 bits (565 format)	1 <sup>st</sup>	R5	R4	R3	R2	R1	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1
	1 <sup>st</sup>	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0
16 bits	2 <sup>nd</sup>	B7	B6	B5	В4	В3	B2	B1	В0	R7	R6	R5	R4	R3	R2	R1	R0
	3 <sup>rd</sup>	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	В3	B2	B1	В0
	1 <sup>st</sup>									R7	R6	R5	R4	R3	R2	R1	R0
8 bits	2 <sup>nd</sup>									G7	G6	G5	G4	G3	G2	G1	G0
	3 <sup>rd</sup>									B7	B6	B5	B4	ВЗ	B2	B1	В0

## **8.Optical Characteristics**

Item		Symbol	Condition.	Min	Тур.	Max.	Unit	Remark
Response time		Tr	$\theta = 0^{\circ}$ , $\Phi = 0^{\circ}$	-	10	20	.ms	Note 3
Kesponse in	iic	Tf	$0 - 0$ , $\Psi = 0$	-	15	30	.ms	Note 3
Contrast rat	io	CR	At optimized viewing angle	400	500	-	-	Note 4
Color	White	Wx	$\theta = 0^{\circ}$ , $\Phi = 0$	0.26	0.31	0.36		Note 2,5,6
Chromaticity	ity   white	Wy	$0 = 0$ , $\Phi = 0$	0.28	0.33	0.38		11016 2,3,0
Viewing angle	Hor. —	ΘR	CR ≥ 10	60	70	-	Deg.	Note 1
(Gray Scale		ΘL		60	70	-		
Inversion	Ver.	ΦТ		40	50	-		
Direction)	ver.	ΦВ		60	70	-		
Brightness		_		350	0 460	-	cd/m <sup>2</sup>	Center of
		_	-	550				display

 $Ta=25\pm2^{\circ}C$ , VLED / ILED= 5V / 400mA

Note 1: Definition of viewing angle range

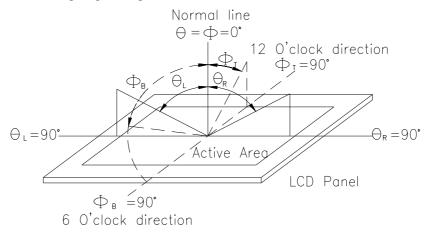


Fig. 8.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7orBM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

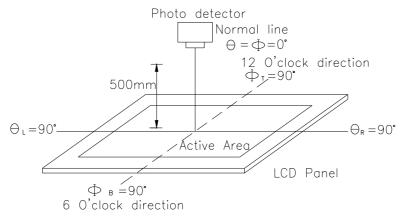
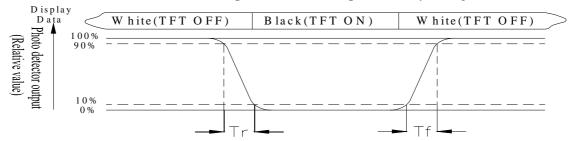


Fig. 8.2. Optical measurement system setup

#### Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time, Tr, is the time between photo detector output intensity changed from 90% to 10%. And fall time, Tf, is the time between photo detector output intensity changed from 10% to 90%



Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

Note 5: White  $Vi = Vi50 \pm 1.5V$ 

Black  $Vi = Vi50 \pm 2.0V$ 

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

### Note 6: Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

<sup>&</sup>quot;±" means that the analog input signal swings in phase with VCOM signal.

<sup>&</sup>quot;±" means that the analog input signal swings out of phase with VCOM signal.

## 9.Interface

### 9.1. LCM PIN Definition (CON2)

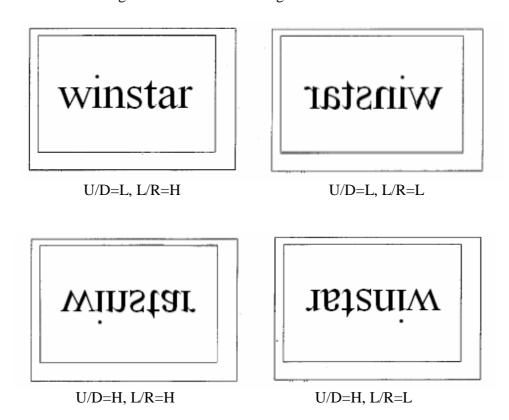
Pin	Symbol	Function	Remark
1	GND	System ground pin of the IC.	
		Connect to system ground.	
2	VDD	Power Supply: +3.3V	
3	BLE	Backlight control signal, H: On \ L: Off	
4	D/C	Data/Command select	
5	WR	Write strobe signal	
6	RD	Read strobe signal	
7	DB0	Data bus	
8	DB1	Data bus	
9	DB2	Data bus	
10	DB3	Data bus	
11	DB4	Data bus	
12	DB5	Data bus	
13	DB6	Data bus	
14	DB7	Data bus	
15	DB8	Data bus (When select 8bits Mode, this pin is NC)	Note1
16	DB9	Data bus (When select 8bits Mode, this pin is NC)	Note1
17	DB10	Data bus (When select 8bits Mode, this pin is NC)	Note1
18	DB11	Data bus (When select 8bits Mode, this pin is NC)	Note1
19	DB12	Data bus (When select 8bits Mode, this pin is NC)	Note1
20	DB13	Data bus (When select 8bits Mode, this pin is NC)	Note1
21	DB14	Data bus (When select 8bits Mode, this pin is NC)	Note1
22	DB15	Data bus (When select 8bits Mode, this pin is NC)	Note1
23	NC	No connect	
24	NC	No connect	
25	CS	Chip select	
26	RST	Hardware reset	
27	L/R	Left / right selection; Default L/R=H	Note 2,3
28	U/D	Up/down selection; ; Default U/D=L	Note 2,3
29	NC	No connect	
30	NC	No connect	
31	NC	No connect	
32	NC	No connect	
33	VLED-	Power for LED Driver IC(GND)	
34	VLED-	Power for LED Driver IC(GND)	
35	VLED+	Power for LED Driver IC(+5V)	
36	VLED+	Power for LED Driver IC(+5V)	

Note1: When select 8bit mode, DB0~DB7 be used, DB8~DB15 no connect When select 16bit mode, DB0~DB15 be used

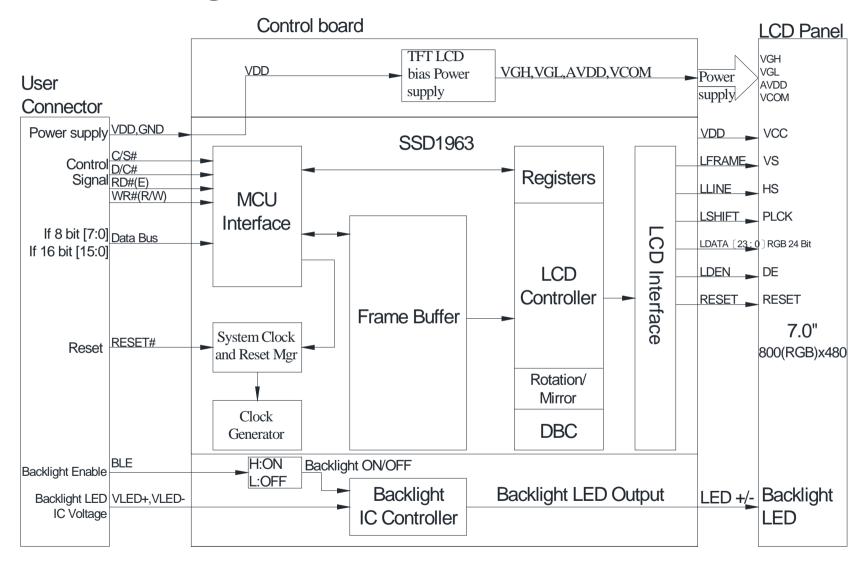
Note 2: Selection of scanning mode

Setting of scan	control input	Scanning direction		
U/D	L/R	Scanning direction		
GND	VDD	Up to down, left to right		
VDD	GND	Down to up, right to left		
GND	GND	Up to down, right to left		
VDD	VDD	Down to up, left to right		

Note 3: Definition of scanning direction. Refer to the figure as below:



## **10.Block Diagram**



## 11.Reliability

Content of Reliability Test (Wide temperature, -20°C ~70°C)

<b>Environmental Test</b>			
Test Item	Content of Test	<b>Test Condition</b>	Note
High Temperature	Endurance test applying the high storage	80°C	2
storage	temperature for a long time.	200hrs	
Low Temperature	Endurance test applying the low storage	-30°C	1,2
storage	temperature for a long time.	200hrs	
High Temperature	Endurance test applying the electric stress	70°C	
Operation	(Voltage & Current) and the thermal stress to the element for a long time.	200hrs	
Low Temperature	Endurance test applying the electric stress under	-20°C	1
Operation	low temperature for a long time.	200hrs	
High Temperature/	The module should be allowed to stand at 60	60°C,90%RH	1,2
Humidity Operation	°C,90%RH max	96hrs	
	For 96hrs under no-load condition excluding the		
	polarizer, Then taking it out and drying it at		
TD1 1 1 1	normal temperature.		
Thermal shock	The sample should be allowed stand the	-20°C/70°C	
resistance	following 10 cycles of	10 cycles	
	operation $-20^{\circ}\text{C}$ $25^{\circ}\text{C}$ $70^{\circ}\text{C}$		
	30min 5min 30min 1 cycle		
Vibration test	Endurance test applying the vibration during	Total fixed	3
	transportation and using.	amplitude : 15mm	
		Vibration	
		Frequency:	
		10~55Hz	
		One cycle 60 seconds to 3	
		directions of X,Y,Z	
		for Each 15	
		minutes	
Static electricity test	Endurance test applying the electric stress to the	VS=800V,	
	terminal.	$RS=1.5k\Omega$	
		CS=100pF	
		1 time	

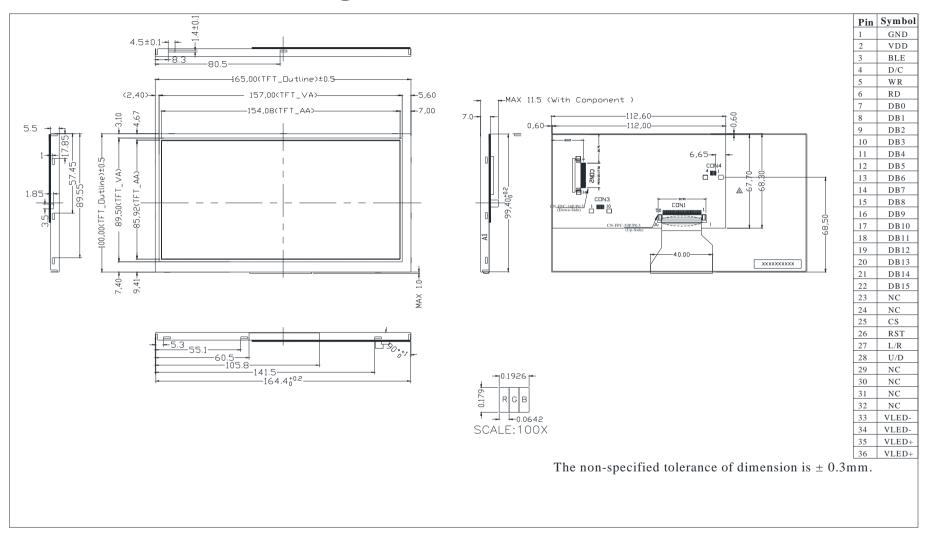
Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal

Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

# **12.Contour Drawing**



## **13.Initial Code For Reference**

```
void Initial SSD1963()
         Write_Command(0x01);
         Delay ms(10);
         Write Command(0xe0);
                                  //START PLL
         Write_Parameter(0x01);
         Delay_ms(50);
         Write_Command(0xe0);
                                  //START PLL
         Write_Parameter(0x03);
         Delay_ms(5);
         Write_Command(0xb0);
         Write Parameter(0x20);
         Write Parameter(0x80);
         Write_Parameter(0x03);
         Write_Parameter(0x1f);
         Write_Parameter(0x01);
         Write_Parameter(0xdf);
         Write_Parameter(0x00);
         Write_Command(0xf0);
         Write Parameter(0x03); //pixel data format, 0x03 is 16bit(565 format);0x00 is for 8-bit
         //Set the MN of PLL
         Write_Command(0xe2);
         Write Parameter(0x1d);
         Write Parameter(0x02);
         Write Parameter(0x54);
         Write_Command(0xe6);
         Write_Parameter(0x04);
         Write_Parameter(0x6f);
         Write_Parameter(0x47);
         //Set front porch and back porch
         Write Command(0xb4);
         Write Parameter(0x04);
         Write_Parameter(0x20);
         Write Parameter(0x00);
         Write_Parameter(0x2e);
         Write_Parameter(0xd2);
         Write_Parameter(0x00);
         Write_Parameter(0x00);
         Write_Parameter(0x00);
         Write_Command(0xb6);
```

Write\_Parameter(0x02);

```
Write_Parameter(0x0d);
 Write_Parameter(0x00);
 Write_Parameter(0x17);
 Write_Parameter(0x16);
 Write_Parameter(0x00);
 Write_Parameter(0x00);
Write_Command(0x2a);
Write_Parameter(0x00);
Write_Parameter(0x00);
Write_Parameter(0x03);
Write_Parameter(0x1f);
Write_Command(0x2b);
Write_Parameter(0x00);
Write_Parameter(0x00);
Write_Parameter(0x01);
Write_Parameter(0x1f);
 Write_Command(0xb8);
 Write_Parameter(0x0f);
 Write_Parameter(0x01);
 Write_Command(0xba);
 Write_Parameter(0x01);
 Write_Command(0x29);
 Write_Command(0x2c);
```

}

			e Feedback Sheet	
	Module Number :			Page: 1
_	Panel Specification:	_		
	Panel Type:	□ Pass	□ NG ,	
		□ Pass	□ NG ,	
3.		□ Pass	□ NG ,	
4.	View Area:		□ NG ,	
5.			□ NG ,	
6.			□ NG ,	
	Storage Temperature:		□ NG ,	
	Others:			
	<u>lechanical Specification</u> :			
1.	PCB Size:	□ Pass	□ NG ,	
2.		□ Pass	□ NG ,	
3.		□ Pass	□ NG ,	
4.	Connector Position:	□ Pass	□ NG ,	
5.			□ NG ,	
6.	Backlight Position:		□ NG ,	
7.	Thickness of PCB:	□ Pass	□ NG ,	
8.	Height of Frame to PCB:	□ Pass	□ NG ,	
9.	Height of Module:	□ Pass	□ NG ,	
10	). Others:	□ Pass	□ NG ,	
3 · <u>I</u>	Relative Hole Size:			
1.	Pitch of Connector:	□ Pass	□ NG ,	
2.	Hole size of Connector:	□ Pass	□ NG ,	
3.	Mounting Hole size:	□ Pass	□ NG ,	
4.	Mounting Hole Type:	□ Pass	□ NG ,	
5.	Others:	□ Pass	□ NG ,	
4 \ <u>E</u>	Backlight Specification:			
1.	B/L Type:	□ Pass	□ NG ,	
2.	B/L Color:	□ Pass	□ NG ,	
3.	B/L Driving Voltage (Refere	ence for LED T	ype) : □ Pass □ NG ,_	
4.	B/L Driving Current:	□ Pass	□ NG ,	
5.	Brightness of B/L:	□ Pass	□ NG ,	
6.	B/L Solder Method:	□ Pass	□ NG ,	
7.	Others:	□ Pass	□ NG ,	
		>> <b>Go to p</b> a	age 2 <<	

	Winstar Module Number	:		Page: 2
5、	<b>Electronic Characteristics</b>	of Module:		_
1.	Input Voltage:	□ Pass	□ NG ,	
2.	Supply Current:	□ Pass	□ NG ,	
3.	Driving Voltage for LCD:	□ Pass		
4.	Contrast for LCD:	□ Pass		
5.	B/L Driving Method:	□ Pass	□ NG ,	
6.	Negative Voltage Output:	□ Pass		
7.	Interface Function:	□ Pass	□ NG ,	
8.	LCD Uniformity:	□ Pass		
9.	ESD test:	□ Pass		
10.	Others:	□ Pass	□ NG ,	
6、	Summary :			
	signature:			
Custo	omer Signature:		Date: /	1